

P-Channel Enhancement-Mode MOSFET

DESIGNED FOR:

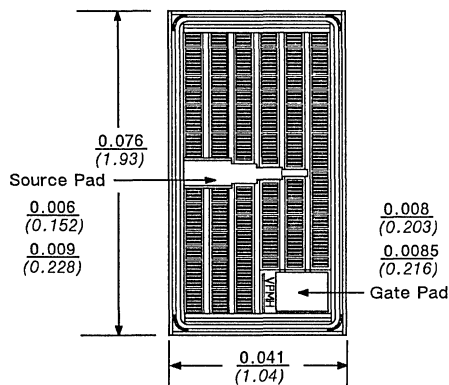
- Switching
- Amplification

FEATURES

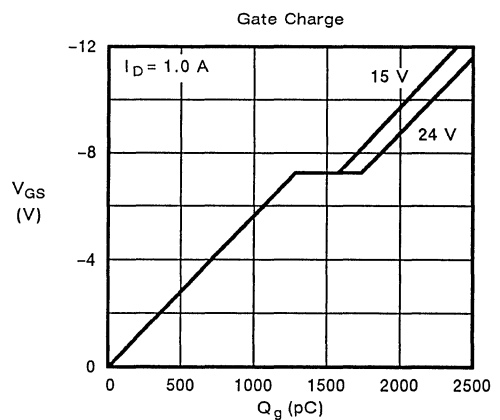
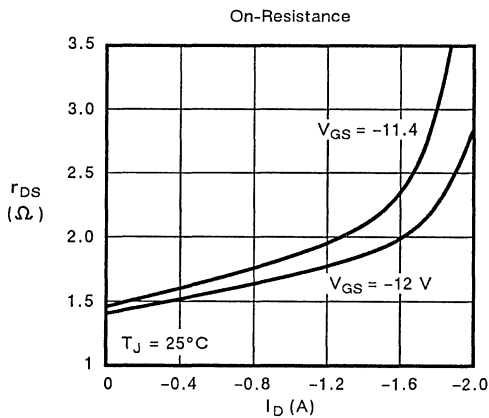
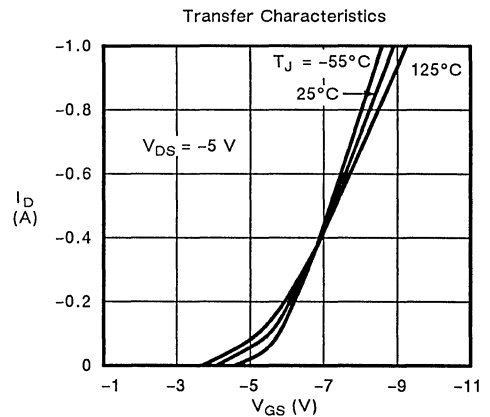
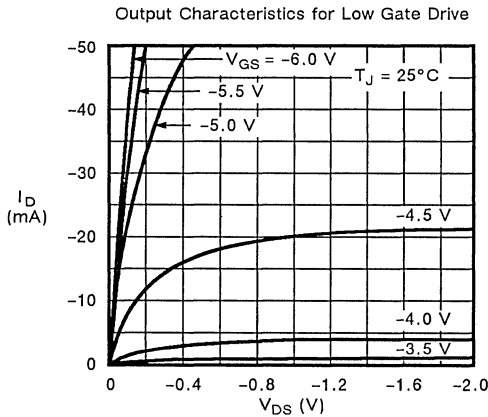
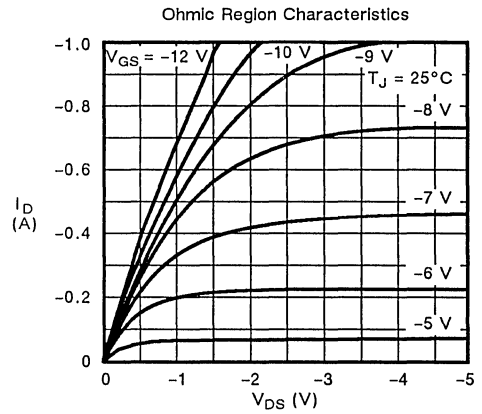
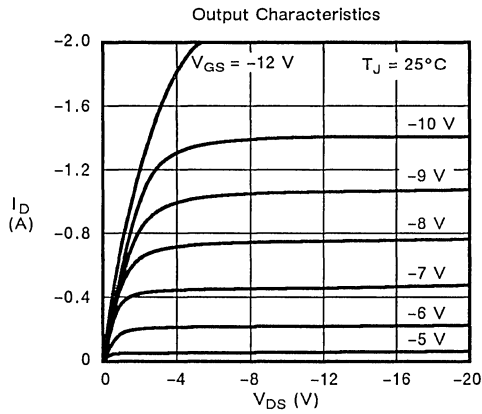
- High Speed
- Low $r_{DS(on)} < 2.5 \Omega$

| TYPE | PACKAGE | DEVICE |
|--------|---------------------|-------------------------------------|
| Single | TO-205AD | • VP0300B |
| | TO-92 | • VP0300L |
| | TO-237 | • VP0300M |
| Quad | 14-Pin Plastic | • VQ2001J |
| | 14-Pin Dual-In-Line | • VQ2001P |
| | Chip | • Available as above specifications |

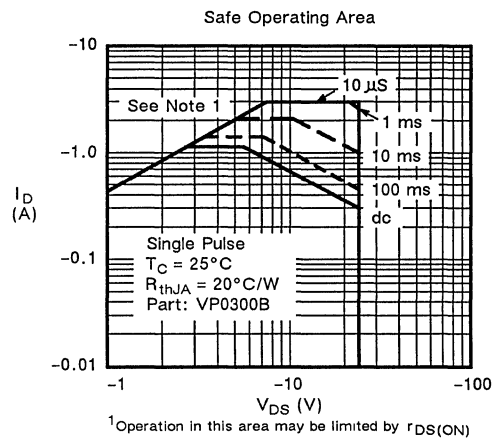
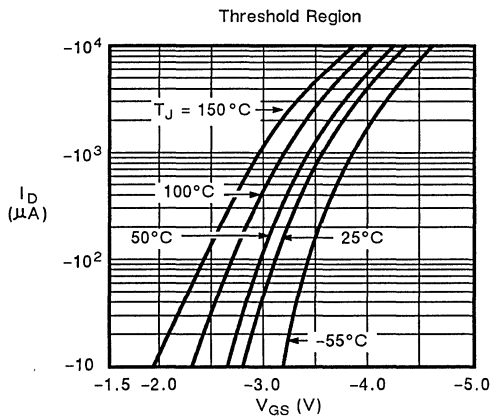
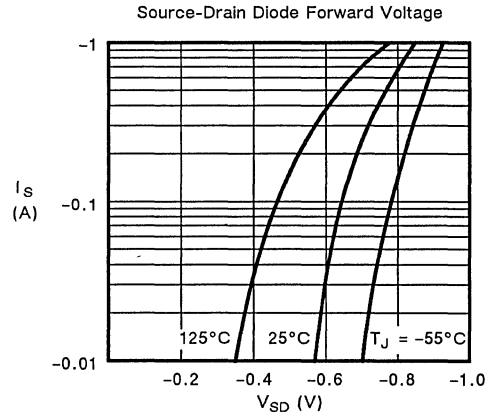
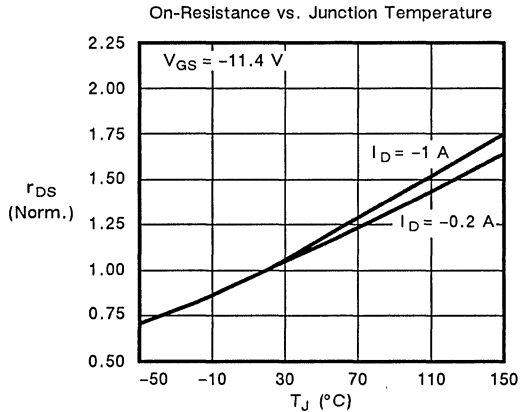
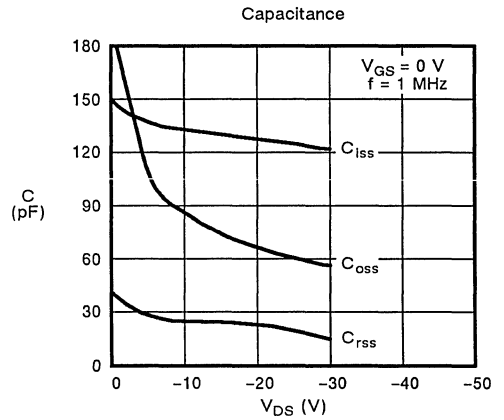
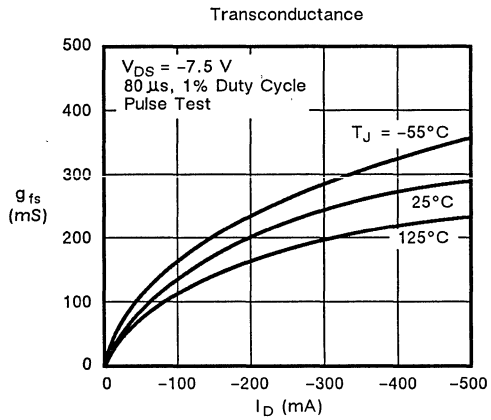
GEOMETRY DIAGRAM



TYPICAL CHARACTERISTICS

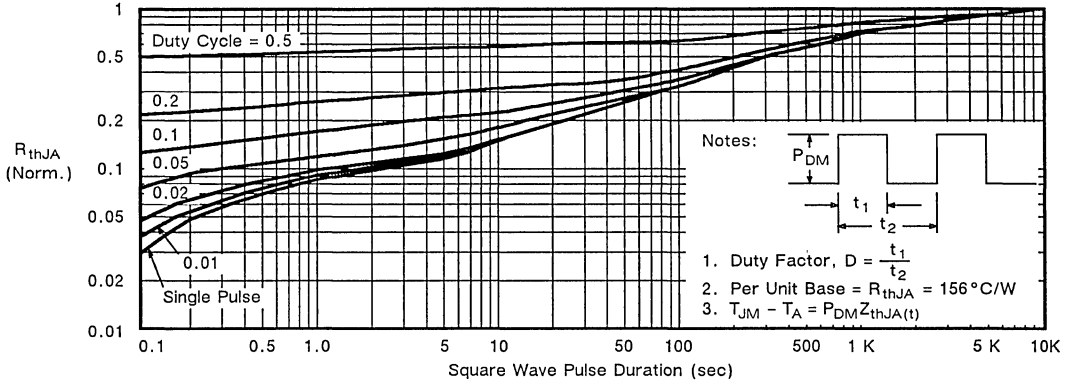


TYPICAL CHARACTERISTICS

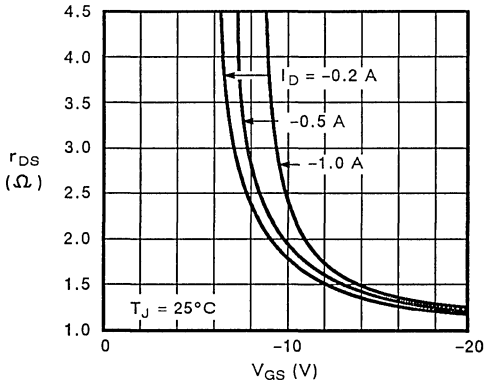


TYPICAL CHARACTERISTICS

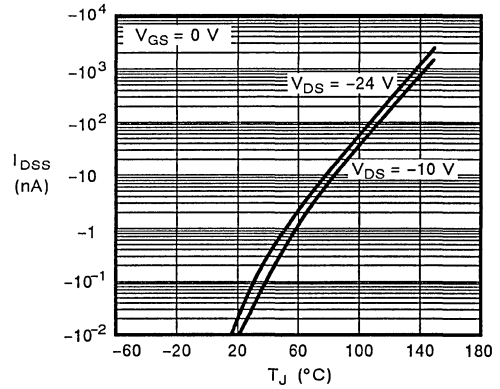
Normalized Effective Transient Thermal Impedance, Junction-to-Ambient (TO-92)



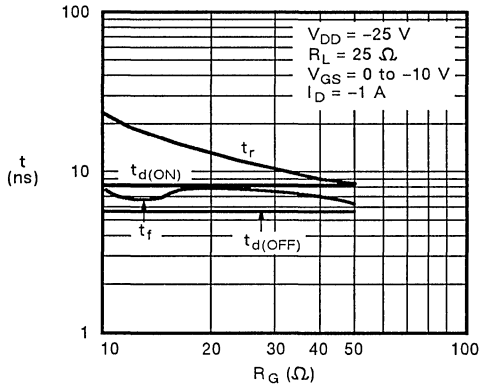
On-Resistance vs. Gate to Source Voltage



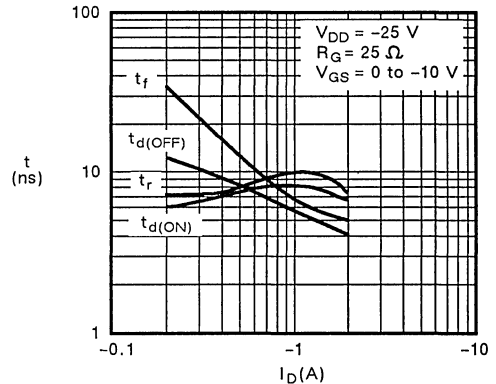
Off-State Current



Drive Resistance Effects on Switching



Load Condition Effects on Switching



TYPICAL CHARACTERISTICS

