

Standard Products

VRG8609/10

Dual Adjustable Negative Voltage Regulators Radiation Tolerant


www.aeroflex.com/voltreg

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AEROFLEX
A passion for performance.

FEATURES


- Manufactured using  Linear Technology Corporation® Space Qualified RH137 die
- Radiation performance
 - Total dose: 100 krad(Si),
Dose rate = 50 - 300 rads(Si)/s
 - ELDRS: 50 kreds(Si),
Dose rate ≤ 10 mrads(Si)/S
- Thermal shutdown
- Output voltage adjustable: -1.25V to -27V
- 3-Terminal
- Output current: 1.5A
- Voltage reference: -1.25V
- Load regulation: 1.0% max
- Line regulation: 0.05% max
- Ripple rejection: >66dB
- Packaging – Hermetic metal
 - Thru-hole or Surface mount
 - 6 Leads, .655"L x .415"W x .200"Ht
 - Power package
 - Weight - 5 gm max
- Designed for aerospace and high reliability space applications
- Aeroflex Plainview's Radiation Hardness Assurance Plan is DLA Certified to MIL-PRF-38534, Appendix G.

DESCRIPTION

The Aeroflex Plainview VRG8609/10 consists of two negative (RH137) voltage regulators each capable of supplying in excess of 1.5Amps over the output voltage range as defined under recommended operating conditions. Each regulator is exceptionally easy to set-up, requiring only 2 external resistors to set the output voltage. The module design has been optimized for excellent regulation and low thermal transients. There is full electrical isolation between the regulators and each regulator to the package.

Further, the VRG8609/10 features internal current limiting, thermal shutdown and safe-area compensation, making them virtually blowout-proof against overloads. The VRG8609/10 serves a wide variety of applications including local on-card regulation, programmable output voltage regulation or precision current regulation.

The VRG8609/10 has been specifically designed to meet exposure to radiation environments. The VRG8609 is configured for a Thru-Hole 6 lead metal power package and the VRG8610 is configured for a Surface Mount 6 lead metal power package. It is guaranteed operational from -55°C to +125°C. Available screened to MIL-STD-883, the VRG8609/10 is ideal for demanding military and space applications.

For detailed performance characteristic curves, applications information and typical applications see the latest  Linear Technology Corporation® data sheets for their RH/LT137, which is available on-line at www.linear.com.

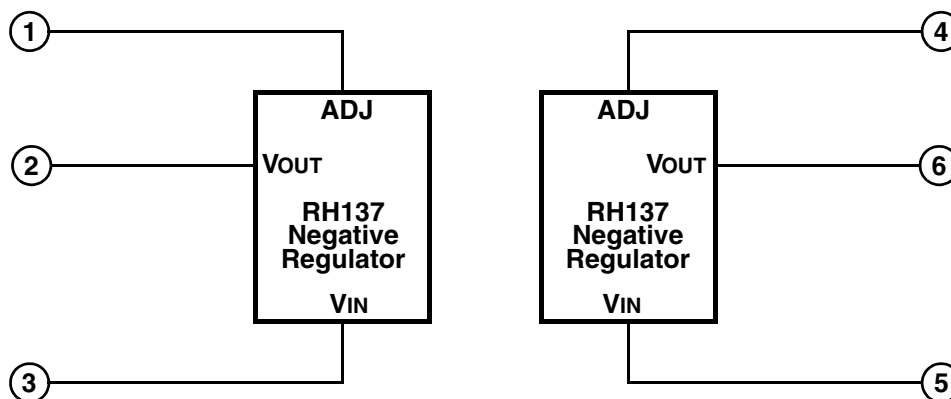


FIGURE 1 – BLOCK DIAGRAM / SCHEMATIC

ABSOLUTE MAXIMUM RATINGS

PARAMETER	RANGE	UNITS
Operating (Junction) Temperature Range	-55 to +150	°C
Lead Temperature (soldering, 10 sec)	300	°C
Storage Temperature Range	-65 to +150	°C
Input-Output Voltage Differential	30 (Neg)	V
Thermal Resistance (junction to case Θ_{JC}) each, Pos. & Neg.	5	°C/W
ESD Rating	1,999 ^{1/}	KV

^{1/} Meets ESD testing per MIL-STD-883, method 3015, Class 1C.

NOTICE: Stresses above those listed under "Absolute Maximums Rating" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

RECOMMENDED OPERATING CONDITIONS

PARAMETER	RANGE	UNITS
Output Voltage Range	-1.3 to -27	VDC
Case Operating Temperature Range	-55 to +125	°C

ELECTRICAL PERFORMANCE CHARACTERISTICS ^{1/}

PARAMETER	SYM	CONDITIONS (P ≤ P _{MAX})	MIN	MAX	UNITS
Reference Voltage ^{4/}	V _{REF}	3V ≤ (V _{IN} - V _{OUT}) ≤ V _{DIFF} MAX, 10mA ≤ I _{OUT} ≤ I _{MAX}	-1.200	-1.300	V
Line Regulation ^{2/} , ^{4/}	$\frac{\Delta V_{OUT}}{\Delta V_{IN}}$	3V ≤ (V _{IN} - V _{OUT}) ≤ 30V,	-	0.05	%/V
Load Regulation ^{2/} , ^{4/}	$\frac{\Delta V_{OUT}}{\Delta I_{OUT}}$	10mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≤ 5V 10mA ≤ I _{OUT} ≤ I _{MAX} , V _{OUT} ≥ 5V	-	50 1.0	mV %
Thermal Regulation		I _{OUT} = 1.5A, (V _{IN} - V _{OUT}) = 13.3V, 20ms Pulse, 20W, T _C = +25°C	-	0.02	%/W
Ripple Rejection		V _{OUT} = -10V, f = 120Hz, C _{ADJ} = 10μF	66	-	dB
Adjustment Pin Current ^{4/}	I _{ADJ}	I _{OUT} = 10mA, 3V ≤ (V _{IN} - V _{OUT}) ≤ 30V	-	100	μA
Adjustment Pin Current Change ^{4/}	ΔI _{ADJ}	3V ≤ (V _{IN} - V _{OUT}) ≤ 30V, 10mA ≤ I _{OUT} ≤ I _{MAX}	-	5	μA
Minimum Load Current ^{3/} ^{4/}	I _{MIN}	(V _{IN} - V _{OUT}) = 30V (V _{IN} - V _{OUT}) ≤ 10V	-	5 3	mA
Current Limit ^{4/} , ^{5/}	I _{MAX}	(V _{IN} - V _{OUT}) ≤ 15V (V _{IN} - V _{OUT}) = 30V, T _C = +25°C	1.5 0.24	-	A
Long Term Stability ^{3/}	$\frac{\Delta V_{OUT}}{\Delta TIME}$	T _C = +125°C	-	1	%
Thermal Resistance, each Regulator (Junction to Case) ^{3/}	Θ _{JC}		-	5	°C/W

Notes:

^{1/} Unless otherwise specified, these specifications apply for, (V_{in} - V_{out}) = 5V, I_{out} = 0.5A and -55°C < T_c < +125°C.

^{2/} Regulation is measured at a constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation. Measurements taken at the output lead must be adjusted for lead resistance.

^{3/} Not tested. Shall be guaranteed to the specified limits.

^{4/} Specification derated to reflect High Dose Rate (1019 condition A) to 100krads(Si) and Low Dose Rate (1019 condition D) to 50krads(Si), @ +25°C.

^{5/} Pulsed @ < 10% duty cycle @ +25°C.

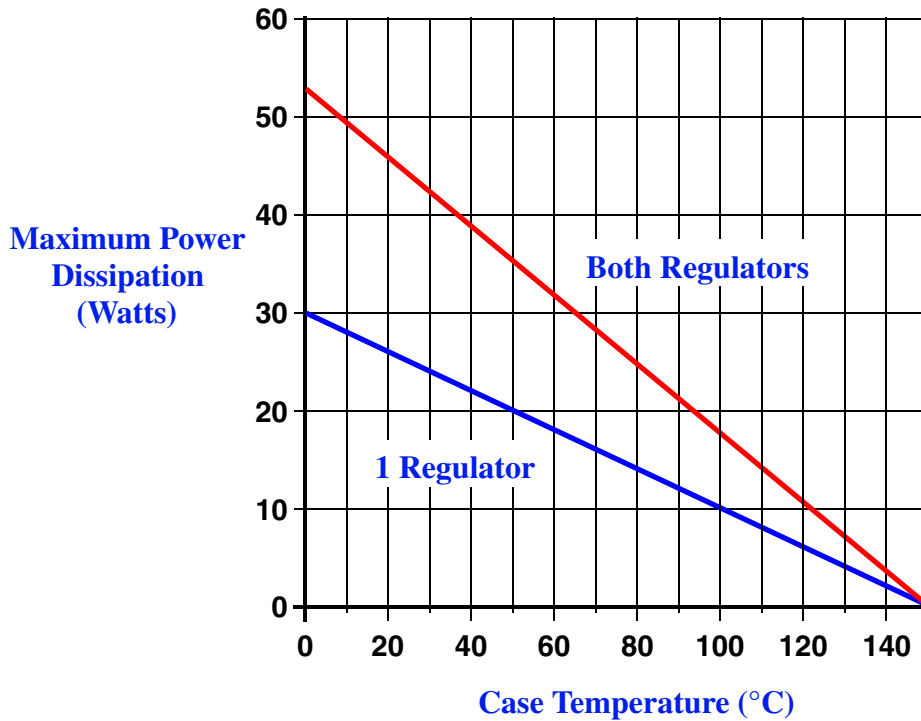
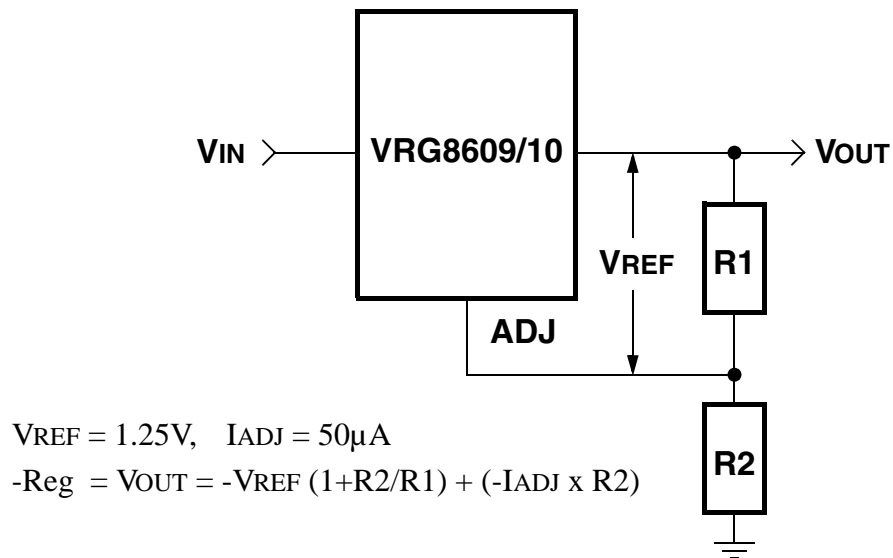


FIGURE 2 – MAXIMUM POWER vs CASE TEMPERATURE

The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VRG8609/10. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VRG8609/10 with one regulator on (the other off) and the other line represents both regulators on dissipating equal power. If both regulators are on and one regulator is dissipating more power than the other, the maximum power dissipation of the VRG8609/10 will fall between the two lines. This graph is based on the maximum junction temperature of 150°C and a thermal resistance (Θ_{JC}) of 5°C/W.

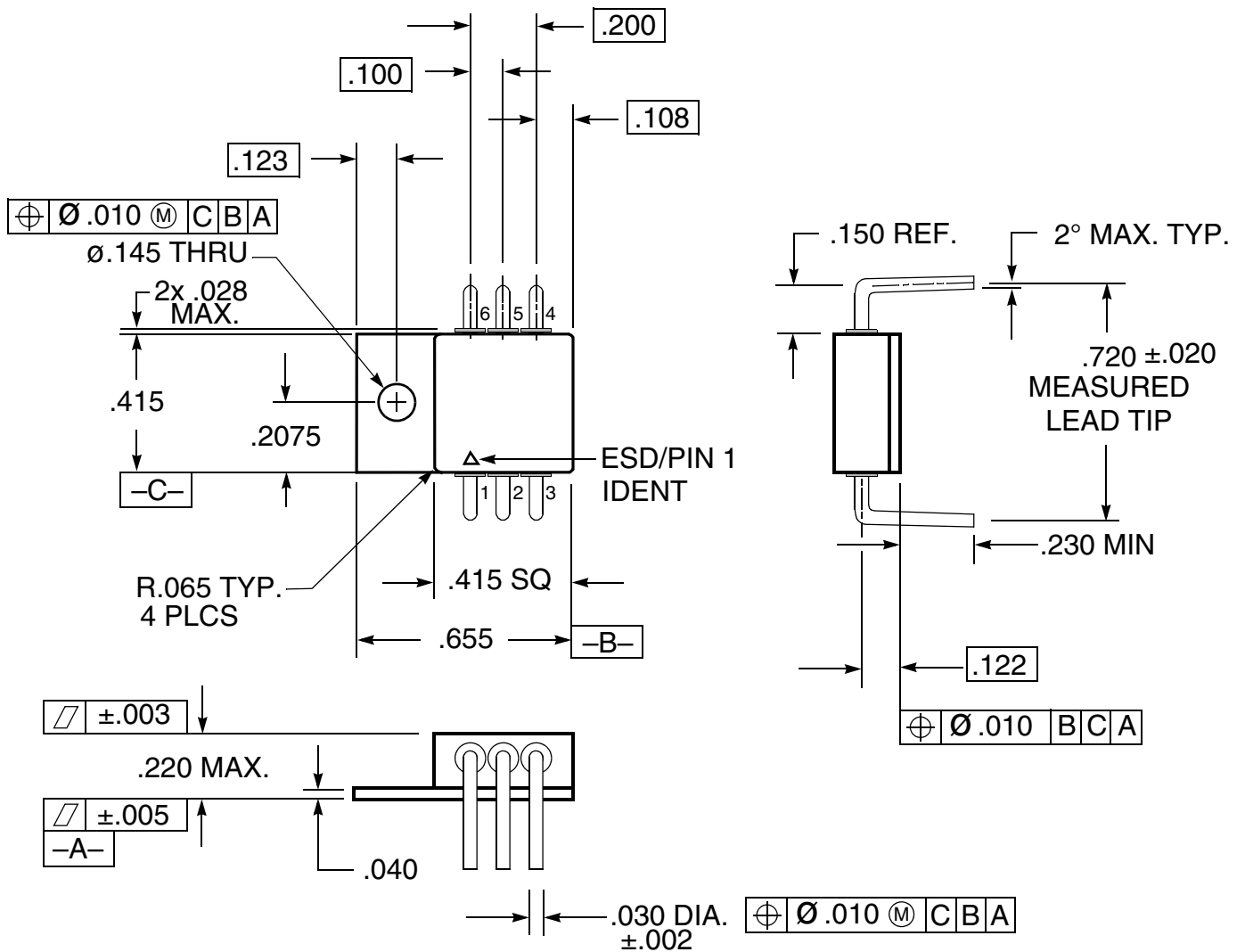


Adjustable Regulator

FIGURE 3 – TYPICAL APPLICATIONS

TABLE I – PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	NEG_ADJ_1
2	NEG_VOUT_1
3	NEG_VIN_1
4	NEG_ADJ_2
5	NEG_VIN_2
6	NEG_VOUT_2



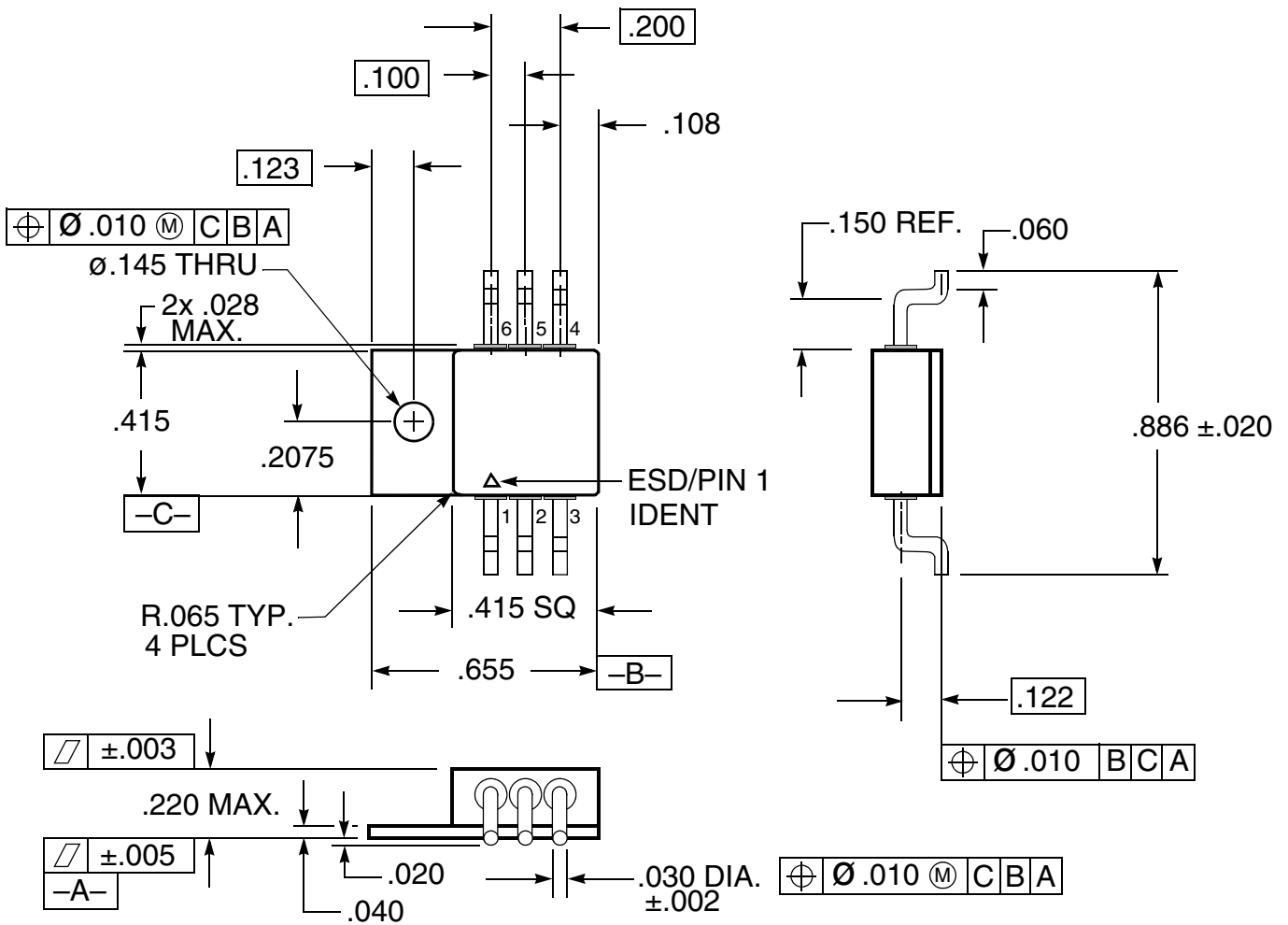
Notes:

1. Dimension Tolerance: $\pm .005$ inches
2. Package contains BeO substrate
3. Case electrically isolated

FIGURE 4 – PACKAGE OUTLINE — THRU-HOLE POWER PACKAGE

TABLE II – PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	NEG_ADJ_1
2	NEG_VOUT_1
3	NEG_VIN_1
4	NEG_ADJ_2
5	NEG_VIN_2
6	NEG_VOUT_2



Notes:

1. Dimension Tolerance: $\pm .005$ inches
2. Package contains BeO substrate
3. Case electrically isolated

FIGURE 5 – PACKAGE OUTLINE — SURFACE MOUNT POWER PACKAGE

ORDERING INFORMATION

MODEL	DLA SMD #	SCREENING	PACKAGE
VRG8609-7	-	Commercial Flow, +25°C testing only	6 Lead Thru-Hole Power Pkg
VRG8609-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
VRG8609-201-1S	5962-0521904KXC	In accordance with DLA SMD	
VRG8609-201-2S	5962-0521904KXA		
VRG8609-901-1S	5962R0521904KXC	In accordance with DLA Certified RHA Program Plan to RHA level "R", 100 krad(Si)	
VRG8609-901-2S	5962R0521904KXA		
VRG8610-7	-	Commercial Flow, +25°C testing only	6 Lead Surface Mount Power Pkg
VRG8610-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
VRG8610-201-1S	5962-0521904KYC	In accordance with DLA SMD	
VRG8610-201-2S	5962-0521904KYA		
VRG8610-901-1S	5962R0521904KYC	In accordance with DLA Certified RHA Program Plan to RHA level "R", 100 krad(Si)	
VRG8610-901-2S	5962R0521904KYA		

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