Voltage Regulator

VRG8666

1A ULDO Adjustable Positive Voltage Regulator

Released Datasheet

Cobham.com/HiRel

March 31, 2016

The most important thing we build is trust

FEATURES

- ❑ Manufactured using LINEAR TECHNOLOGY Space Qualified RH3080 die
- ❑ Radiation performance
  - Total dose: 100 krad(Si), Dose rate = 50-300 rad(Si)/s
  - ELDRS: 50 krad(Si), Dose rate ≤ 0.01 rad(Si)/s
- ❑ Current Limit with Foldback and Over-temperature protection
- ❑ Output voltage adjustable: 0V to 36V
- ❑ Outputs may be paralleled for higher current
- ❑ Post Radiated Dropout voltage:
  - 0.60V @ 1.0 Amps
  - 0.39V @ 0.5 Amps
- ❑ Output current: 1.0 Amps
- ❑ Packaging – Hermetic Ceramic
  - Hermetic Surface Mount Power
  - 5 Pads, .545"L x .296"W x .120"Ht
  - Weight - 2.0 gm max
- ❑ Designed for aerospace and high reliability space applications
- ❑ **Radiation Hardness Assurance Plan: DLA Certified to MIL-PRF-38534, Appendix G.**

DESCRIPTION

The VRG8666 consists of a Positive Adjustable (RH3080) ULDO voltage regulator capable of supplying 1.0 Amps over the output voltage range as defined under recommended operating conditions. The VRG8666 offers excellent line and load regulation specifications and ripple rejection.

The VRG8666 serves a wide variety of applications including SCSI-2 Active Terminator, High Efficiency Linear Regulators, Post Regulators for Switching Supplies, Constant Current Regulators, Battery Chargers and Microprocessor Supply.

The VRG8666 has been specifically designed to meet exposure to radiation environments and is configured for an SMD power package. It is guaranteed operational from -55°C to +125°C. Available screened to MIL-STD-883, the VRG8666 is ideal for demanding military and space applications.

Dropout (V_{IN} - V_{OUT}) decreases at lower load currents.

Input capacitance is required for load regulation. 1uF is recommended on Vin and Vcontrol. For stable operation, a 0.1uF capacitor should be placed on Vset and a low ESR capacitor on Vout. See Figure 5.

For detailed performance characteristic curves, applications information and typical applications see the latest LINEAR TECHNOLOGY Linear Technology Corporation® data sheets for their RH/LT3080, which is available on-line at www.linear.com.

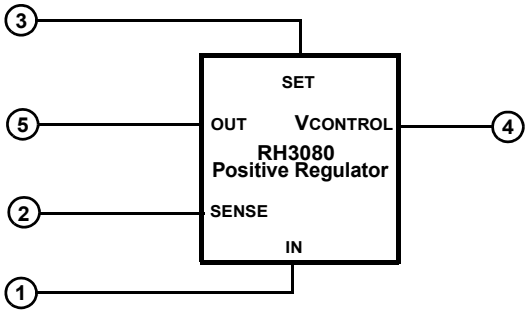


FIGURE 1 – BLOCK DIAGRAM / SCHEMATIC

ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Units
Input Voltage, $V_{CONTROL}$ (Voltages are Relative to V_{OUT})	+40, -0.3	V_{DC}
Output Current	1.2	A
Lead temperature (soldering 10 Sec)	300	°C
Input Output Differential	26	V_{DC}
Output Voltage	+36	V_{DC}
ESD 1/	2,000 - 3,999	V
Operating Junction Temperature Range	-55 to +150	°C
Storage Temperature Range	-65 to +150	°C
Thermal Resistance (Junction to Case) Θ_{JC}	5	°C/W

1/ Meets ESD testing per MIL-STD-883, method 3015, Class 2.
NOTICE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

RECOMMENDED OPERATING CONDITIONS

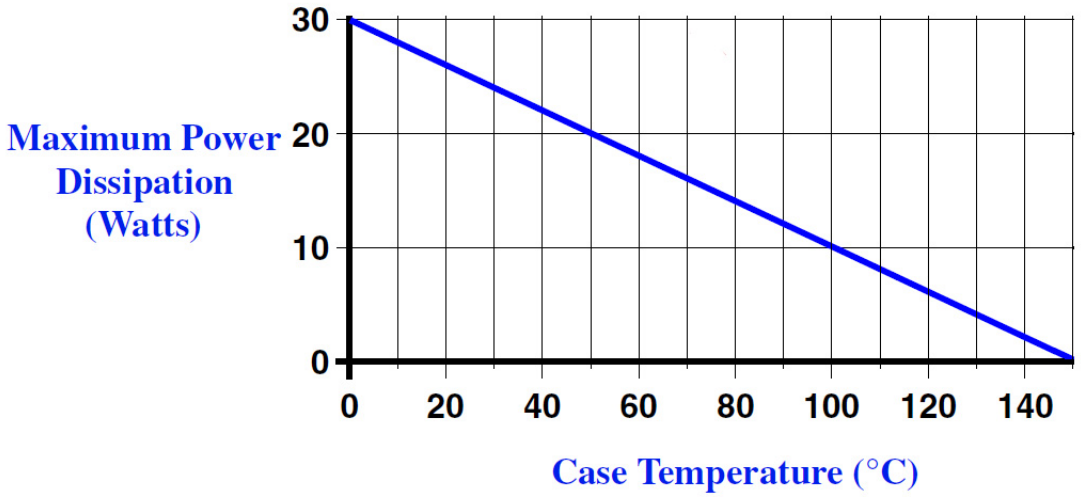
Parameter	Range	Units
Output Voltage Range	0 to 35	V_{DC}
Input Output Differential	0.5 to 26	V_{DC}
Case Operating Temperature Range	-55 to +125	°C
Input Voltage (Voltages are Relative to V_{OUT})	1 to 36	V
$V_{CONTROL}$ (Voltages are Relative to V_{OUT})	1.6 to 36	V

ELECTRICAL PERFORMANCE CHARACTERISTICS

Unless otherwise specified: -55°C ≤ Tc ≤ +125°C.

Parameter	Symbol	Conditions ($P \leq P_{MAX}$)	Min	Max	Units
Set Pin Current	I_{REF_1}	1.0mA ≤ I_{LOAD} ≤ 1.0A, (V_{IN} - V_{OUT}) = 1.6V	9.85	10.35	µA
Set Pin Current 1/	I_{REF_2}	V_{IN} = 1V, $V_{CONTROL}$ = 2V, I_{LOAD} = 1mA	9.85	10.35	µA
Output Offset Voltage (V_{OUT} - V_{SET}) 1/	V_{OS}	V_{IN} = 1V, $V_{CONTROL}$ = 2V, I_{LOAD} = 1mA,	-9.0	9.0	mV
Line Regulation 1/	ΔV_{OS}	1V ≤ V_{IN} ≤ 26V, 2V ≤ $V_{CONTROL}$ ≤ 26V, I_{LOAD} = 1mA	-0.15	0.15	mV/V
Load Regulation 1/	ΔV_{OS}	(V_{IN} - V_{OUT}) = 3V, I_{LOAD} = 1mA to 0.1A	-1.4	1.4	mV
$V_{CONTROL}$ Dropout Voltage 2/	V_{CDROP}	I_{LOAD} = 1.0A	-	1.60	V
		I_{LOAD} = 0.1A 1/, 4/	-	1.60	
V_{IN} Dropout Voltage 2/	V_{INDROP}	I_{LOAD} = 1.0A	-	0.5	V
		I_{LOAD} = 0.1A 1/, 4/	-	0.25	
Current Limit 3/	I_{MAX}	V_{IN} = $V_{CONTROL}$ = +5V, V_{SET} = 0V , V_{OUT} = 0.1V	1.1	-	A
Minimum Load Current 1/, 4/	I_{MIN}	V_{IN} = $V_{CONTROL}$ = 26V, V_{OUT} = 0.1V	-	0.9	mA
Ripple Rejection	-	I_{LOAD} = 0.2A, (V_{IN} - V_{OUT}) = 3V, f = 120Hz, C_{OUT} = 2.2µF, C_{SET} = 0.1µF	60	-	dB
Thermal Regulation	-	30ms pulse, T_C = +25°C	-	0.03	%/W

Notes:
1/ Specification derated to reflect Total Dose exposure to 100 krad(Si) @+25°C.
2/ Dropout results from either minimum control voltage, $V_{CONTROL}$, or minimum input voltage, V_{IN}, both specified with respect to V_{OUT}. These specifications represent the minimum input-to-output differential voltage required to maintain regulation.
3/ Pulsed @ <10% duty cycle @ +25°C for characterization only. (See note 1/).
4/ Not tested. Shall be guaranteed to the specified limits.



The maximum Power dissipation is limited by the thermal shutdown function of the regulator chip in the VRG8666. The graph above represents the achievable power before the chip shuts down. The line in the graph represents the maximum power dissipation of the VRG8666 This graph is based on the maximum junction temperature of 150°C and a thermal resistance (Θ_{JC}) of 5°C/W.

FIGURE 2 – MAXIMUM POWER vs CASE TEMPERATURE

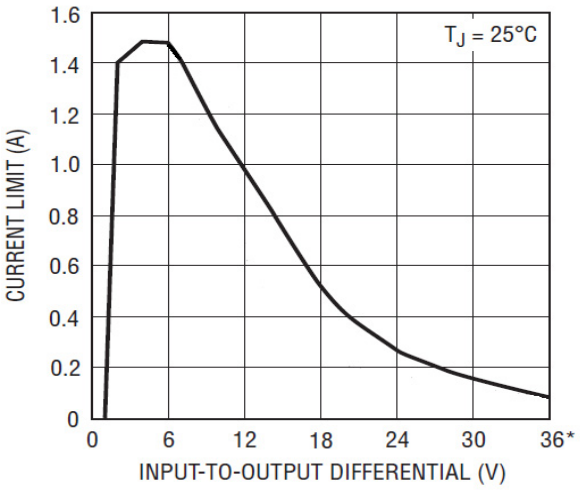


FIGURE 3 – RH3080 TYPICAL CURRENT LIMIT

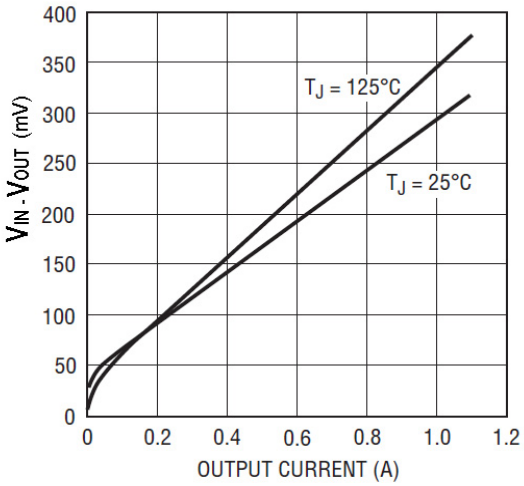


FIGURE 4 – RH3080 TYPICAL DROPOUT VOLTAGE CURVE, $V_{CONTROL}$ ≥ 1.6V

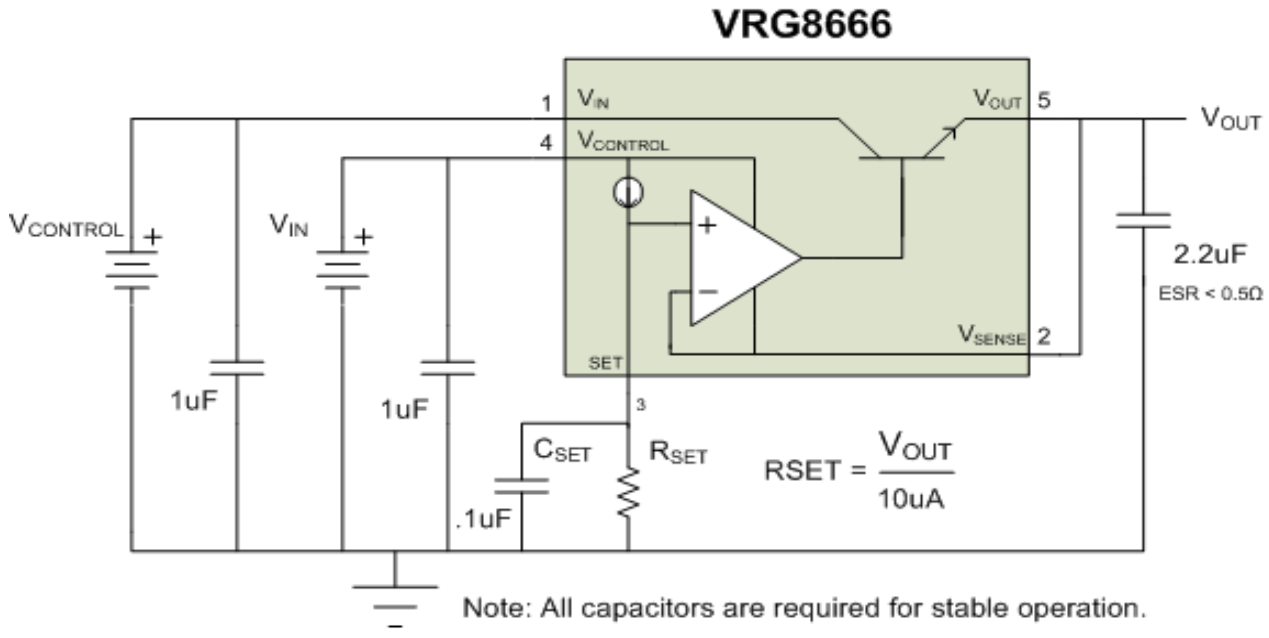
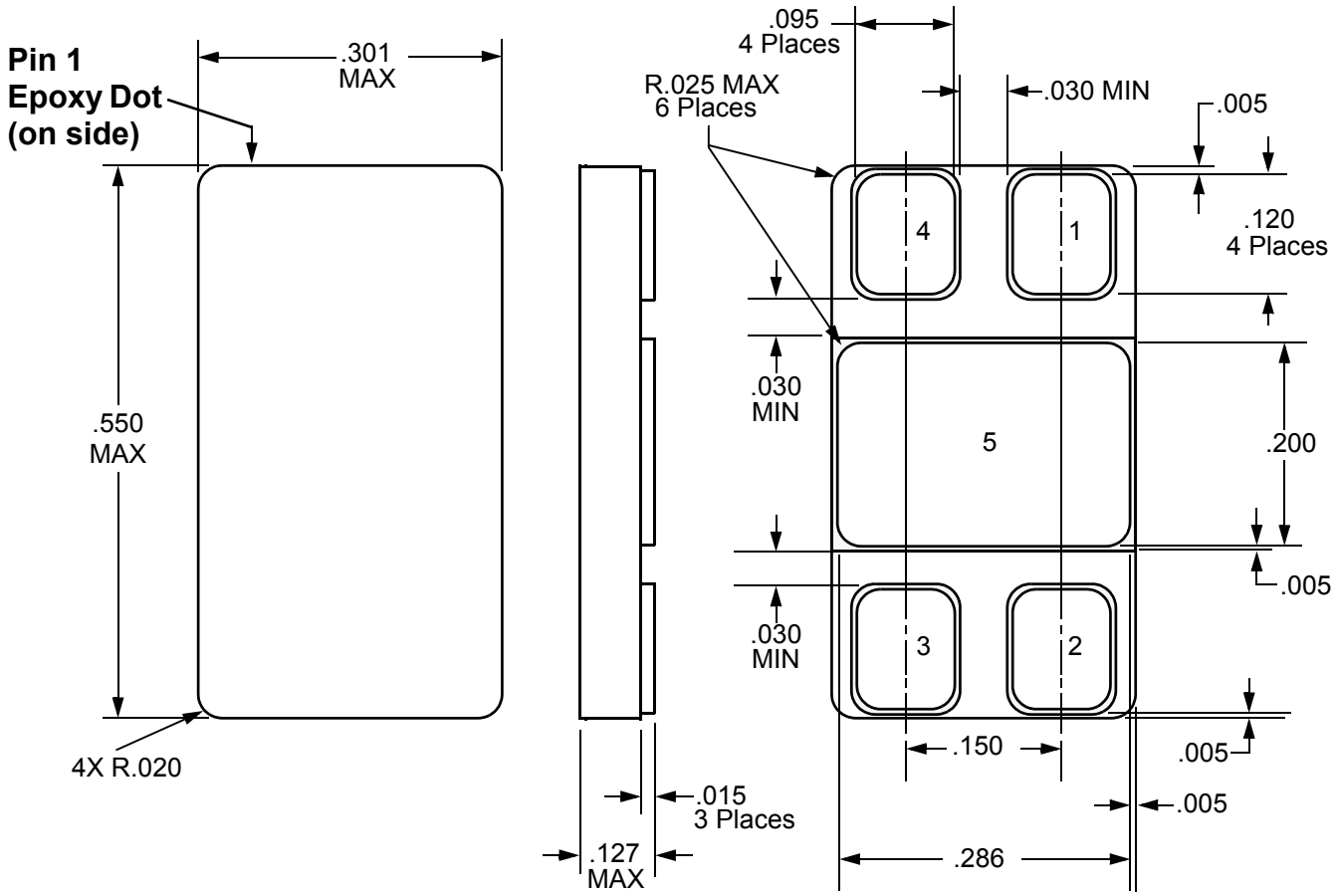


FIGURE 5 – BASIC VRG8666 ADJUSTABLE REGULATOR APPLICATION



NOTES:

1. Package & Lid are electrically isolated from signal pads

FIGURE 6 – PACKAGE OUTLINE — SURFACE MOUNT

ORDERING INFORMATION

Model	DLA SMD #	Screening	Package
VRG8666-7	-	Commercial Flow, +25°C testing only	SMD Power Pkg
VRG8666-201-1S	5962-1120501KYC	In accordance with DLA SMD	
VRG8666-201-2S	5962-1120501KYA		
VRG8666-901-1S	5962R1120501KYC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8666-901-2S	5962R1120501KYA		

REVISION HISTORY

Date	Revision	Change Description
03/31/2016	M	Import into Cobham format

Datasheet Definition

Advanced Datasheet - Product In Development

Preliminary Datasheet - Shipping Prototype

Datasheet - Shipping QML & Reduced Hi-Rel

For detailed performance characteristic curves, applications information and typical applications, see the latest LINEAR TECHNOLOGY datasheet for their RH3080, which is available on-line at www.linear.com.

EXPORT CONTROL:

This product is controlled for export under the Export Administration Regulations (EAR), 15 CFR Parts 730-774.
A license from the Department of Commerce may be required prior to the export of this product from the United States.

Cobham Semiconductor Solutions
35 S. Service Road
Plainview, NY 11803

E: info-ams@cobham.com
T: 800 645 8862

COBHAM