Voltage Regulator

VRG8667/68

Dual 1A ULDO Adjustable Positive Voltage Regulators

Released Datasheet

Cobham.com/HiRel

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The most important thing we build is trust

FEATURES

- Manufactured using LINEAR TECHNOLOGY Space Qualified RH3080 die
- Radiation performance
  - Total dose: 100 krad(Si), Dose rate = 50 - 300 rad(Si)/s
  - ELDRS: 50 krad(Si), Dose rate ≤ 0.01 rad(Si)/s
- Two-Independent voltage regulators
- Current Limit with Foldback
- Over-temperature protection
- Output voltage adjustable: 0V to 36V
- Outputs may be paralleled for higher current
- Post Radiated Dropout voltage:
  - 0.60V @ 1.0Amps
  - 0.39V @ 0.5Amps
- Output current: 1.0Amps
- Packaging – Hermetic Meter Power Package
  - Thru-hole or Surface mount
  - 8 Leads, .755"L x .415"W x .200"Ht
  - Weight - 6 gm max
- Designed for aerospace and high reliability space applications
- **Radiation Hardness Assurance Plan: DLA Certified to MIL-PRF-38534, Appendix G.**

DESCRIPTION

The VRG8667/8668 consists of two Positive Adjustable (RH3080) ULDO voltage regulators each capable of supplying 1.0Amps over the output voltage range as defined under recommended operating conditions. The VRG8667/8668 offers excellent line and load regulation specifications and ripple rejection. There is full electrical isolation between the regulators and each regulator to the package.

The VRG8667/8668 has been specifically designed to meet exposure to radiation environments. The VRG8667 is configured for a Thru-Hole 8 lead metal power package and the VRG8668 is configured for a Surface Mount 8 lead metal power package. It is guaranteed operational from -55°C to +125°C. Available screened in accordance with MIL-PRF-38534, the VRG8667/8668 is ideal for demanding military and space applications.

Dropout (VIN - VOUT) decreases at lower load currents for both regulators.

Input capacitance is required for load regulation. 1uF is recommended on Vin and Vcontrol. For stable operation, a 0.1uF capacitor should be placed on Vset and a low ESR capacitor on Vout. See Figure 5.

For detailed performance characteristic curves, applications information and typical applications see the latest LINEAR TECHNOLOGY Linear Technology Corporation® data sheets for their RH/LT3080, which is available on-line at www.linear.com.

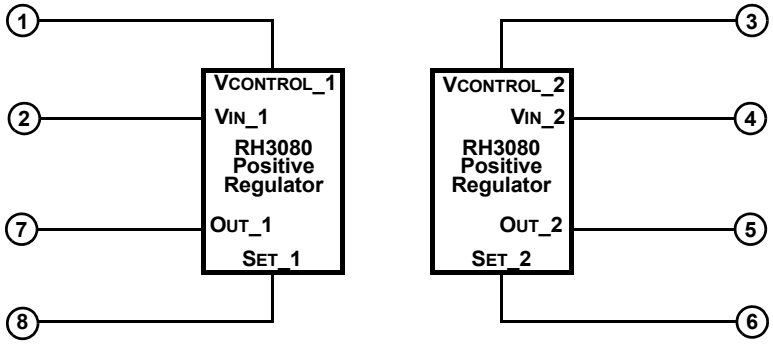


FIGURE 1 – BLOCK DIAGRAM / SCHEMATIC

ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Units
Input Voltage, $V_{CONTROL}$ (Voltages are Relative to V_{OUT})	+40, -0.3	V_{DC}
Output Current	1.2	A
Lead temperature (soldering 10 Sec)	300	°C
Input Output Differential	26	V_{DC}
ESD 1/	2,000 - 3,999	V
Operating Junction Temperature Range	-55 to +150	°C
Storage Temperature Range	-65 to +150	°C
Thermal Resistance (Junction to Case) Θ_{JC}	7	°C/W

1/ Meets ESD testing per MIL-STD-883, method 3015, Class 2.
NOTICE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

RECOMMENDED OPERATING CONDITIONS

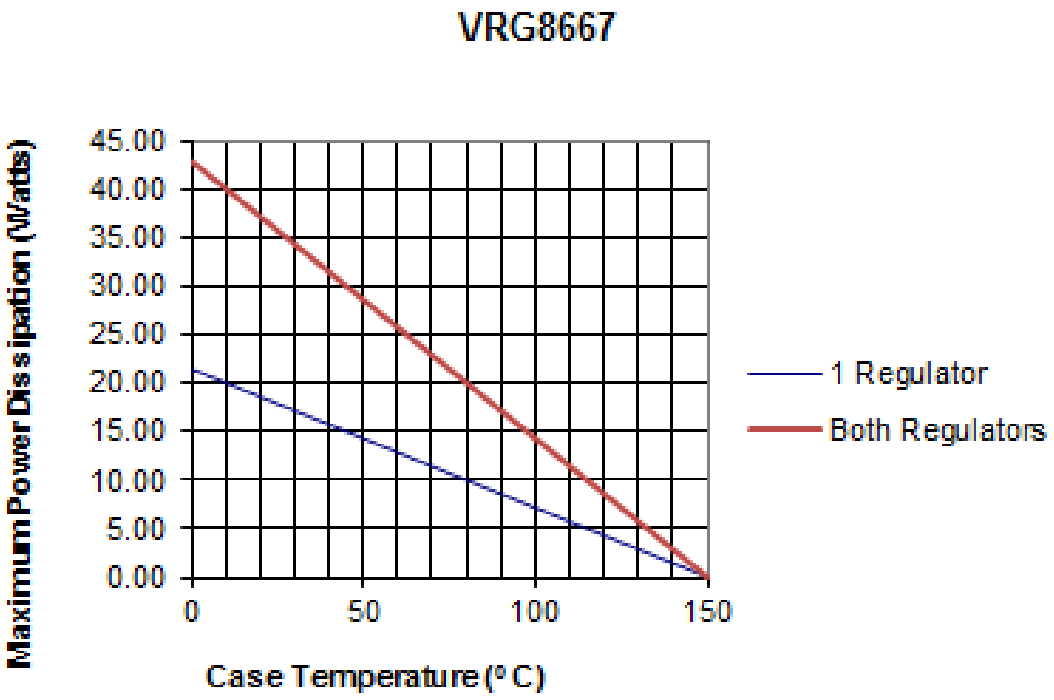
Parameter	Range	Units
Output Voltage Range	0 to 35	V_{DC}
Input Output Differential	0.5 to 26	V_{DC}
Case Operating Temperature Range	-55 to +125	°C
Input Voltage (Voltages are Relative to V_{OUT})	1 to 36	V
$V_{CONTROL}$ (Voltages are Relative to V_{OUT})	1.6 to 36	V

ELECTRICAL PERFORMANCE CHARACTERISTICS

Unless otherwise specified: -55°C ≤ Tc ≤ +125°C.

Parameter	Symbol	Conditions ($P \leq P_{MAX}$)	Min	Max	Units
Set Pin Current	I_{REF_1}	1.0mA ≤ I_{LOAD} ≤ 1.0A, (V_{IN} - V_{OUT}) = 1.6V	9.85	10.35	µA
Set Pin Current 1/	I_{REF_2}	V_{IN} = 1V, $V_{CONTROL}$ = 2V, I_{LOAD} = 1mA	9.85	10.35	µA
Output Offset Voltage (V_{OUT} - V_{SET}) 1/	V_{OS}	V_{IN} = 1V, $V_{CONTROL}$ = 2V, I_{LOAD} = 1mA,	-9.0	9.0	mV
Line Regulation 1/	ΔV_{OS}	1V ≤ V_{IN} ≤ 26V, 2V ≤ $V_{CONTROL}$ ≤ 26V, I_{LOAD} = 1mA	-0.15	0.15	mV/V
Load Regulation 1/	ΔV_{OS}	(V_{IN} - V_{OUT}) = 3V, I_{LOAD} = 1mA to 0.1A	-1.4	1.4	mV
$V_{CONTROL}$ Dropout Voltage 2/	V_{CDROP}	I_{LOAD} = 1.0A	-	1.65	V
		I_{LOAD} = 0.1A 1/, 4/	-	1.65	
V_{IN} Dropout Voltage 2/	V_{INDROP}	I_{LOAD} = 1.0A	-	0.5	V
		I_{LOAD} = 0.1A 1/, 4/	-	0.25	
Current Limit 3/	I_{MAX}	V_{IN} = $V_{CONTROL}$ = +5V, V_{SET} = 0V , V_{OUT} = 0.1V	1.1	-	A
Minimum Load Current 1/, 4/	I_{MIN}	V_{IN} = $V_{CONTROL}$ = 26V, V_{OUT} = 0.1V	-	0.9	mA
Ripple Rejection	-	I_{LOAD} = 0.2A, (V_{IN} - V_{OUT}) = 3V, f = 120Hz, C_{OUT} = 2.2µF, C_{SET} = 0.1µF	60	-	dB
Thermal Regulation	-	30ms pulse, Tc = +25°C	-	0.03	%/W

Notes:
1/ Specification derated to reflect Total Dose exposure to 100 krad(Si) @+25°C.
2/ Dropout results from either minimum control voltage, $V_{CONTROL}$, or minimum input voltage, V_{IN}, both specified with respect to V_{OUT}. These specifications represent the minimum input-to-output differential voltage required to maintain regulation.
3/ Pulsed @ <10% duty cycle @ +25°C for characterization only. (See note 1/).
4/ Not tested. Shall be guaranteed to the specified limits.



The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VRG8667/8668. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VRG8667/8668 with one regulator on (the other off) and the other line represents both regulators on, dissipating equal power. If both regulators are on and one regulator is dissipating more power than the other, the maximum power dissipating of the VRG8667/8668 will fall between the two lines. This graph is based on the maximum junction temperature of 150°C and a thermal resistance (Θ_{JC}) of 7°C/W.

FIGURE 2 – MAXIMUM POWER vs CASE TEMPERATURE

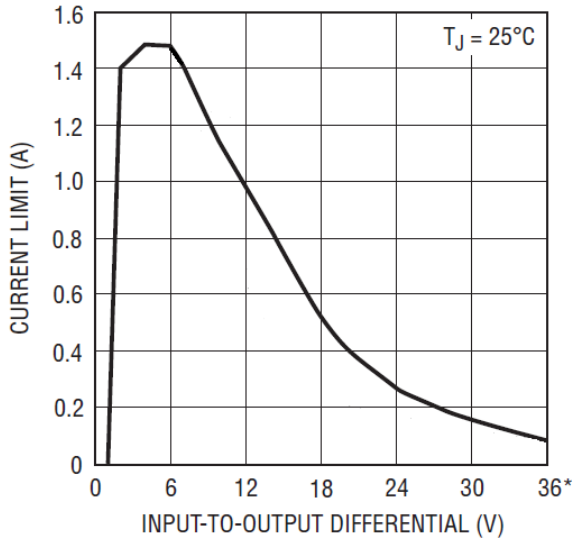


FIGURE 3 – RH3080 CURRENT LIMIT

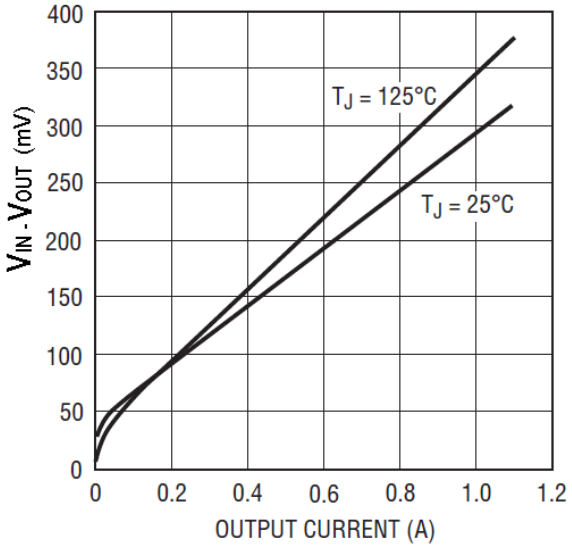
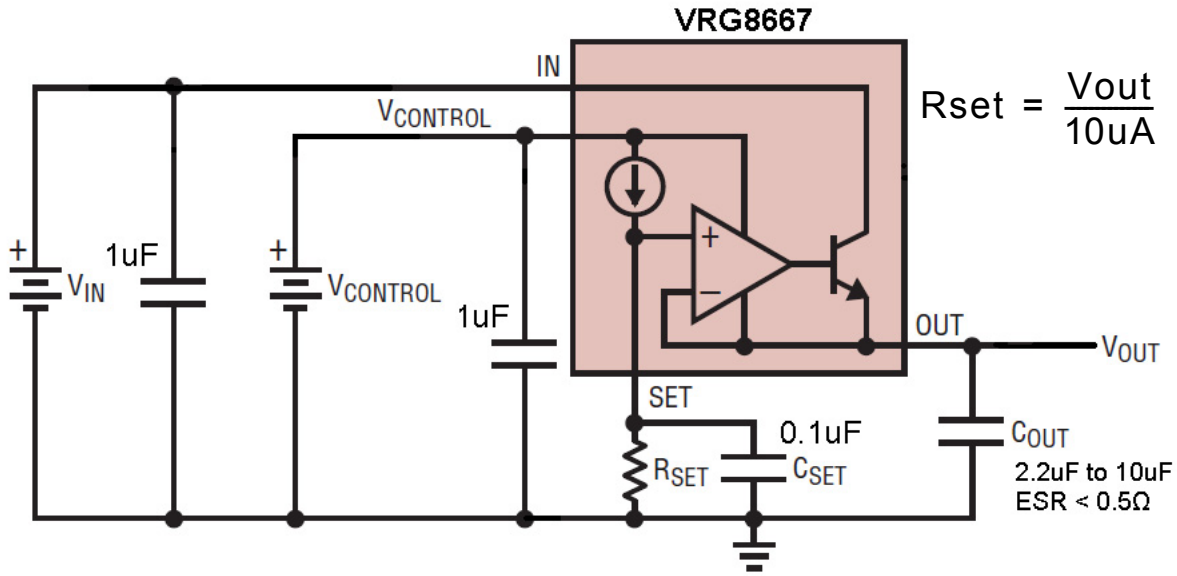


FIGURE 4 – RH3080 TYPICAL DROPOUT VOLTAGE CURVE ($V_{CONTROL}$ ≥ 1.6V)

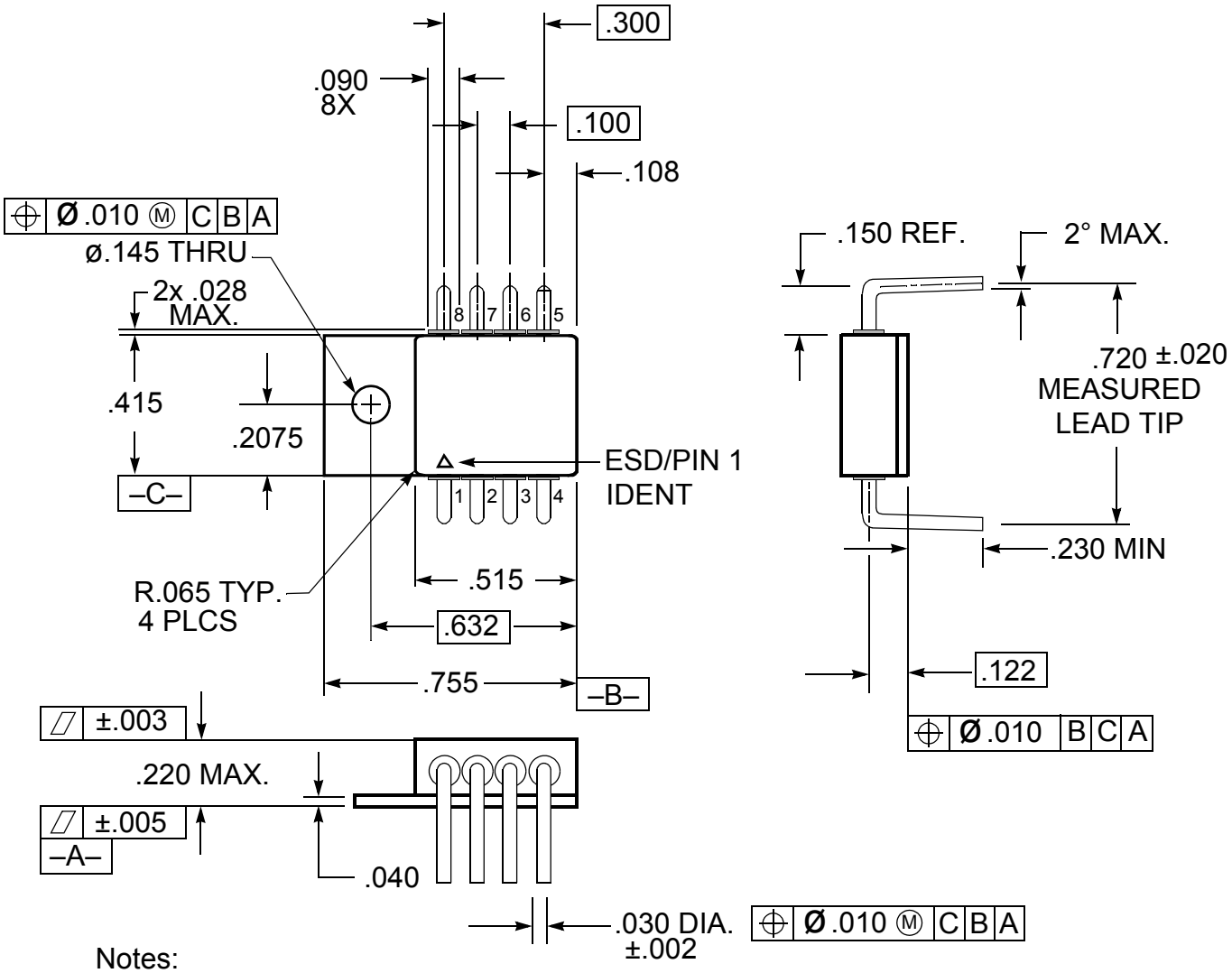


Note: All Capacitors are required for stable operation

FIGURE 5 – BASIC VRG8667 ADJUSTABLE REGULATOR APPLICATION

TABLE I – PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	VCONTROL_1
2	VIN_1
3	VCONTROL_2
4	VIN_2
5	OUT_2
6	SET_2
7	OUT_1
8	SET_1

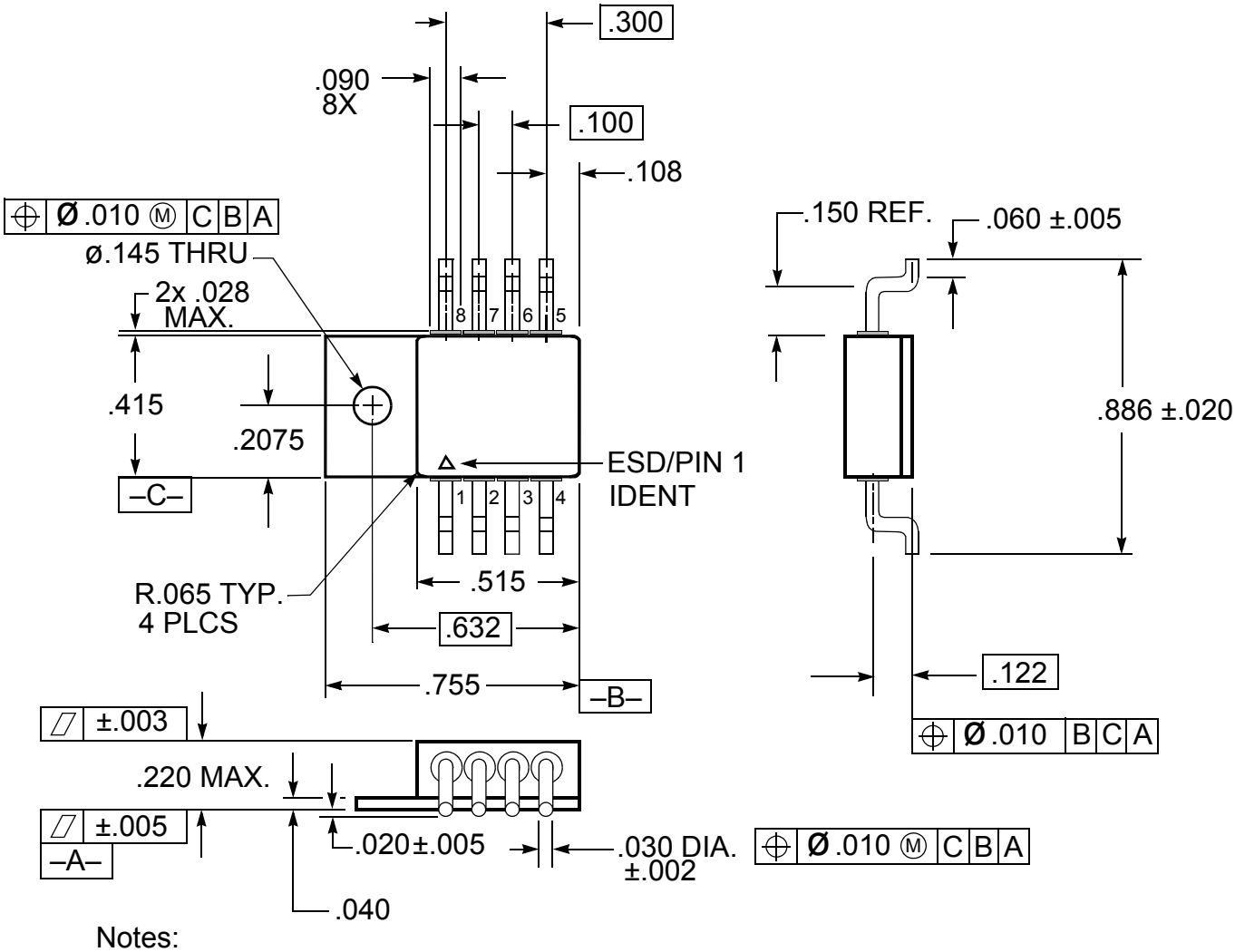


Notes:
1. Dimension Tolerance: ±.005 inches
2. Package contains BeO substrate
3. Case electrically isolated

FIGURE 6 – VRG8667 PACKAGE OUTLINE — THRU-HOLE POWER PACKAGE

TABLE II – PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	VCONTROL_1
2	VIN_1
3	VCONTROL_2
4	VIN_2
5	OUT_2
6	SET_2
7	OUT_1
8	SET_1



Notes:
1. Dimension Tolerance: ±.005 inches
2. Package contains BeO substrate
3. Case electrically isolated

FIGURE 7 – VRG8668 PACKAGE OUTLINE — SURFACE MOUNT POWER PACKAGE

ORDERING INFORMATION

Model	DLA SMD #	Screening	Package
VRG8667-7	-	Commercial Flow, +25°C testing only	8-Lead Thru-Hole Power Pkg
VRG8667-901-1S	5962R1320301KUC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8667-901-2S	5962R1320301KUA		
VRG8668-7		Commercial Flow, +25°C testing only	8-Lead Surface-Mount Power Pkg
VRG8668-901-1S	5962R1320301KZC	In accordance with DLA Certified RHA Program Plan to RHA Level "R", 100 krad(Si)	
VRG8668-901-2S	5962R1320301KZA		

REVISION HISTORY

Date	Revision	Change Description
03/24/2016	E	Import into Cobham format

Datasheet Definition

Advanced Datasheet - Product In Development

Preliminary Datasheet - Shipping Prototype

Datasheet - Shipping QML & Reduced Hi-Rel

For detailed performance characteristic curves, applications information and typical applications, see the latest LINEAR TECHNOLOGY datasheet for their RH3080, which is available on-line at www.linear.com.

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