Datasheet

#### **Standard Products**

# VRG8697/VRG8698

# Dual Voltage Regulator, 2.5 Amp, Positive Ultra Low Dropout (ULDO), Adjustable

**Radiation Tolerant** 

www.aeroflex.com/voltreg
December 10, 2015





# **FEATURES**

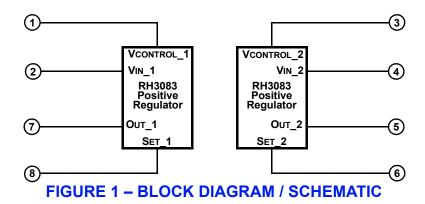
- ☐ Manufactured using Space Qualified RH3083 die
- □ Radiation performance
  - Total dose: 100 krads(Si), Dose rate = 50 300 rads(Si)/s
     ELDRS: 50 krads(Si), Dose rate ≤ 0.01 rads(Si)/s
- ☐ Two-Independent voltage regulators
- □ Current Limit with Foldback
- □ Over-temperature protection
- ☐ Input voltage range: 1.0V to 23V
- □ Output voltage adjustable: 0V to 22V
- □ Outputs may be paralleled for higher current
- □ Post Radiated Dropout voltage, VCONTROL  $\geq$  2.0V:
  - 0.75V @ 2.5Amps
  - 0.28V (a) 1.0Amps
- □ Output current: 2.5Amps
- □ Packaging Hermetic Meter Power Package
  - Thru-hole or Surface mount
  - 8 Leads, .755"L x .415"W x .200"Ht
  - Weight 6 gm max
- ☐ Designed for aerospace and high reliability space applications
- ☐ Aeroflex Plainview's Radiation Hardness Assurance Plan is DLA Certified to MIL-PRF-38534, Appendix G.

#### DESCRIPTION

The Aeroflex Plainview VRG8697/8698 consists of two Positive Adjustable (RH3083) ULDO voltage regulators each capable of supplying 2.5Amps over the output voltage range as defined under recommended operating conditions. The VRG8697/8698 offers excellent line and load regulation specifications and ripple rejection. There is full electrical isolation between the regulators and each regulator to the package.

The VRG8697/8698 has been specifically designed to meet exposure to radiation environments. The VRG8697 is configured for a Thru-Hole 8 lead metal power package and the VRG8698 is configured for a Surface Mount 8 lead metal power package. It is guaranteed operational with a case operating temperature from -55°C to +125°C. Available screened in accordance with MIL-PRF-38534, the VRG8697/8698 is ideal for demanding military and space applications.

Dropout (VIN - VOUT) decreases at lower load currents for both regulators.



#### ABSOLUTE MAXIMUM RATINGS

Parameter (Voltage is Relative to Vout)	Rating	Units
Input Voltage (No Overload or Short Circuit)	+23	VDC
VCONTROL	+28	VDC
Output Short Circuit Duration	Indefinite	-
Lead temperature (soldering 10 Sec)	300	°C
Input Output Differential	18	VDC
ESD <u>1</u> /	2,000 - 3,999	V
Operating Junction Temperature Range	-55 to +150	°C
Storage Temperature Range	-65 to +150	°C
Thermal Resistance (Junction to Case) ⊖JC	4	°C/W

<sup>1/</sup> Meets ESD testing per MIL-STD-883, method 3015, Class 2.

NOTICE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may effect device reliability.

# RECOMMENDED OPERATING CONDITIONS

Parameter	Range	Units
Output Voltage Range	0 to 22	VDC
Input Output Differential	0.5 to 18	VDC
Case Operating Temperature Range	-55 to +125	°C
Input Voltage (Voltages are Relative to Vout)	1 to 23	V
VCONTROL (Voltages are Relative to VOUT)	1.6 to 28	V

# **ELECTRICAL PERFORMANCE CHARACTERISTICS**

Unless otherwise specified: -55°C ≤ Tc ≤ +125°C.

Parameter	Symbol	Conditions (P≤PMAX)	Min	Max	Units
Set Pin Current	IREF <sub>1</sub>	$1.0\text{mA} \le \text{ILOAD} \le 2.5\text{A}$ , $\text{Vin} \ge 2\text{V}$ , $\text{VCONTROL} \ge 3\text{V}$	49.0	51.5	μΑ
Set Pin Current 1/	IREF <sub>2</sub>	VIN = 2V, VCONTROL = 3V, ILOAD = 1mA, TC = 25°C	49	51	μA
Output Offset Voltage (Vout - Vset) 1/	Vos	VIN = 2V, VCONTROL = 3V, ILOAD = 1mA,	-6.0	6.0	mV
Line Regulation 1/	ΔVos	$1V \le VIN \le 23V$ , $2V \le VCONTROL \le 25V$ , $ILOAD = 1mA$ , $TC = +25^{\circ}C$	-0.07	0.07	mV/V
Load Regulation 1/	ΔVos	(VIN - VOUT) = 3V, ILOAD = 5mA to 2.5A, Tc = 25°C	-10.0	10.0	mV
VCONTROL Dropout Voltage 2/	Vcdrop	ILOAD = 2.5A	-	1.65	V
	VCDROP	ILOAD = 1.0A <u>1</u> /	-	1.60	V
VIN Dropout Voltage 2/ VIND	VINDROP	ILOAD = 2.5A	-	0.75	V
	VINDROP	ILOAD = 1.0A <u>1</u> /	-	0.28	V
Current Limit 3/	IMAX	VIN = VCONTROL = +5V, VSET = 0V, VOUT = +0.4V	2.6	-	Α
Minimum Load Current 1/ 4/	IMIN	VCONTROL = 25V, VIN = 23V	-	1.0	mA
Ripple Rejection	-	ILOAD = 0.2A, (VIN - VOUT) = 3V, $f = 120$ Hz, COUT = $10\mu F$ , CSET = $0.1\mu F$	60	ı	dB

1/ Specification derated to reflect Total Dose exposure to 100 Krad (Si) @+25°C.

Z/ Dropout results from either minimum control voltage, VCONTROL, or minimum input voltage, VIN, both specified with respect to VOUT. These specifications represent the minimum input-to-output differential voltage required to maintain regulation.

3/ Pulsed @ <10% duty cycle @ +25°C for characterization only.

<sup>4/</sup> Not tested. Shall be guaranteed to the specified limits.

# VRG8697

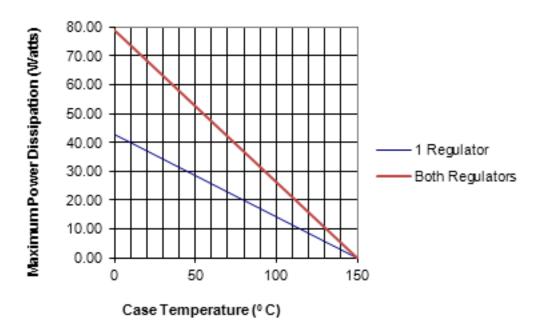
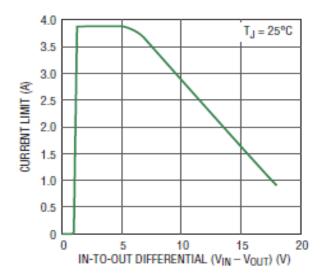


FIGURE 2 – MAXIMUM POWER vs CASE TEMPERATURE

The maximum Power dissipation is limited by the thermal shutdown function of each regulator chip in the VRG8697/8698. The graph above represents the achievable power before the chip shuts down. The first line in the graph represents the maximum power dissipation of the VRG8697/8698 with one regulator on (the other off) and the other line represents both regulators on, dissipating equal power. If both regulators are on and one regulator is dissipating more power than the other, the maximum power dissipating of the VRG8697/8698 will fall between the two lines. This graph is based on the maximum junction temperature of 150°C and a thermal resistance ( $\Theta$ JC) of 4°C/W.



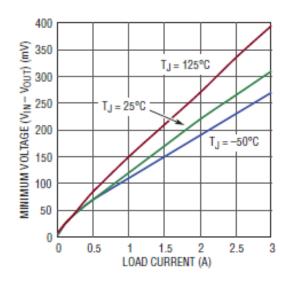


FIGURE 3 - RH3083 TYPICAL CURRENT LIMIT

FIGURE 4 – RH3083 TYPICAL DROPOUT VOLTAGE CURVE (VCONTROL ≥ 1.65V)

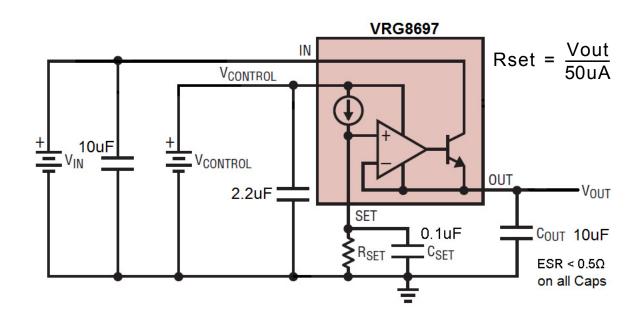
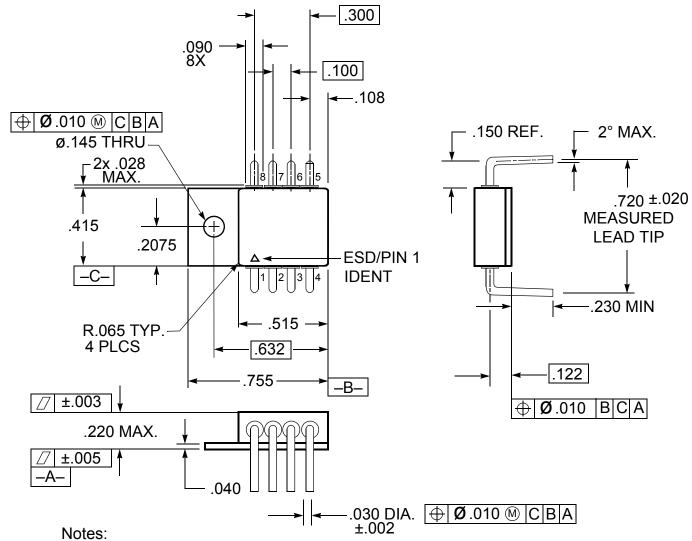


FIGURE 5 - BASIC VRG8697 ADJUSTABLE REGULATOR APPLICATION

TABLE I - PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	VCONTROL_1
2	VIN_1
3	VCONTROL_2
4	VIN_2
5	Out_2
6	SET_2
7	Out_1
8	SET_1

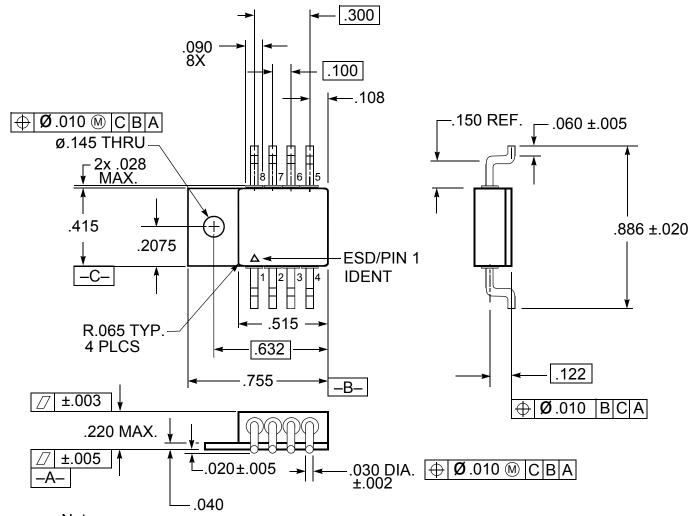


- 1. Dimension Tolerance: ±.005 inches
- 2. Package contains BeO substrate
- 3. Case electrically isolated

# FIGURE 6 – VRG8697 PACKAGE OUTLINE — THRU-HOLE POWER PACKAGE

# TABLE II - PIN NUMBERS vs FUNCTION

PIN	FUNCTION
1	VCONTROL_1
2	VIN_1
3	VCONTROL_2
4	VIN_2
5	Out_2
6	SET_2
7	OuT_1
8	SET_1



# Notes:

- 1. Dimension Tolerance: ±.005 inches
- 2. Package contains BeO substrate
- 3. Case electrically isolated

# FIGURE 7 - VRG8698 PACKAGE OUTLINE — SURFACE MOUNT POWER PACKAGE

# ORDERING INFORMATION

Model	DLA SMD#	Screening	Package	
VRG8697-7	-	Commercial Flow, +25°C testing only	8-Lead	
VRG8697-901-1S	5962R1420201KUC	In accordance with DLA Certified RHA Program Plan to RHA	Thru-Hole Power Pkg	
VRG8697-901-2S	5962R1420201KUA	Level "R", 100krads(Si)		
VRG8698-7		Commercial Flow, +25°C testing only	8-Lead	
VRG8698-901-1S	5962R1420201KZC	In accordance with DLA Certified RHA Program Plan to RHA	Surface-Mount Power Pkg	
VRG8698-901-2S	5962R1420201KZA	Level "R", 100krads(Si)		

For detailed performance characteristic curves, applications information and typical applications see the latest datasheet for their LT3083 and RH3083, which is available on-line at www.linear.com.

# **EXPORT CONTROL:**

This product is controlled for export under the Export Administration Regulations (EAR), 15 CFR Parts 730-774.

A license from the Department of Commerce may be required prior to the export of this product from the United States.

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Advanced Product in Development
Preliminary Shipping Non-Flight Prototypes
Datasheet Shipping QML and Reduced HiRel

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Performance-Driven

Customer-Focused

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