**Vishay Semiconductors** 

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# High Performance Schottky Rectifier, 100 A

Anode

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PowerTab<sup>®</sup>

PRODUCT SUMMARY				
Package	PowerTab <sup>®</sup>			
I <sub>F(AV)</sub>	100 A			
V <sub>R</sub>	30 V			
V <sub>F</sub> at I <sub>F</sub>	0.56 V			
I <sub>RM</sub>	460 mA at 125 °C			
T <sub>J</sub> max.	150 °C			
Diode variation	Single die			
E <sub>AS</sub>	9 mJ			

### FEATURES

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability
  COMPLIANT
  COMPLIANT
- Screw mounting only
- Designed and qualified according to JEDEC®-JESD 47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-100BGQ030 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1	Rectangular waveform	100	А		
I <sub>F(AV)</sub>	T <sub>C</sub>	106	°C		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	4500	А		
V <sub>F</sub>	100 A <sub>pk</sub> (typical)	0.49	V		
vF	TJ	150	O°		
TJ	Range	-55 to +150	۵°		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	100BGQ030	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	30	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	50	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 106 °C, rectangular waveform 100 A		А	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	4500	А
non-repetitive surge current	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	$V_{\text{RRM}}$ applied	850	~
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 8 A, L = 1.12 mH 36 r		mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s8AFrequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical8		А	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
	V <sub>FM</sub> <sup>(1)</sup>	50 A	• T <sub>J</sub> = 25 °C	0.47	0.5	V
Forward valtage drep		100 A		0.56	0.63	
Forward voltage drop		50 A	- T <sub>J</sub> = 150 °C	0.36	0.4	
		100 A		0.49	0.56	
Reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 15 V		80	160	mA
		$T_{J} = 150 \text{ °C}, V_{R} = 30 \text{ V}$		800	1100	
		T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.6	2.4	
		T <sub>J</sub> = 125 °C		260	460	
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ , (test signal range 100 kHz to 1 MHz) 25 °C		38	00	pF
Typical series inductance	L <sub>S</sub>	Measured from tab to mounting plane 3.5		nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs		

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and temperature range	storage	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C
Maximum thermal resis junction to case	Maximum thermal resistance, junction to case RthJC DC operation		DC operation	0.50	°C/W
Typical thermal resistar case to heatsink	nce,	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.30	0/14
Approximate weight				5	g
				0.18	oz.
	minimum			1.2 (10)	N · m
Mounting torque -	maximum			2.4 (20)	(lbf $\cdot$ in)
Marking device      Case style PowerTab <sup>®</sup>		100BG	GQ030		

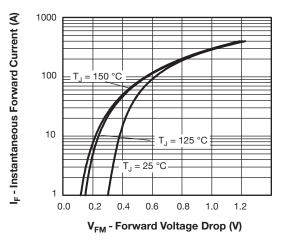
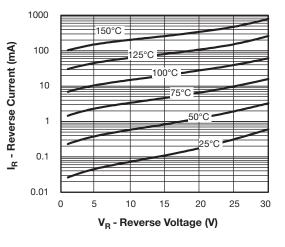
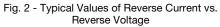


Fig. 1 - Maximum Forward Voltage Drop Characteristics





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### VS-100BGQ030

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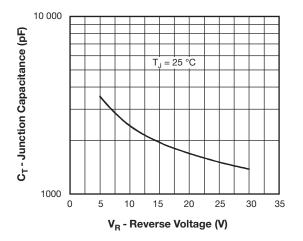


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

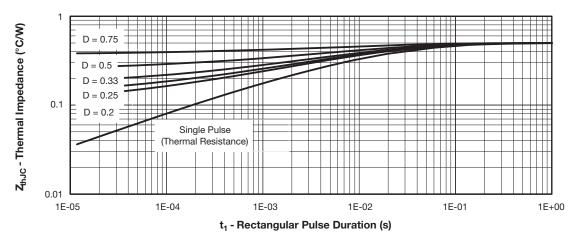
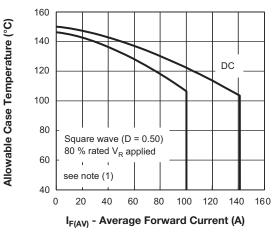
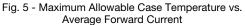


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics





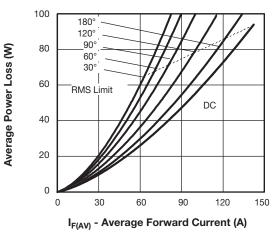


Fig. 6 - Forward Power Loss Characteristics

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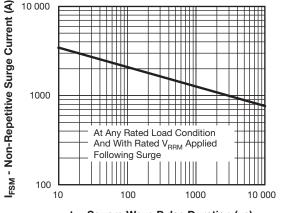
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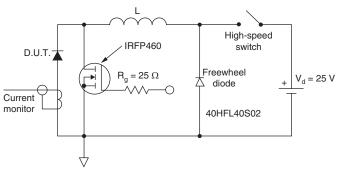
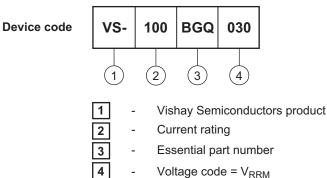


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(2)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward power loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \; \mathsf{at} \; (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \; (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse power loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \; (\mathsf{1} - \mathsf{D}); \; \mathsf{I}_{\mathsf{R}} \; \mathsf{at} \; \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \; \% \; \mathsf{rated} \; \mathsf{V}_{\mathsf{R}} \\ \end{array}$ 

#### **ORDERING INFORMATION TABLE**



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95240			
Part marking information	www.vishay.com/doc?95370			
Application note	www.vishay.com/doc?95179			

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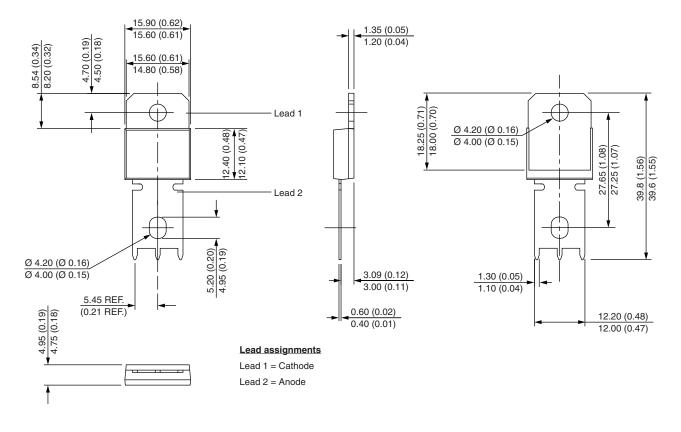
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#### **DIMENSIONS** in millimeters (inches)





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