Vishay Semiconductors

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High Performance Schottky Rectifier, 1.0 A

Anode



SMB

PRODUCT SUMMARY				
Package	SMB			
I _{F(AV)}	1.0 A			
V _R	60 V			
V _F at I _F	0.6 V			
I _{RM}	5.0 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Single die			
E _{AS}	2.0 mJ			

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\mathrm{C}$
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10BQ060PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS				
I _{F(AV)}	Rectangular waveform	1.0	А			
V _{RRM}		60	V			
I _{FSM}	t _p = 5 μs sine	700	А			
V _F	1.0 A _{pk} , T _J = 125 °C	0.57	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-10BQ060PbF	UNITS			
Maximum DC reverse voltage	V _R	60	V			
Maximum working peak reverse voltage	V _{RWM}	80	v			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS			
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 103 °C,	50 % duty cycle at $T_L = 103$ °C, rectangular waveform		А	
Maximum peak one cycle	1	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	700	A	
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	42		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 4 mH		2.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 1.0		А		

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COMPLIANT



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A	T ₁ = 25 °C	0.6	v
		2 A	1j=25 C	0.76	
		1 A	T 105 %O	0.57	
		2 A	T _J = 125 °C	0.69	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	0.1	mA
See fig. 2		T _J = 125 °C	$v_{\rm R}$ = naleu $v_{\rm R}$	5.0	
Typical junction capacitance	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		62	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of charge	dV/dt	Rate	10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	0/10
			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar DO-214AA)	V1	Н

Notes

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

(2) Mounted 1" square PCB

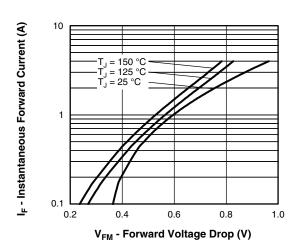


Fig. 1 - Maximum Forward Voltage Drop Characteristics

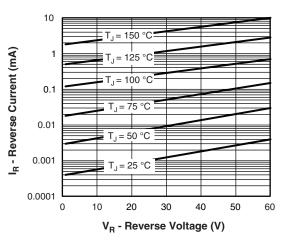


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

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VS-10BQ060PbF

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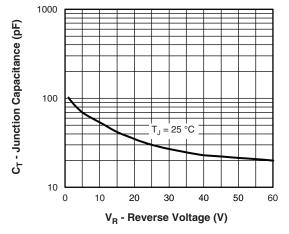


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

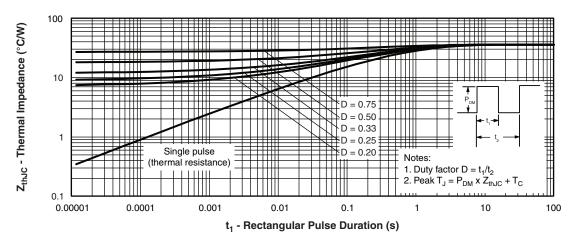


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

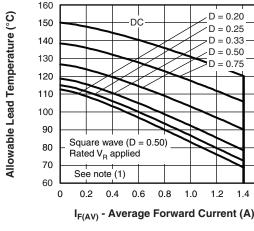
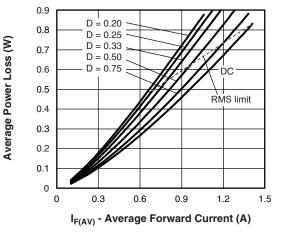


Fig. 5 - Maximum Average Forward Current vs. Allowable Lead Temperature

1.4

1.6





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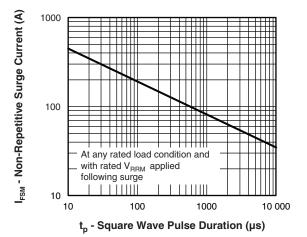


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

ORDERING INFORMATION TABLE

Device code	VS-	10	в	Q	060	TR	PbF
		2	3	4	5	6	7
	1 - 2 - 3 - 4 -	Cur B =	nay Serr rent rati single le Schottk	ng ead dioc	le	oduct	
	5 -		tage rati	-)	
	6 -		one = be R = tape		•)
	7 -		= lead			picces)

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95017				
Part marking information		www.vishay.com/doc?95029		
Deckeding information	Tape and reel	www.vishay.com/doc?95034		
Packaging information	Bulk	www.vishay.com/doc?95397		

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

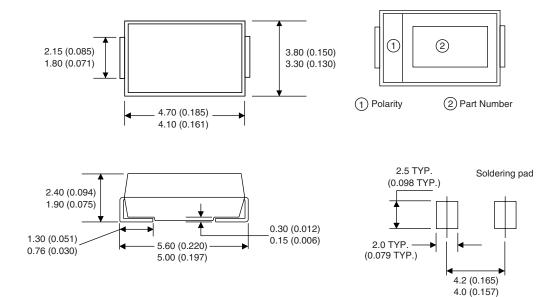


Outline Dimensions

Vishay High Power Products

SMB

DIMENSIONS in millimeters (inches)





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