

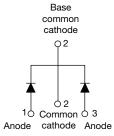
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Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 8 A

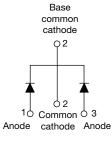
VS-16CTQ...SPbF











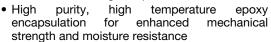
D²PAK

TO-262

| PRODUCT SUMMARY | |
|----------------------------------|---|
| Package | TO-263AB (D ² PAK), TO-262AA |
| I _{F(AV)} | 2 x 8 A |
| V _R | 60 V to 100 V |
| V _F at I _F | 0.58 V |
| I _{RM} | 7 mA at 125 °C |
| T _J max. | 175 °C |
| Diode variation | Common cathode |
| E _{AS} | 7.5 mJ |

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- Low forward voltage drop





ROHS COMPLIANT HALOGEN

FREE

• High frequency operation

- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | | |
|-----------------------------------|--|------------|-------|--|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | | |
| I _{F(AV)} | Rectangular waveform | 16 | A | | | |
| V _{RRM} | | 60 to 100 | V | | | |
| I _{FSM} | $t_p = 5 \mu s sine$ | 850 | A | | | |
| V _F | 8 Apk, T _J = 125 °C (per leg) | 0.58 | V | | | |
| T _J | Range | -55 to 175 | °C | | | |

| VOLTAGE RATINGS | | | | | | |
|--|----------------|----|----|-----|---|--|
| PARAMETER SYMBOL VS-16CTQ060SPbF VS-16CTQ100SPbF VS-16CTQ100SPbF VS-16CTQ100SPbF VS-16CTQ100-1PbF VS-16CTQ100-1Pb | | | | | | |
| Maximum DC reverse voltage | V _R | 60 | 80 | 100 | W | |
| Maximum working peak reverse voltage | V_{RWM} | 00 | 00 | 100 | V | |

| ABSOLUTE MAXIMUM RATINGS | | | | | | |
|--|--------------------|---|---|--------|-------|--|
| PARAMETER | SYMBOL | TEST COND | ITIONS | VALUES | UNITS | |
| Maximum average per leg | | | | 8 | | |
| forward current See fig. 5 per device | I _{F(AV)} | 50 % duty cycle at T _C = 148 °C | 0 % duty cycle at T _C = 148 °C, rectangular waveform | | Α | |
| Maximum peak one cycle | | 5 μs sine or 3 μs rect. pulse | Following any rated load | 850 | | |
| non-repetitive surge current per leg See fig. 7 | I _{FSM} | 10 ms sine or 6 ms rect. pulse condition and with rated V _{RRM} applied | | 275 | А | |
| Non-repetitive avalanche energy per leg | E _{AS} | T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH | | 7.50 | mJ | |
| Repetitive avalanche current per leg | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical | | 0.50 | А | |



VS-16CTQ...SPbF, VS-16CTQ...-1PbF Series

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| ELECTRICAL SPECIFICATIONS | | | | | |
|---|--------------------------------|---|---------------------------------------|--------|------|
| PARAMETER | SYMBOL | TEST CO | VALUES | UNITS | |
| | | 8 A | T _{.1} = 25 °C | 0.72 | V |
| Maximum forward voltage drop per leg | V _{FM} ⁽¹⁾ | 16 A | 1j=25 C | 0.88 | |
| See fig. 1 | V _{FM} (1) | 8 A | T 105 °C | 0.58 | |
| | | 16 A | T _J = 125 °C | 0.69 | |
| Maximum reverse leakage current per leg | I _{RM} ⁽¹⁾ | T _J = 25 °C | V _R = Rated V _R | 0.55 | mA |
| See fig. 2 | | T _J = 125 °C | | 7.0 | |
| Threshold voltage | V _{F(TO)} | T T manyimum | | 0.415 | V |
| Forward slope resistance | r _t | $T_J = T_J$ maximum | | 11.07 | mΩ |
| Maximum junction capacitance per leg | C _T | V _R = 5 V _{DC} (test signal ran | ge 100 kHz to 1 MHz), 25 °C | 500 | pF |
| Typical series inductance per leg | L _S | Measured lead to lead 5 n | 8.0 | nH | |
| Maximum voltage rate of change | dV/dt | Rated V _R | | 10 000 | V/µs |

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | | |
|--|---------|-----------------------------------|--------------------------------------|------------|------------|
| PARAMETER Maximum junction and storage temperature range | | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| | | T _J , T _{Stg} | | -55 to 175 | °C |
| Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package | | J | DC operation | 3.25 | |
| | | R_{thJC} | DC operation | 1.63 | °C/W |
| Typical thermal resistance, case to heatsink | | R _{thCS} | Mounting surface, smooth and greased | 0.50 | |
| Annyovimata waisht | | | | 2 | g |
| Approximate weight | | | | 0.07 | oz. |
| Manustina taurus | minimum | | | 6 (5) | kgf · cm |
| Mounting torque - | maximum | | | 12 (10) | (lbf · in) |
| Marking davise | | | Case style D ² PAK | 16CT | QS |
| Marking device | | | Case style TO-262 | 16CT | Q1 |



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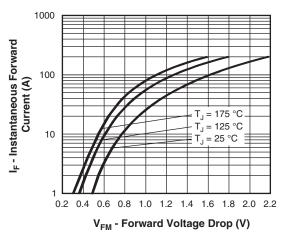


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

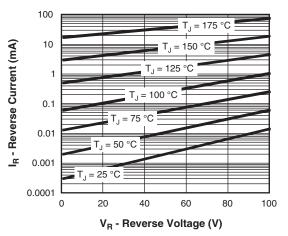


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

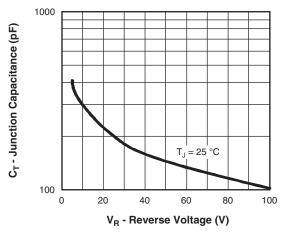


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

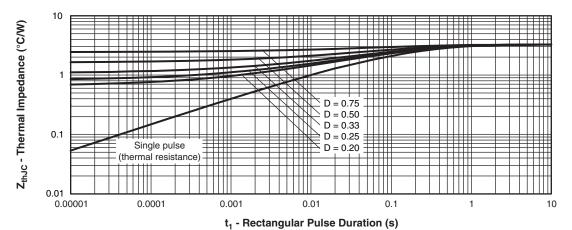


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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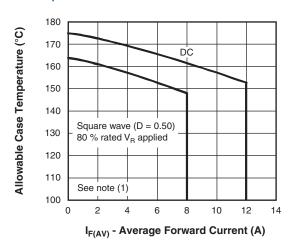


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

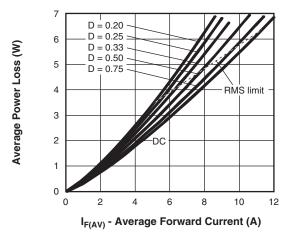


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

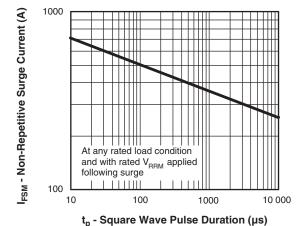


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

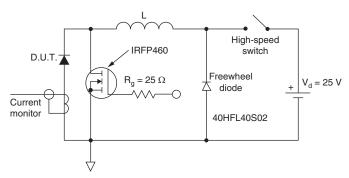


Fig. 8 - Unclamped Inductive Test Circuit

Note

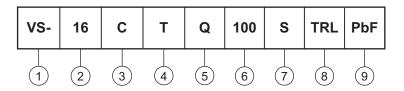
[1] Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse$ power loss $= V_{R1} \times I_{R}$ (1 - D); I_{R} at $V_{R1} = 80$ % rated V_{R} applied

VS-16CTQ...SPbF, VS-16CTQ...-1PbF Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product suffix

Current rating (16 A)

3 Circuit configuration: C = Common cathode

T = TO-220

Schottky "Q" series

060 = 60 V V = 080Voltage ratings

• $S = D^2PAK$

• -1 = TO-262

8 • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

100 = 100 V

• TRR = Tape and reel (right oriented - for D²PAK only)

9 PbF = Lead (Pb)-free

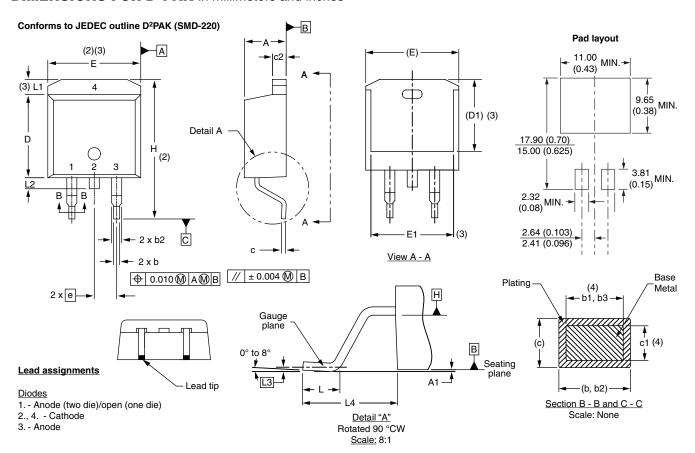
| LINKS TO RELATED DOCUMENTS | | | | | |
|--|--------------------------|--|--|--|--|
| Dimensions <u>www.vishay.com/doc?95014</u> | | | | | |
| Part marking information | www.vishay.com/doc?95008 | | | | |
| Packaging information | www.vishay.com/doc?95032 | | | | |
| SPICE model | www.vishay.com/doc?95279 | | | | |



Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches



| 0)//// | MILLIM | IETERS | INC | NOTEO | |
|--------|--------|--------|-------|-------|-------|
| SYMBOL | MIN. | MAX. | MIN. | MAX. | NOTES |
| Α | 4.06 | 4.83 | 0.160 | 0.190 | |
| A1 | 0.00 | 0.254 | 0.000 | 0.010 | |
| b | 0.51 | 0.99 | 0.020 | 0.039 | |
| b1 | 0.51 | 0.89 | 0.020 | 0.035 | 4 |
| b2 | 1.14 | 1.78 | 0.045 | 0.070 | |
| b3 | 1.14 | 1.73 | 0.045 | 0.068 | 4 |
| С | 0.38 | 0.74 | 0.015 | 0.029 | |
| c1 | 0.38 | 0.58 | 0.015 | 0.023 | 4 |
| c2 | 1.14 | 1.65 | 0.045 | 0.065 | |
| D | 8.51 | 9.65 | 0.335 | 0.380 | 2 |

| SYMBOL | MILLIM | MILLIMETERS | | INCHES | | |
|--------|----------|-------------|-----------|--------|-------|--|
| | MIN. | MAX. | MIN. | MAX. | NOTES | |
| D1 | 6.86 | 8.00 | 0.270 | 0.315 | 3 | |
| E | 9.65 | 10.67 | 0.380 | 0.420 | 2, 3 | |
| E1 | 7.90 | 8.80 | 0.311 | 0.346 | 3 | |
| е | 2.54 BSC | | 0.100 BSC | | | |
| Н | 14.61 | 15.88 | 0.575 | 0.625 | | |
| L | 1.78 | 2.79 | 0.070 | 0.110 | | |
| L1 | - | 1.65 | - | 0.066 | 3 | |
| L2 | 1.27 | 1.78 | 0.050 | 0.070 | | |
| L3 | 0.25 BSC | | 0.010 | BSC | | |
| L4 | 4.78 | 5.28 | 0.188 | 0.208 | | |

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

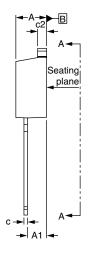
(7) Outline conforms to JEDEC outline TO-263AB

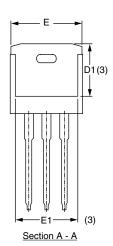
Vishay High Power Products

D²PAK, TO-262



DIMENSIONS FOR TO-262 in millimeters and inches





⊕ 0.010**⋒**|A**⋒**|B

Lead assignments

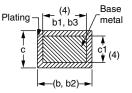


<u>Diodes</u>

-3 x b2 --3 x b

> 1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

| OVMBOL | MILLIMETERS | | INC | INCHES | | |
|--------|-------------|-------|-------|--------|-------|--|
| SYMBOL | MIN. | MAX. | MIN. | MAX. | NOTES | |
| Α | 4.06 | 4.83 | 0.160 | 0.190 | | |
| A1 | 2.03 | 3.02 | 0.080 | 0.119 | | |
| b | 0.51 | 0.99 | 0.020 | 0.039 | | |
| b1 | 0.51 | 0.89 | 0.020 | 0.035 | 4 | |
| b2 | 1.14 | 1.78 | 0.045 | 0.070 | | |
| b3 | 1.14 | 1.73 | 0.045 | 0.068 | 4 | |
| С | 0.38 | 0.74 | 0.015 | 0.029 | | |
| c1 | 0.38 | 0.58 | 0.015 | 0.023 | 4 | |
| c2 | 1.14 | 1.65 | 0.045 | 0.065 | | |
| D | 8.51 | 9.65 | 0.335 | 0.380 | 2 | |
| D1 | 6.86 | 8.00 | 0.270 | 0.315 | 3 | |
| E | 9.65 | 10.67 | 0.380 | 0.420 | 2, 3 | |
| E1 | 7.90 | 8.80 | 0.311 | 0.346 | 3 | |
| е | 2.54 BSC | | 0.10 | 0 BSC | | |
| L | 13.46 | 14.10 | 0.530 | 0.555 | | |
| L1 | - | 1.65 | - | 0.065 | 3 | |
| L2 | 3.56 | 3.71 | 0.140 | 0.146 | | |

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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Vishay

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