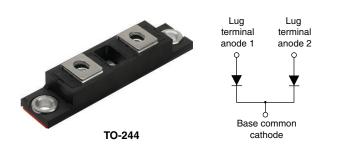
Vishay Semiconductors

High Performance Schottky Rectifier, 200 A



200 A

45 V

TO-244

Two diodes common cathode

- 150 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION	V
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The VS-200CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UN						
I _{F(AV)}	Rectangular waveform	200	А				
V _{RRM}		45	V				
I _{FSM}	t _p = 5 μs sine	26 000	А				
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.52	V				
Тј	Range	-55 to 150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-200CNQ045PbF	UNITS			
Maximum DC reverse voltage	V _R	45	V			
Maximum working peak reverse voltage	V _{RWM}	45	v			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDI	VALUES	UNITS		
Maximum average	per leg				100	٨	
forward current See fig. 5	per device	IF(AV)	$I_{F(AV)}$ 50 % duty cycle at T _C = 116 °C, rectangular waveform 200	200	A		
Maximum peak one cycle non-repetitive surge current per leg			5 µs sine or 3 µs rect. pulse Following any rated load condition and with		26 000	А	
See fig. 7	rent per leg	IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	1550	A	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 17 A, L = 1 mH		135	mJ	
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		20	А	

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1



COMPLIANT



PRODUCT SUMMARY

I_{F(AV)}

 V_{R}

Package

Circuit



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VALUES

0.55

0.52

UNITS

٧

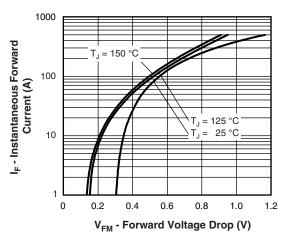
	ELECTRICAL SPECIFICATIONS						
	PARAMETER	SYMBOL	DL TEST CONDITIONS				
			100 A	T _{.1} = 25 °C			
	Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	200 A	1j=25 C			
			100 A	T.I = 125 °C			
			200 A	$I_{\rm J} = 125$ C			
ĺ	Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B			

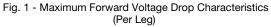
		200 A	1j = 120 0	0.69		
Maximum reverse leakage current per leg	$I_{\rm RM}$ ⁽¹⁾ $T_{\rm J} = 25 ^{\circ}{\rm C}$	I (1)	T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	10	mA
See fig. 2	'RM \''	T _J = 125 °C	VR - Haleu VR	800	ША	
Threshold voltage	V _{F(TO)}	$T_{i} = T_{i}$ maximum		0.27	V	
Forward slope resistance	r _t	i j = i j maximum	2.0	mΩ		
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal ran	5200	pF		
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

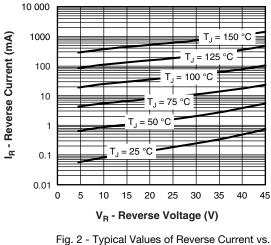
Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temper	ature range	T _J , T _{Stg}	- 55	-	150	°C	
per leg		Р	-	-	0.38		
Thermal resistance, junction to case	per module	R _{thJC}	-	-	0.19	°C/W	
Thermal resistance, case to heatsink	sistance, case to heatsink		-	0.10	-		
Weight			_	68	_	g	
			-	2.4	-	oz.	
Mounting torque			35.4 (4)	-	53.1 (6)		
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf · in (N · m)	
Terminal torque	nal torque 30 (3.4)		30 (3.4)	-	44.2 (5)	(
Vertical pull			-	-	80	lbf ⋅ in	
2" lever pull			_	-	35	ni · Tui	







Reverse Voltage (Per Leg)

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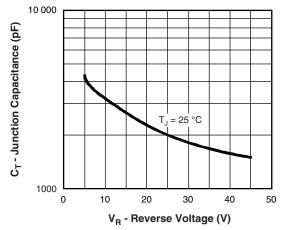


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

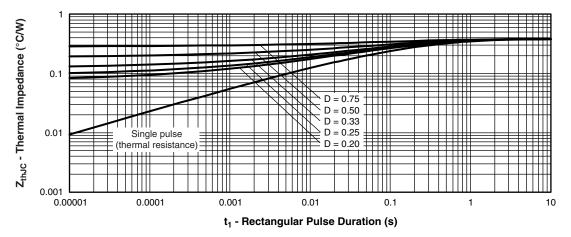
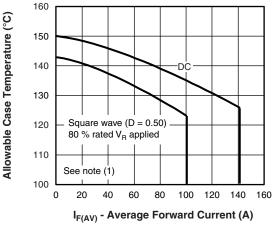
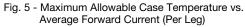


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)





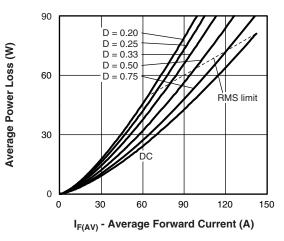


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

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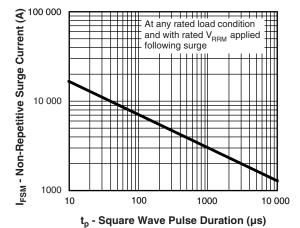


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

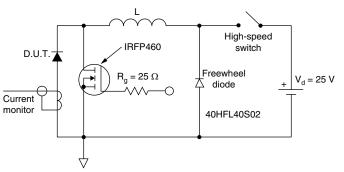


Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; $I_R at V_{R1} = 80 \%$ rated V_R

ORDERING INFORMATION TABLE

Device code	VS-	20	0	С	Ν	Q	045	PbF
		2	3	4	5	6	7	8
	 Vishay Semiconductors product Average current rating (x 10) 							
	3 - Product silicon identification							
	4 - C = Circuit configuration							
	5 - N = Not isolated							
	6 - Q = Schottky rectifier diode							
	7 - Voltage rating (045 = 45 V)							
	8 -	Lea	d (Pb)-f	ree				

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95021						
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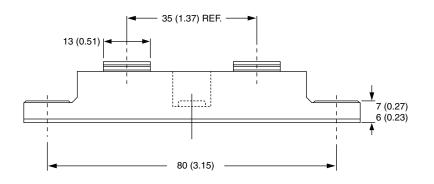


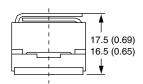
Outline Dimensions

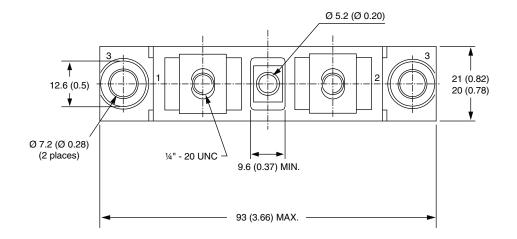
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TO-244

DIMENSIONS in millimeters (inches)









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