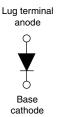
Vishay Semiconductors

High Performance Schottky Rectifier, 240 A



www.vishay.com



PRODUCT SUMMARY			
I _{F(AV)}	240 A		
V _R	45 V		
Package	HALF-PAK (D-67)		
Circuit	Single diode		

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- · High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-240NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	240	A		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 μs sine	26 000	А		
V _F	240 Арк, T _J = 125 °C	0.64	V		
TJ	Range	-55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-240NQ045PbF	UNITS	
Maximum DC reverse voltage	V _R	45	V	
Maximum working peak reverse voltage	V _{RWM}	45	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 104 °C, rectangular waveform		240	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	26 000	A
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	3400	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 18 A, L = 1 mH		162	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 48		48	А



COMPLIANT

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	240 A	T _J = 25 °C	0.72	V
Maximum forward voltage drop		480 A		1.04	
See fig. 1		240 A	T _J = 125 °C	0.64	
		480 A		0.97	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	20	mA
See fig. 2		T _J = 125 °C		1120	
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 $^{\circ}\mathrm{C}$		14 800	pF
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 500 μs

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storag	e	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05	
A				30	g
Approximate weight	Approximate weight			1.06	oz.
Mounting torque	minimum			3 (26.5)	
Mounting torque maximum				4 (35.4)	N⋅m
Terresident terreside	minimum		Non-lubricated threads	3.4 (30)	(lbf ∙ in)
Terminal torque	maximum			5 (44.2)	
Case style				HALF-PAK	K module

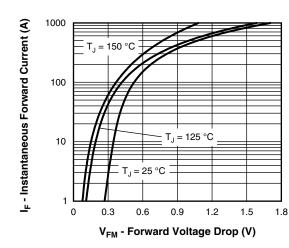


Fig. 1 - Maximum Forward Voltage Drop Characteristics

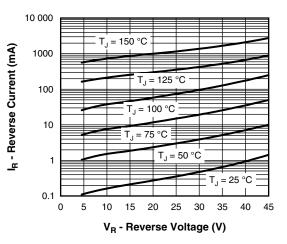


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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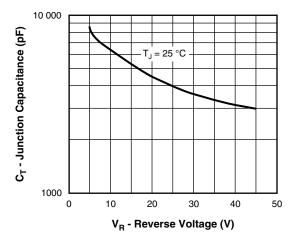


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

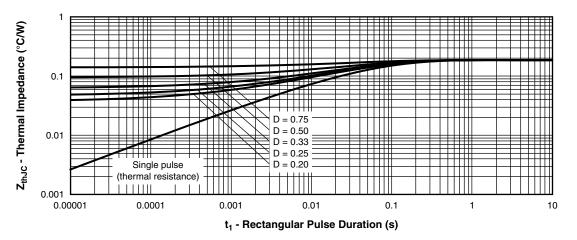
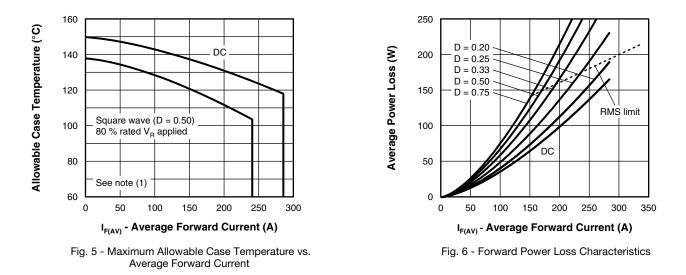


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



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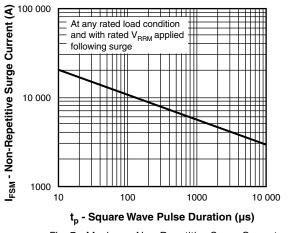


Fig. 7 - Maximum Non-Repetitive Surge Current

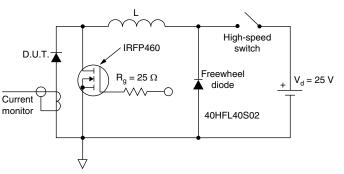
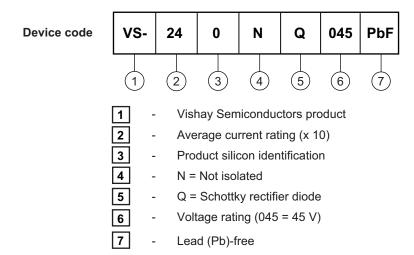


Fig. 8 - Unclamped Inductive Test Circuit

Note

ORDERING INFORMATION TABLE



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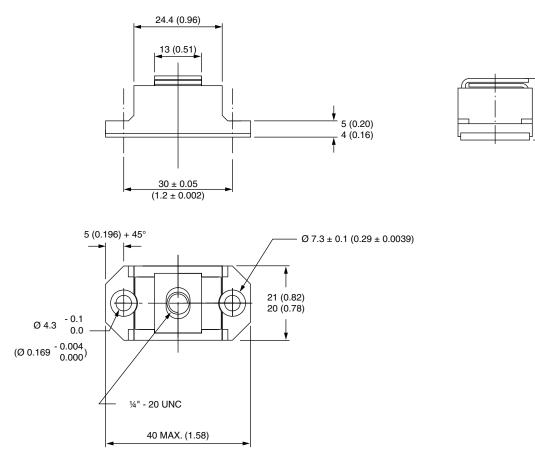
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17.5 (0.69) 16.5 (0.65)



DIMENSIONS in millimeters (inches)

SHAY





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