

Molding Type Module IGBT, 2-in-1 Package, 1200 V and 100 A



DUCT SUMMARY	
Voce	1200 V

PRODUCT SUMMARY	
V _{CES}	1200 V
I _C at T _C = 80 °C	100 A
$V_{CE(on)}$ (typical) at $I_C = 100 \text{ A}, 25 ^{\circ}\text{C}$	1.90 V
Package	Double INT-A-PAK
Circuit	Half bridge

FEATURES

- Low V_{CE(on)} SPT + IGBT technology
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL APPLICATIONS

- · Inverter for motor drive
- · AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	V	
Collector current		T _C = 25 °C	200		
Collector current	I _C	T _C = 80 °C	100		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	А	
Diode continuous forward current	I _F	T _C = 80 °C	100		
Diode maximum forward current	I _{FM}	t _p = 1 ms	200		
Maximum power dissipation	P _D	T _J = 150 °C	833	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: Pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	MBOL TEST CONDITIONS MIN. TYP.		MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	1.9	2.35	V
Collector to entitler voltage	$V_{CE(on)}$	V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	2.1	-]
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 4.0$ mA, $T_{J} = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA

SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	279	-	
Rise time	t _r		-	61	-	ns mJ
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	308	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	205	-	
Turn-on switching loss	E _{on}		-	5.56	-	
Turn-off switching loss	E _{off}		-	6.95	-	
Turn-on delay time	t _{d(on)}		-	287	-	- ns
Rise time	t _r		-	63	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{q} = 5.6 \Omega,$	-	328	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 125 °C	-	360	-	
Turn-on switching loss	E _{on}		-	7.85	-	
Turn-off switching loss	E _{off}	7	-	10.55	-	- mJ
Input capacitance	C _{ies}		-	8.58	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	0.60	-	nF
Reverse transfer capacitance	C _{res}		-	0.40	-	
SC data	I _{SC}	$t_{\text{SC}} \leq 10 \; \mu\text{s}, V_{\text{GE}} = 15 \; \text{V}, T_{\text{J}} = 125 \; ^{\circ}\text{C}, \\ V_{\text{CC}} = 900 \; \text{V}, V_{\text{CEM}} \leq 1200 \; \text{V}$	-	600	-	Α
Internal gate resistance	R _{GINT}		-	5.0	-	Ω
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Diode forward voltage	\/_	I _F = 100 A	T _J = 25 °C	ı	1.82	2.22	- V	
blode lorward voltage	V _F		T _J = 125 °C	ı	1.95	-		
Diode reverse recovery charge	Q _{rr}		$T_J = 25 ^{\circ}C$	-	5.5	-		
Diode reverse recovery charge			T _J = 125 °C	-	11.9	-	μC	
Diede peak reverse resource current	I _{rr}	$I_{rr} = 100 \text{ A}, V_{R} = 600 \text{ V},$ $dI/dt = -2000 \text{ A}/\mu\text{s},$ $V_{GF} = -15 \text{ V}$		T _J = 25 °C	-	85	-	^
Diode peak reverse recovery current			T _J = 125 °C	-	103	-	Α	
Diodo reverso recovery energy	E _{rec}	GE.	T _J = 25 °C	-	2.07	=	mJ	
Diode reverse recovery energy			T _J = 125 °C	ı	5.56	-	1113	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Operating junction temperature	T _J		-	-	150	°C	
Storage temperature range	T _{STG}		- 40	-	125		
Junction to case	R _{thJC}		-	-	0.150	K/W	
Diode			-	-	0.225		
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-		
Maunting toyang		Power terminal screw: M6	2.5 to 5.0)	Nies	
Mounting torque		Mounting screw: M6	3	3.0 to 5.0)	Nm	
Weight				300		g	

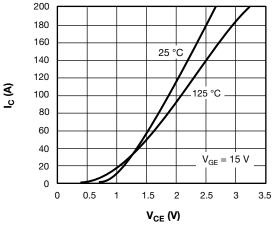


Fig. 1 - IGBT Typical Output Characteristics

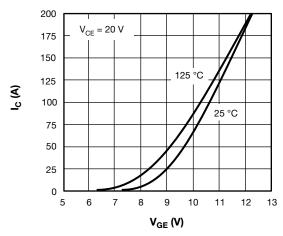


Fig. 2 - IGBT Typical Transfer Characteristics

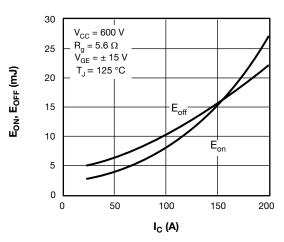


Fig. 3 - IGBT Switching Loss vs. I_{C}

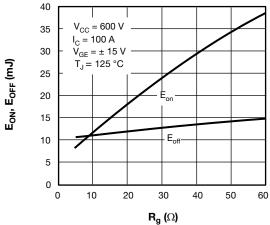


Fig. 4 - IGBT Switching Loss vs. R_a

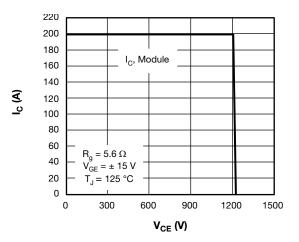


Fig. 5 - RBSOA

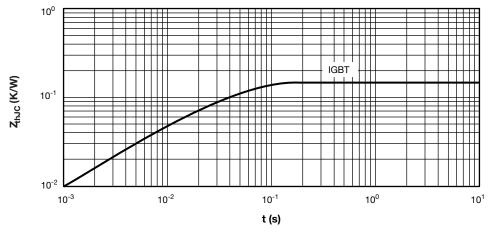


Fig. 6 - IGBT Transient Thermal Impedance

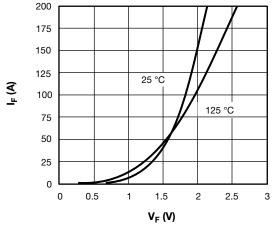


Fig. 7 - Diode Typical Forward Characteristics

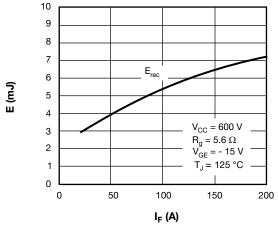


Fig. 8 - Diode Switching Loss vs. I_F

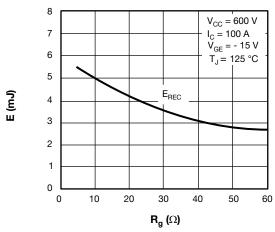


Fig. 9 - Diode Switching Loss vs. Rg

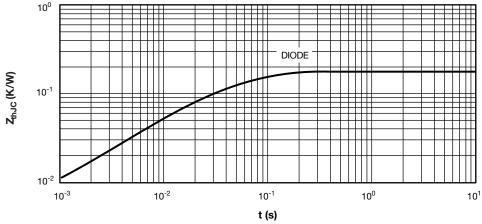
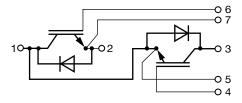


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95525			



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