

Molding Type Module IGBT, 2 in 1 Package, 1200 V, 100 A



PRODUCT SUMMARY				
V_{CES}	1200 V			
I_C at $T_C = 80$ °C	100 A			
$V_{CE(on)}$ (typical) at $I_C = 100$ A, 25 °C	3.45 V			
Package	INT-A-PAK			
Circuit	Half Bridge			

FEATURES

- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient



- Rugged with ultrafast performance
- Square RBSOA
- · Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- · Switching mode power supplies
- · Inductive heating
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
Gate to emitter voltage	V _{GES}		± 20	V
Collector current		T _C = 25 °C	150	
Collector current	llector current		100	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	200	Α
Diode continuous forward current	I _F		100	
Diode maximum forward current	I _{FM}		200	
Maximum power dissipation	P _D	T _J = 150 °C	735	W
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V
I ² t-value, diode	l ² t	V _R = 0 V, t = 10 ms, T _J = 125 °C	1700	A ² s

Note

⁽¹⁾ Repetitive rating: Pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 100 \text{ A}, T_{J} = 25 \text{ °C}$	-	3.45	3.90	V
		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	3.75	-	
Gate to emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$, $I_{C} = 1$ mA, $T_{J} = 25$ °C	4.4	5.0	6.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	2.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA

SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	310	-	ns mJ
Rise time	t _r		-	64	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 100 \text{ A}, R_{g} = 5.6 \Omega,$	-	350	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	105	-	
Turn-on switching loss	E _{on}		-	4.76	-	
Turn-off switching loss	E _{off}		-	4.25	-	
Turn-on delay time	t _{d(on)}		-	328	-	ns ns
Rise time	t _r		-	65	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V, } I_C = 100 \text{ A, } R_g = 5.6 \Omega,$ $V_{GE} = \pm 15 \text{ V, } T_J = 125 \text{ °C}$	-	350	-	
Fall time	t _f		-	132	-	
Turn-on switching loss	E _{on}	1	-	7.20	-	I
Turn-off switching loss	E _{off}	1	-	5.50	-	mJ
Input capacitance	C _{ies}		-	4.30	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz},$ $T_{.1} = 25 \text{ °C}$	-	0.40	-	nF
Reverse transfer capacitance	C _{res}	- 1,1 - 20 0	-	0.16	-	
SC data	I _{SC}	$t_{\text{SC}} \leq 10 \; \mu\text{s}, V_{\text{GE}} = 15 \; \text{V}, T_{\text{J}} = 125 \; ^{\circ}\text{C}, \\ V_{\text{CC}} = 900 \; \text{V}, V_{\text{CEM}} \leq 1200 \; \text{V}$	-	310	-	Α
Stray inductance	L _{CE}		-	-	30	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	\/	I _F = 100 A	T _J = 25 °C	ı	2.05	2.45	V
blode forward voltage	V _F		T _J = 125 °C	ı	1.95	ı	
Diada rayaraa raaayany aharaa	Q _{rr}		T _J = 25 °C	-	5.4	-	
Diode reverse recovery charge			T _J = 125 °C	-	11.2	-	μC
Diede peek veree veesten en west	I _{rr}	V _{GE} = - 15 V	T _J = 25 °C	-	81	-	^
Diode peak reverse recovery current			T _J = 125 °C	-	101	-	Α
Dia da usus sus sus sus sus sus sus sus sus s	Е		T _J = 25 °C	-	3.54	-	m l
Diode reverse recovery energy	E _{rec}		T _J = 125 °C	-	6.57	-	mJ



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature	TJ		-	-	150	°C
Storage temperature range	T _{STG}		- 40	-	125	
Junction to case IGBT (per 1/2 module)	R _{thJC}		-	-	0.17	
Diode (per 1/2 module)			-	-	0.36	K/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-	
Mounting torque		Power terminal screw: M5		2.5 to 5.0		Nm
		Mounting screw: M6	3.0 to 5.0		INIII	
Weight of module				150		g

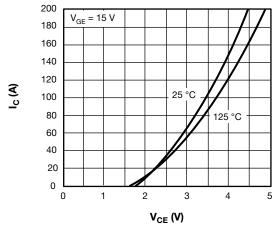


Fig. 1 - IGBT Typical Output Characteristics

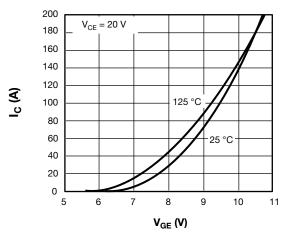


Fig. 2 - IGBT Typical Transfer Characteristics

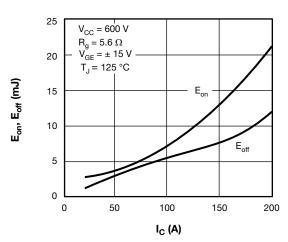


Fig. 3 - IGBT Switching Loss vs. I_C

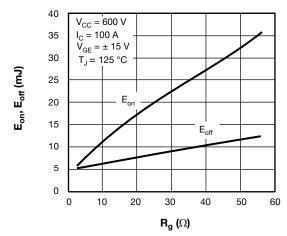


Fig. 4 - IGBT Switching Loss vs. Ra

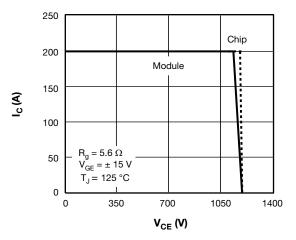


Fig. 5 - RBSOA

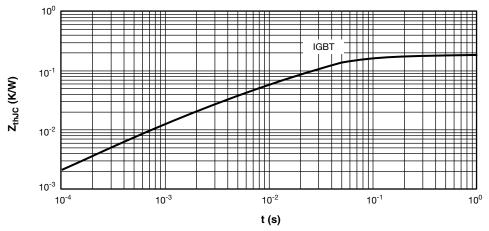


Fig. 6 - IGBT Transient Thermal Impedance

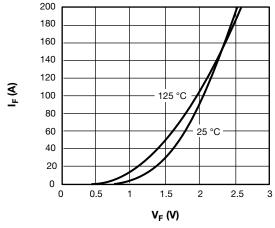


Fig. 7 - Diode Forward Characteristics

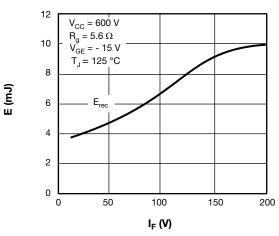


Fig. 8 - Diode Switching Loss vs. I_F

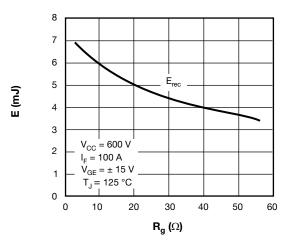


Fig. 9 - Diode Switching Loss vs. R_g

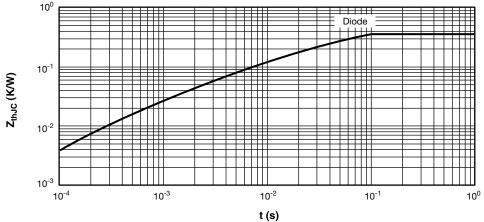
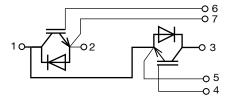


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			



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