

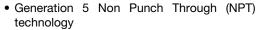
INT-A-PAK "Half-Bridge" (Ultrafast Speed IGBT), 108 A



INT-A-PA	Κ
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PRODUCT SUMMARY					
V _{CES}	600 V				
I _C DC	108 A				
V _{CE(on)} at 100 A, 25 °C	2.6 V				
Speed	8 kHz to 30 kHz				
Package	INT-A-PAK				
Circuit	Half bridge				

FEATURES





· Ultrafast: optimized for hard switching speed

ROHS

- Low V_{CE(on)}
- 10 µs short circuit capability
- Square RBSOA
- Positive V_{CE(on)} temperature coefficient
- HEXFRED® antiparallel diode with ultrasoft reverse recovery characteristics
- · Industry standard package
- Al₂O₃ DBC
- UL approved file E78996 **T**
- Designed for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Benchmark efficiency for UPS and welding application
- · Rugged transient performance
- · Direct mounting on heatsink
- · Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		600	V		
Continuous collector current		T _C = 25 °C	108			
Continuous collector current	lc	T _C = 80 °C	74	А		
Pulsed collector current	I _{CM}		200			
Clamped inductive load current	I _{LM}		200			
	I _F	T _C = 25 °C	106			
Diode continuous forward current		T _C = 80 °C	69			
Gate to emitter voltage	V _{GE}		± 20	V		
NA-view en en en elle ein ekine	D	T _C = 25 °C	390	14/		
Maximum power dissipation	P _D	T _C = 80 °C	219	W		
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Operating junction temperature range	TJ		-40 to +150	°C		
Storage temperature range	T _{Stg}		-40 to +150	.0		





ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{BR(CES)}	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	600	-	ı	
	V _{CE(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}$	-	1.95	2.1	V
Collector to emitter voltage		V _{GE} = 15 V, I _C = 100 A	-	2.6	2.85	
Collector to enlitter voltage		$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	2.21	2.44	
		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	3.05	3.38	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}, I_{C} = 500 \mu A$	3	4.6	6	
Collector to emitter leakage current	1	V _{GE} = 0 V, V _{CE} = 600 V	-	0.01	0.1	mA
Collector to enfitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V, T _J = 150 °C	-	3.7	10	IIIA
Diode forward voltage drop		I _C = 50 A	-	1.35	1.66	
	V _{FM}	I _C = 100 A	-	1.57	1.96	V
		I _C = 50 A, T _J = 125 °C	-	1.27	1.50	
		I _C = 100 A, T _J = 125 °C	-	1.57	1.89	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on switching loss	E _{on}		-	0.6	-	
Turn-off switching loss	E _{off}	$I_C = 100 \text{ A}, V_{CC} = 360 \text{ V}, V_{GE} = 15 \text{ V},$ $R_0 = 4.7 \Omega, L = 200 \mu\text{H}, T_J = 25 ^{\circ}\text{C}$	-	1.1	-	
Total switching loss	E _{tot}	1 1 1g = 4.7 32, Ε = 200 μπ, τη = 20 0	-	1.7	-	
Turn-on switching loss	E _{on}		-	0.8	-	- mJ
Turn-off switching loss	E _{off}		-	1.3	-	
Total switching loss	E _{tot}	100 4 1/ 000 1/ 15 1/	-	2.1	-	
Turn-on delay time	t _{d(on)}	$I_C = 100$ A, $V_{CC} = 360$ V, $V_{GE} = 15$ V, $R_a = 4.7$ Ω, $L = 200$ μH, $T_J = 125$ °C	-	197	-	
Rise time	t _r	- 1.ig = 1.7 ±2, Σ = 200 μπ, τη = 120 °C	-	50	-	
Turn-off delay time	t _{d(off)}		-	225	-	ns
Fall time	t _f		-	72	-	
Reverse bias safe operating area	RBSOA	$T_J = 150 ^{\circ}\text{C}, I_C = 200 \text{A}, \ R_g = 27 \Omega, V_{GE} = 15 \text{V to } 0$	Fullsquare			
Short circuit safe operating area	SCSOA	$T_J = 150 ^{\circ}\text{C}, V_{CC} = 400 \text{V}, V_P = 600 \text{V}, R_g = 27 \Omega, V_{GE} = 15 \text{V to } 0$	10	-	-	
Diode reverse recovery time	t _{rr}		-	116	140	ns
Diode peak reverse current	I _{rr}	$I_F = 50 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_{CC} = 400 \text{ V}, T_{LI} = 25 \text{ °C}$	-	11	15	Α
Diode recovery charge	Q_{rr}	100 V, 11 = 20 C	-	600	1050	nC
Diode reverse recovery time	t _{rr}		-	152	190	ns
Diode peak reverse current	I _{rr}	$I_F = 50 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s},$ $V_{CC} = 400 \text{ V}, T_{II} = 125 ^{\circ}\text{C}$	-	16	20	Α
Diode recovery charge	Q _{rr}	1 VOC = 100 V, 1J = 120 C	-	1215	1900	nC

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Operating junction and st	orage temperature range	T _J , T _{Stg}	-40	-	150	°C
Junction to case per leg	IGBT	- R _{thJC}	-	0.23	0.32	
	Diode		-	0.38	0.64	°C/W
Case to sink per module		R _{thCS}	-	0.1	-	
Mounting torque	case to heatsink		-	-	4	Nm
	case to terminal 1, 2, 3		-	-	3	
Weight			-	185	-	g

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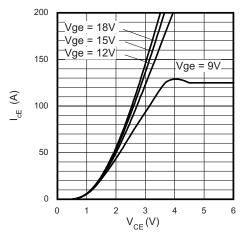


Fig. 1 - Typical IGBT Output Characteristics $T_J = 25~^{\circ}C, \, t_p = 500~\mu s$

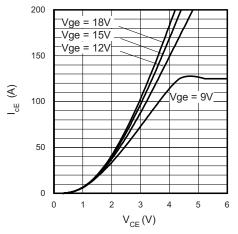


Fig. 2 - Typical IGBT Output Characteristics T_J = 125 °C, t_p = 500 μs

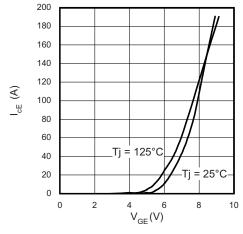


Fig. 3 - Typical Transfer Characteristics $V_{CE} = 20 \text{ V}, t_p = 500 \mu \text{s}$

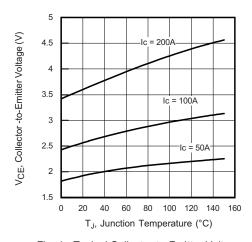


Fig. 4 - Typical Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15 \text{ V}, 500 \text{ } \mu \text{s} \text{ pulse width}$

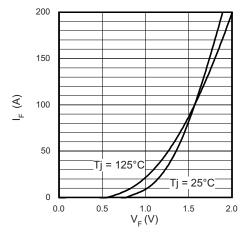


Fig. 5 - Diode Forward Characteristics, $t_p = 500 \mu s$

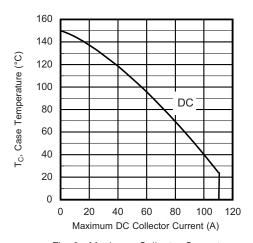


Fig. 6 - Maximum Collector Current vs.
Case Temperature

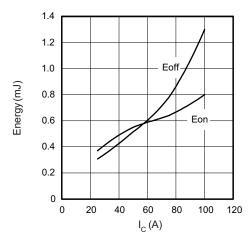


Fig. 7 - Typical Energy Loss vs. I_C, T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, R_g = 4.7 Ω , V_{GE} = 15 V

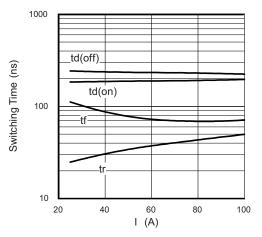


Fig. 8 - Typical Switching Time vs. I_C T_J = 125 °C, L = 200 μ H, V_{CC} = 360 V, R_q = 4.7 Ω , V_{GE} = 15 V

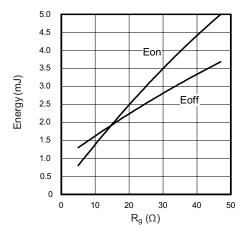


Fig. 9 - Typical Energy Loss vs. R_g $T_J = 125~^{\circ}C$, $L = 200~\mu H$, $V_{CC} = 360~V$, $I_{CE} = 100~A$, $V_{GE} = 15~V$

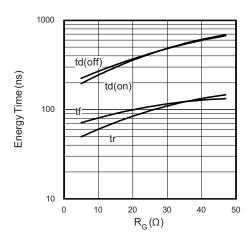


Fig. 10 - Typical Switching Time vs. R_g $T_J = 125$ °C, $L = 200~\mu H, V_{CC} = 360~V,$ $I_{CE} = 100~A, V_{GE} = 15~V$

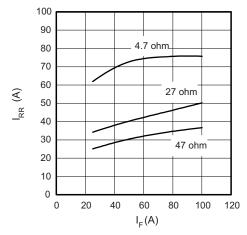


Fig. 11 - Typical Diode I_{rr} vs. I_{F} , T_{J} = 125 °C

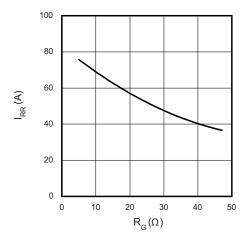


Fig. 12 - Typical Diode I_{rr} vs. R_g , $T_J = 125~{}^{\circ}\text{C}, \ I_F = 100~\text{A}$

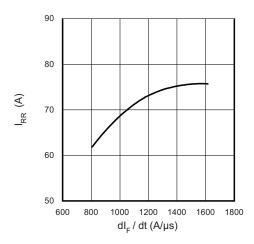


Fig. 13 - Typical Diode I $_{rr}$ vs. dI $_{F}$ /dt, T $_{J}$ = 125 °C, V $_{CC}$ = 360 V, I $_{F}$ = 150 A, V $_{GE}$ = 15 V

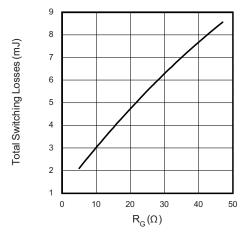


Fig. 14 - Typical Switching Losses vs. Gate Resistance, T_J = 125 °C, L = 200 μ H, R_g = 10 Ω , V_{CC} = 360 V, V_{GE} = 15 V

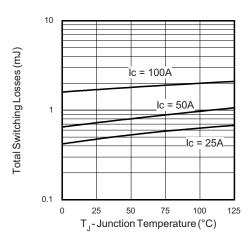


Fig. 15 - Typical Switching Losses vs. Junction Temperature, L = 200 μ H, R_g = 10 Ω , V_{CC} = 360 V, V_{GE} = 15 V

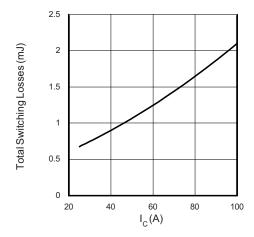


Fig. 16 - Typical Switching Losses vs. Collector to Emitter Current, $T_J = 125~^{\circ}C,~R_{g1} = 4.7~V,~R_{g2} = 0~\Omega,~V_{CC} = 360~V,~V_{GE} = 15~V$

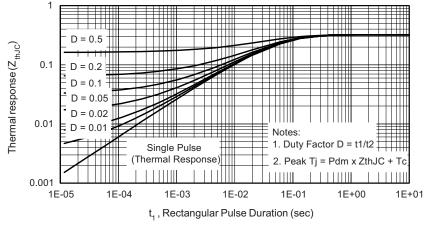


Fig. 17 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

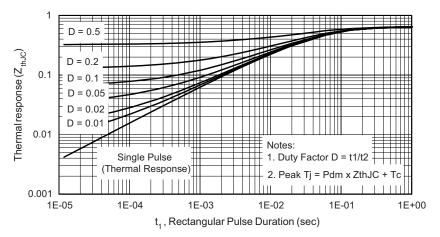
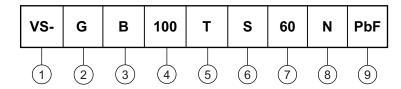


Fig. 18 - Maximum Transient Thermal Impedance, Junction to Case (HEXFRED®)

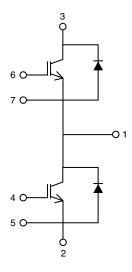
ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Insulated Gate Bipolar Transistor (IGBT)
- 3 B = IGBT Generation 5 NPT
- Current rating (100 = 100 A)
- Circuit configuration (T = Half-bridge)
- 6 Package indicator (S = INT-A-PAK)
- 7 Voltage rating (60 = 600 V)
- Speed/type (N = Ultrafast IGBT)
- 9 Lead (Pb)-free

CIRCUIT CONFIGURATION

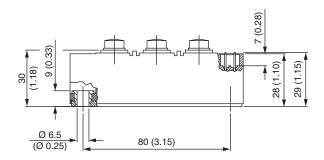


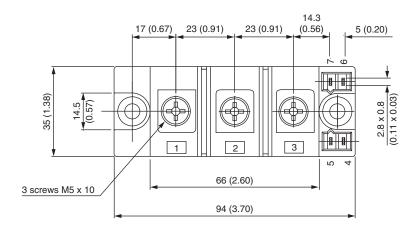
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95543		

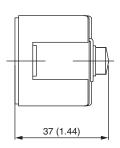


INT-A-PAK IGBT

DIMENSIONS in millimeters (inches)









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