

Molding Type Module IGBT, 2-in-1 Package, 1200 V and 150 A



PRODUCT SUMMARY					
V _{CES}	1200 V				
I _C at T _C = 80 °C	150 A				
V _{CE(on)} (typical) at I _C = 150 A, 25 °C	1.9 V				
Package	Double INT-A-PAK				
Circuit	Half bridge				

FEATURES

- Low V_{CE(on)} SPT + IGBT technology
- 10 µs short circuit capability



- Maximum junction temperature 150 °C
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- · Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
Gate to emitter voltage	V _{GES}		± 20	V
Collector ourment		T _C = 25 °C	300	
Collector current	Ic	T _C = 80 °C	150	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	300	А
Diode continuous forward current	I _F	T _C = 80 °C	150	
Diode maximum forward current	I _{FM}	t _p = 1 ms	300	
Maximum power dissipation	P _D	T _J = 150 °C	1008	W
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V

Note

⁽¹⁾ Repetitive rating: Pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V	V _{GE} = 15 V, I _C = 150 A, T _J = 25 °C - 1.90	1.90	2.35	V	
Collector to enlitter voltage	$V_{CE(on)}$	V _{GE} = 15 V, I _C = 150 A, T _J = 125 °C	-	2.10	-	V
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 6$ mA, $T_{J} = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA

SWITCHING CHARACTERISTICS	3					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	336	-	
Rise time	t _r		-	75	-	ns mJ
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 150 \text{ A}, R_{g} = 4.7 \Omega,$	-	346	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	182	-	
Turn-on switching loss	E _{on}]	-	7.25	-	
Turn-off switching loss	E _{off}		-	9.30	-	
Turn-on delay time	t _{d(on)}		-	346	-	- ns
Rise time	t _r		-	77	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 150 \text{ A}, R_{g} = 4.7 \Omega,$	-	389	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 125 \text{ °C}$	-	322	-	
Turn-on switching loss	E _{on}		-	9.95	-	m l
Turn-off switching loss	E _{off}		-	16.0	-	mJ
Input capacitance	C _{ies}		-	11.0	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	0.80	-	nF
Reverse transfer capacitance	C _{res}]	-	0.52	-	
SC data	I _{SC}	$t_{sc} \le 10 \ \mu s, \ V_{GE} = 15 \ V, \ T_J = 125 \ ^{\circ}C, \ V_{CC} = 900 \ V, \ V_{CEM} \le 1200 \ V$	-	890	-	А
Internal gate resistance	R _{GINT}		-	1.5	-	Ω
Stray inductance	L _{CE}		-	-	20	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Diode forward voltage	V _F I _F = 150 A	I 150 A	$T_J = 25 ^{\circ}C$	-	1.80	2.20	V	
blode lorward voltage		T _J = 125 °C	ı	1.85	ı]		
Diode reverse recovery charge	0	Q _{rr}	$T_J = 25 ^{\circ}C$	-	16.2	-		
Diode reverse recovery charge	Q _{rr}		T _J = 125 °C	-	26.6	-	μC	
Diada park reverse recevem event	I _{rr}		$I_F = 150 \text{ A}, V_R = 600 \text{ V},$ I_{rr} $dI/dt = -2360 \text{ A}/\mu\text{s},$	T _J = 25 °C	-	138	-	
Diode peak reverse recovery current		$V_{GF} = -15 \text{ V}$	T _J = 125 °C	-	166	-	Α	
Diada vayayaa yaaayan, anayay	E _{rec}	31	T _J = 25 °C	-	7.48	-	ml	
Diode reverse recovery energy			T _J = 125 °C	-	13.4	-	mJ	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Operating junction temperature range	TJ		-	-	150	°C	
Storage temperature range	T _{STG}		- 40	-	125		
Junction to case	Б		-	-	0.124		
Diode	R_{thJC}		-	-	0.174	K/W	
Case to sink	R _{thCS}	Conductive grease applied	-	0.035	-		
Manustina tagana		Power terminal screw: M6	2.5 to 5.0)	Nies	
Mounting torque		Mounting screw: M6	;	3.0 to 5.0)	Nm	
Weight				300		g	

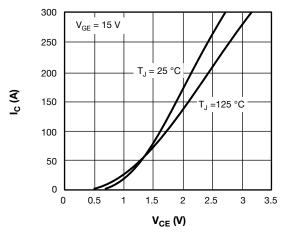


Fig. 1 - IGBT Typical Output Characteristics

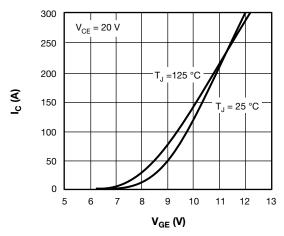


Fig. 2 - IGBT Typical Transfer Characteristics

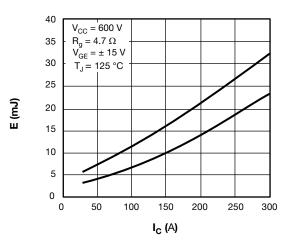


Fig. 3 - IGBT Switching Loss vs. I_C

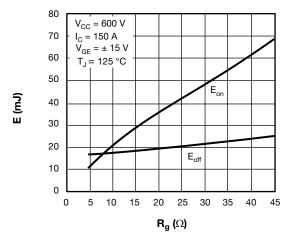


Fig. 4 - IGBT Switching Loss vs. Ra

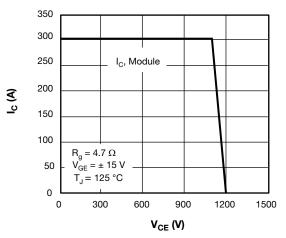


Fig. 5 - RBSOA

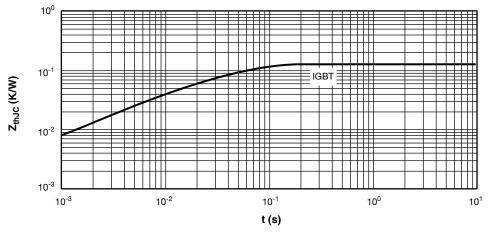


Fig. 6 - IGBT Transient Thermal Impedance

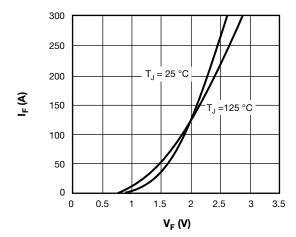


Fig. 7 - Diode Typical Forward Characteristics

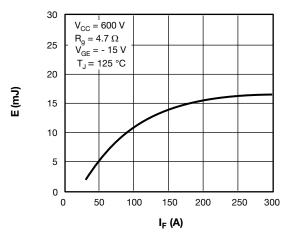


Fig. 8 - Diode Switching Loss vs. I_{F}

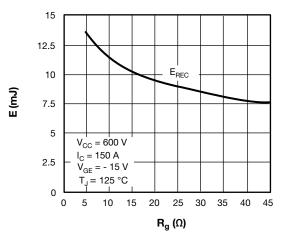


Fig. 9 - Diode Switching Loss vs.R_q

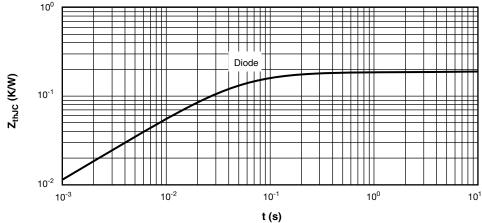
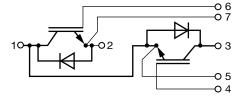


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95525			



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