## VS-GB150TS60NPbF

Vishay Semiconductors



## INT-A-PAK "Half-Bridge" (Ultrafast Speed IGBT), 138 A



INT-A-PAK

PRODUCT SUMMARY				
V <sub>CES</sub>	600 V			
I <sub>C</sub> DC	138 A			
V <sub>CE(on)</sub> at 150 A, 25 °C	2.64 V			
Package	INT-A-PAK			
Circuit	Half bridge			

#### FEATURES

Generation 5 Non Punch Through (NPT) technology



ROHS COMPLIANT

- Ultrafast: Optimized for hard switching speed 8 kHz to 60 kHz
- Low V<sub>CE(on)</sub>
- 10 µs short circuit capability
- Square RBSOA
- Positive V<sub>CE(on)</sub> temperature coefficient
- HEXFRED<sup>®</sup> antiparallel diode with ultrasoft reverse recovery characteristics
- Industry standard package
- Al<sub>2</sub>O<sub>3</sub> DBC
- UL approved file E78996 😱
- Designed for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- · Benchmark efficiency for UPS and welding application
- Rugged transient performance
- Direct mounting on heatsink
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V <sub>CES</sub>		600	V	
Continuous collector current		$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	138		
	I <sub>C</sub>	T <sub>C</sub> = 80 °C	93		
Pulsed collector current	I <sub>CM</sub>		300	^	
Clamped inductive load current	I <sub>LM</sub>		300	A	
Diode continuous forward current	١ <sub>F</sub>	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	178		
		T <sub>C</sub> = 80 °C	121		
Gate to emitter voltage	V <sub>GE</sub>		± 20	V	
Maximum power dissipation	Р	T <sub>C</sub> = 25 °C	500	w	
	P <sub>D</sub>	T <sub>C</sub> = 80 °C	280	vv	
Isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V	
Operating junction temperature range	TJ		-40 to +150	°C	
Storage temperature range	T <sub>Stg</sub>		-40 to +150	C	

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<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V <sub>BR(CES)</sub>	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 500 \mu\text{A}$	600	-	-		
Collector to emitter voltage	V <sub>CE(on)</sub>	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 100 \text{ A}$	-	2.2	2.7		
		V <sub>GE</sub> = 15 V, I <sub>C</sub> = 150 A	-	2.64	3	V	
		$V_{GE}$ = 15 V, I <sub>C</sub> = 100 A, T <sub>J</sub> = 125 °C	-	2.68	3.11		
		$V_{GE}$ = 15 V, I <sub>C</sub> = 150 A, T <sub>J</sub> = 125 °C	-	3.25	3.79		
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 500 \ \mu A$	3	4.2	6		
Collector to emitter leakage current	I <sub>CES</sub>	$V_{GE} = 0 V, V_{CE} = 600 V$	-	0.01	0.2	mA	
		$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 600 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$	-	7.5	15		
Diode forward voltage drop	V <sub>FM</sub>	I <sub>C</sub> = 100 A	-	1.39	1.78		
		I <sub>C</sub> = 150 A	-	1.52	1.91	v	
		I <sub>C</sub> = 100 A, T <sub>J</sub> = 125 °C	-	1.31	1.72		
		I <sub>C</sub> = 150 A, T <sub>J</sub> = 125 °C	-	1.49	2.05		
Gate to emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ± 20 V	-	-	± 200	nA	

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Turn-on switching loss	Eon		-	2.0	-		
Turn-off switching loss	E <sub>off</sub>	$I_{C}$ = 150 A, V <sub>CC</sub> = 360 V, V <sub>GE</sub> = 15 V, R <sub>q</sub> = 10 Ω, L = 200 µH, T <sub>J</sub> = 25 °C	-	3.9	-		
Total switching loss	E <sub>tot</sub>	···g ····,·· p··; · g _·· p	-	5.9	-		
Turn-on switching loss	Eon		-	2.42	-	mJ	
Turn-off switching loss	E <sub>off</sub>		-	4.2	-	1	
Total switching loss	E <sub>tot</sub>		-	6.62	-		
Turn-on delay time	t <sub>d(on)</sub>	I <sub>C</sub> = 150 A, V <sub>CC</sub> = 360 V, V <sub>GE</sub> = 15 V, R <sub>a</sub> = 10 Ω, L = 200 μH, T <sub>J</sub> = 125 °C	-	390	-		
Rise time	t <sub>r</sub>	···g ····,·· p··; · g ··· -	-	100	-		
Turn-off delay time	t <sub>d(off)</sub>		-	402	-	ns	
Fall time	t <sub>f</sub>		-	80	-		
Reverse bias safe operating area	RBSOA						
Short circuit safe operating area	SCSOA	$ \begin{array}{l} T_{J} = 150 \ ^{\circ}\text{C}, \ V_{CC} = 400 \ \text{V}, \ V_{P} = 600 \ \text{V}, \\ R_{g} = 10 \ \Omega, \ V_{GE} = 15 \ \text{V} \ \text{to} \ 0 \end{array} $	10	-	-		
Diode reverse recovery time	t <sub>rr</sub>		-	226	260	ns	
Diode peak reverse current	I <sub>rr</sub>	I <sub>F</sub> = 50 A, dI <sub>F</sub> /dt = 200 A/μs, V <sub>CC</sub> = 400 V, T <sub>J</sub> = 25 °C	-	17	20	А	
Diode recovery charge	Q <sub>rr</sub>		-	1900	2600	nC	
Diode reverse recovery time	t <sub>rr</sub>		-	290	330	ns	
Diode peak reverse current	I <sub>rr</sub>	I <sub>F</sub> = 50 A, dI <sub>F</sub> /dt = 200 A/μs, V <sub>CC</sub> = 400 V, T <sub>I</sub> = 125 °C	-	25	30	А	
Diode recovery charge	Q <sub>rr</sub>		_	3600	5000	nC	

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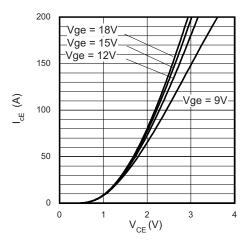
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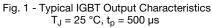
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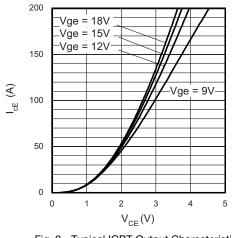


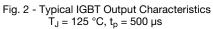
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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	- 40	-	150	°C	
Junction to case per leg	IGBT	R <sub>thJC</sub>	-	0.17	0.25	°C/W	
	Diode		-	0.19	0.32		
Case to sink per module		R <sub>thCS</sub>	-	0.1	-		
Mounting torque	case to heatsink		-	-	4	Nm	
	case to terminal 1, 2, 3		-	-	3		
Weight			-	185	-	g	









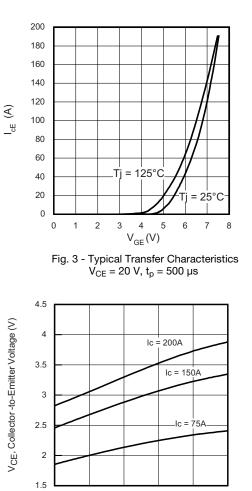


Fig. 4 - Typical Collector to Emitter Voltage vs. Junction Temperature  $V_{GE}$  = 15 V, 500 µs pulse width

T<sub>J</sub>, Junction Temperature (°C)

90

120

150

60

0

30

3



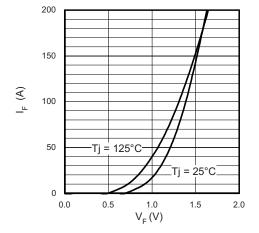


Fig. 5 - Diode Forward Characteristics,  $t_p = 500 \ \mu s$ 

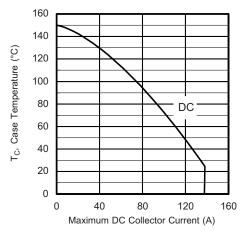
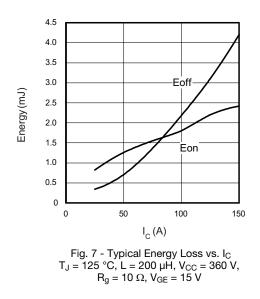
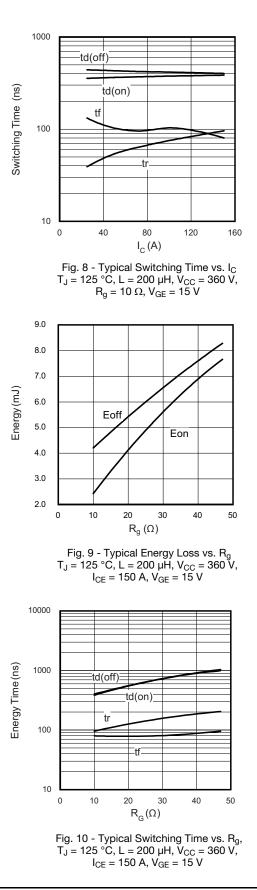


Fig. 6 - Maximum Collector Current vs. Case Temperature





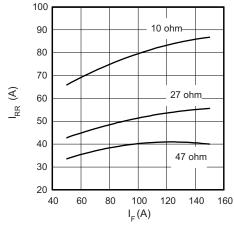
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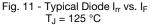
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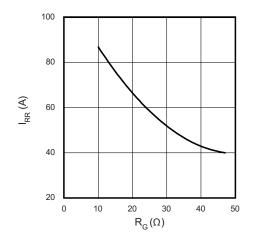
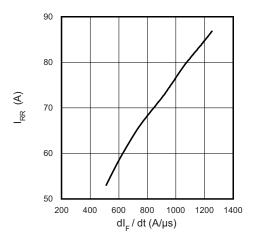
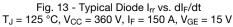


Fig. 12 - Typical Diode I<sub>rr</sub> vs. R<sub>g</sub>  $T_J$  = 125 °C, I<sub>F</sub> = 150 A





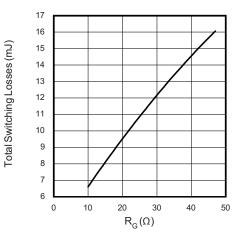


Fig. 14 - Typical Switching Losses vs. Gate Resistance,  $T_J$  = 125 °C, L = 200 µH,  $R_g$  = 10  $\Omega,$   $V_{CC}$  = 360 V,  $V_{GE}$  = 15 V

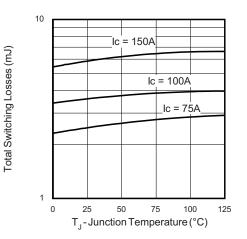
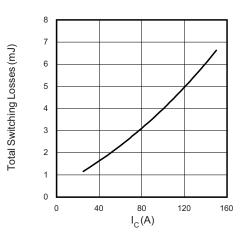
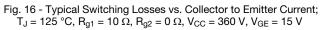


Fig. 15 - Typical Switching Losses vs. Junction Temperature; L = 200  $\mu H,\,R_g$  = 10  $\Omega,\,V_{CC}$  = 360 V,  $V_{GE}$  = 15 V





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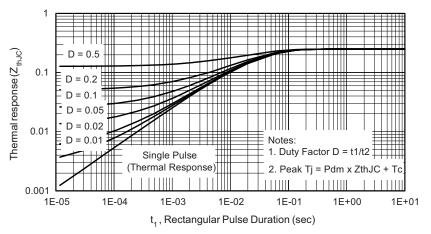


Fig. 17 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

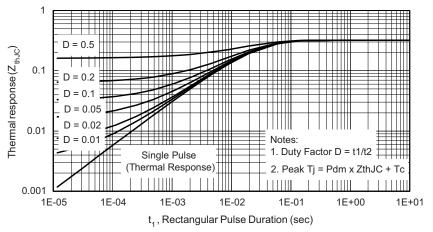
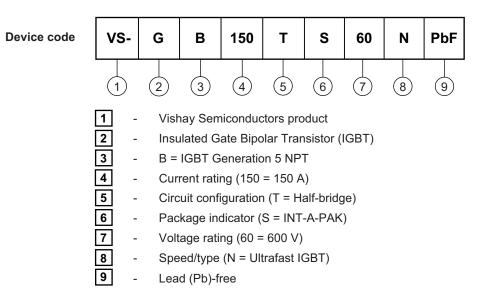


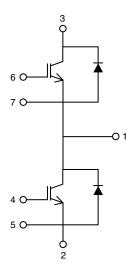
Fig. 18 - Maximum Transient Thermal Impedance, Junction to Case (HEXFRED®)



#### **ORDERING INFORMATION TABLE**



#### **CIRCUIT CONFIGURATION**



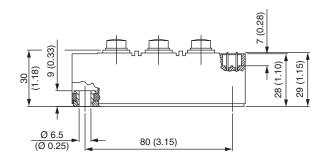
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95543			

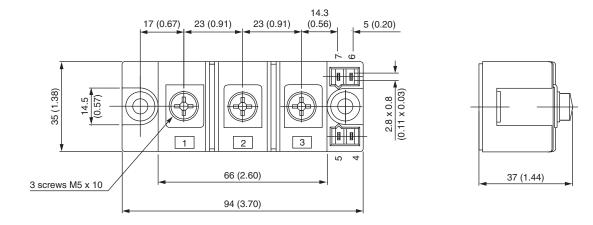




**INT-A-PAK IGBT** 

#### **DIMENSIONS** in millimeters (inches)







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