

Molding Type Module IGBT, 2-in-1 Package, 1200 V and 200 A



PRIMARY CHARACTERISTICS					
V _{CES}	1200 V				
I _C at T _C = 80 °C	200 A				
$V_{CE(on)}$ (typical) at $I_C = 200 \text{ A}, 25 ^{\circ}\text{C}$	3.10 V				
Speed	8 kHz to 30 kHz				
Package	Dual INT-A-PAK				
Circuit configuration	Half bridge				

FEATURES

- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- Low switching losses
- · Rugged with ultrafast performance
- · Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Switching mode power supplies
- · Inductive heating
- · Electronic welder

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as electronic welder and inductive heating.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS MAX.		UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20]	
Collector current		T _C = 25 °C	262		
Collector current I _C	T _C = 80 °C	200			
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	400	Α	
Diode continuous forward current	IF	T _C = 80 °C	200		
Diode maximum forward current	I _{FM}	t _p = 1 ms	400		
Maximum power dissipation	P _D	T _J = 150 °C	1315	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, I_{C} = 200 \text{ A}, T_{J} = 25 \text{ °C}$	-	3.00	3.45] _v
Collector to enlitter voltage		V _{GE} = 15 V, I _C = 200 A, T _J = 125 °C	-	3.80	-]
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 2.0$ mA, $T_{J} = 25$ °C	4.5	5.4	6.5	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I_{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA



SWITCHING CHARACTERISTICS)					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	87	-	- ns
Rise time	t _r		-	40	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 200 \text{ A}, R_{g} = 4.7 \Omega,$	-	451	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	63	-	
Turn-on switching loss	E _{on}		-	6.8	-	- mJ
Turn-off switching loss	E _{off}	1	-	11.9	-	
Turn-on delay time	t _{d(on)}		-	88	-	- ns
Rise time	t _r	$V_{CC} = 600 \text{ V}, I_{C} = 200 \text{ A}, R_{g} = 4.7 \Omega, \\ V_{GE} = \pm 15 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	44	-	
Turn-off delay time	t _{d(off)}		-	483	-	
Fall time	t _f		-	78	-	
Turn-on switching loss	E _{on}		-	11.4	-	m l
Turn-off switching loss	E _{off}	1	-	13.5	-	mJ
Input capacitance	C _{ies}		-	13.0	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 30 \text{ V}, f = 1.0 \text{ MHz}$	-	1.51	-	nF
Reverse transfer capacitance	C _{res}	1	-	0.85	-	
SC data	Isc	$t_{\text{SC}} \leq 10 \; \mu\text{s}, V_{\text{GE}} = 15 \; \text{V}, T_{\text{J}} = 125 \; ^{\circ}\text{C}, \\ V_{\text{CC}} = 900 \; \text{V}, V_{\text{CEM}} \leq 1200 \; \text{V}$	-	1300	-	А
Internal gate resistance	R _{gint}		-	1.3	-	Ω
Stray inductance	L _{CE}		-	-	30	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.35	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V _F	I _F = 200 A, V _{GE} = 0 V	$T_J = 25 ^{\circ}C$	ı	1.95	2.40	V
Diode forward voltage			T _J = 125 °C	-	2.00	-	
Diode reverse recovery charge	0	Q _{rr}	T _J = 25 °C	-	13.3	-	
Diode reverse recovery charge	Q _{rr}		T _J = 125 °C	-	23.0	-	μC
Diada paak vayawa vaasyaw ayyawt	I _{rr}	$I_F = 200 \text{ A, } V_R = 600 \text{ V,}$ $dI/dt = 4600 \text{ A/}\mu\text{s,}$ $V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	236	-	^
Diode peak reverse recovery current			T _J = 125 °C	-	269	-	Α
Diada variavas vasariam anavari	E		T _J = 25 °C	-	6.6	-	m l
Diode reverse recovery energy	E _{rec}		T _J = 125 °C	-	10.5	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction temperature		T_J		-	-	150	
Operating junction temperature	range	T_J		-40	-	125	°C
Storage temperature range		T _{STG}		-40	-	125	
Junction to case	IGBT	R _{thJC}		-	-	0.095	
Junction to case	Diode			-	-	0.202	
	IGBT	R _{thCS}		-	0.029	-	K/W
Case to heatsink	Diode			-	0.063	-	
Module			-	0.010	-		
Mounting torque			Power terminal screw: M5	2.5	-	5.0	Nm
			Mounting screw: M6	3.0	-	5.0	INIII
Weight				-	300	-	g



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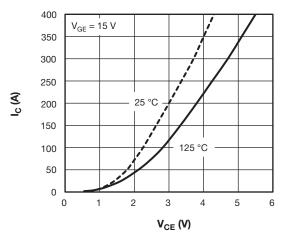


Fig. 1 - IGBT Typical Output Characteristics

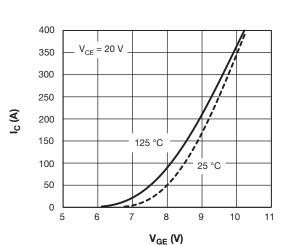


Fig. 2 - IGBT Typical Transfer Characteristics

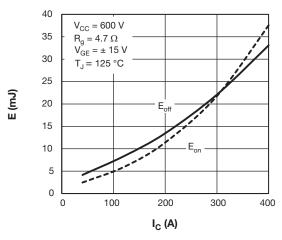


Fig. 3 - IGBT Switching Loss vs. I_C

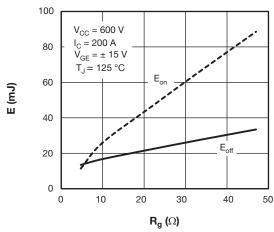


Fig. 4 - IGBT Switching Loss vs. R_q

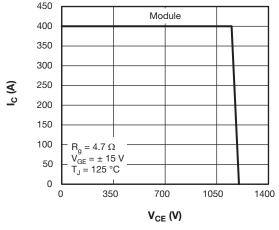


Fig. 5 - RBSOA

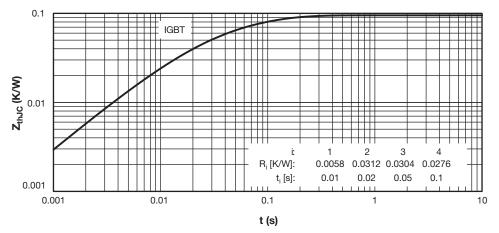
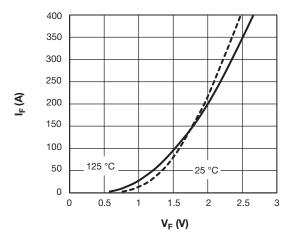


Fig. 6 - IGBT Transient Thermal Impedance





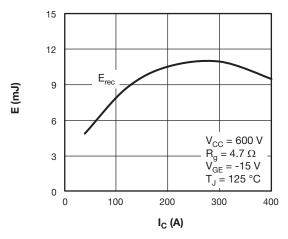


Fig. 8 - Diode Switching Loss vs. I_F

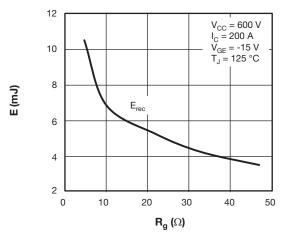


Fig. 9 - Diode Switching Loss vs. Gate Resistance



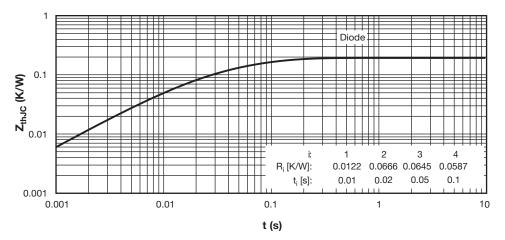
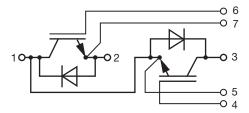


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION

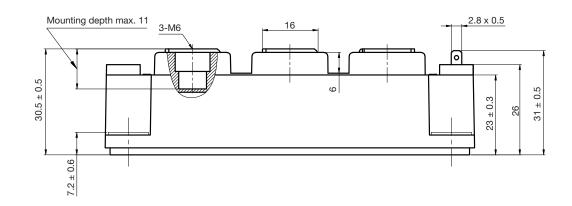


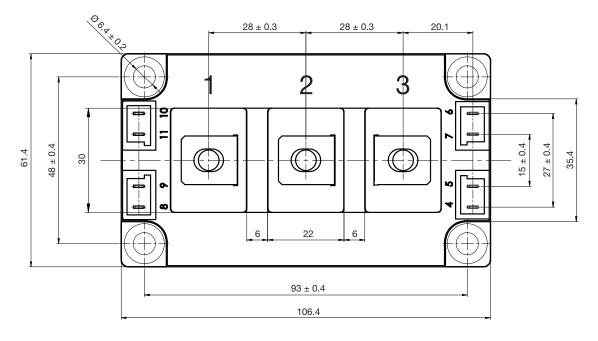
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95525			



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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