

Molding Type Module IGBT, 2 in 1 Package, 1200 V, 50 A



PRODUCT SUMMARY					
V_{CES}	1200 V				
I _C at T _C = 80 °C	50 A				
$V_{CE(on)}$ (typical) at $I_C = 50$ A, 25 °C	1.75 V				
Package	INT-A-PAK				
Circuit	Half Bridge				

FEATURES





- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Maximum junction temperature 150 °C
- · Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

- · AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 20	V	
Collector current		T _C = 25 °C	100		
Collector current I _C	T _C = 80 °C	50			
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	100	А	
Diode continuous forward current	I _F		50		
Diode maximum forward current	I _{FM}		100		
Maximum power dissipation	P _D	T _J = 150 °C	446	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	
l ² t-value, diode	l ² t	$V_R = 0 \text{ V}, \text{ t} = 10 \text{ ms}, T_J = 125 ^{\circ}\text{C}$	420	A ² s	

Note

⁽¹⁾ Repetitive rating: Pulse width limited by maximum junction temperature.



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 1.0 \text{ mA}, T_{J} = 25 ^{\circ}\text{C}$	1200	-	-	
Collector to emitter voltage	V	$V_{GE} = 15 \text{ V}, I_{C} = 50 \text{ A}, T_{J} = 25 \text{ °C}$	-	1.75	2.15	V
Collector to enlitter voltage	$V_{CE(on)}$	V _{GE} = 15 V, I _C = 50 A, T _J = 125 °C	-	2.0	-	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Gate to emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$, $I_{C} = 2.0$ mA, $T_{J} = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA

SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	382	-	ns ns
Rise time	t _r]	-	74	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 50 \text{ A}, R_{g} = 15 \Omega,$	-	379	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	380	-	
Turn-on switching loss	E _{on}]	-	6.28	-	- mJ
Turn-off switching loss	E _{off}		-	3.23	-	
Turn-on delay time	t _{d(on)}		-	403	-	ns ns
Rise time	t _r]	-	72	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 50 \text{ A}, R_{g} = 15 \Omega,$	-	404	-	
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, T_{J} = 125 \text{ °C}$	-	381	-	
Turn-on switching loss	E _{on}		-	7.30	-	I
Turn-off switching loss	E _{off}		-	5.22	-	- mJ
Input capacitance	C _{ies}	V 07 V 05 V 6 4 0 M	-	4.29	-	
Output capacitance	C _{oes}	$V_{GE} = 0 \text{ V}, V_{CE} = 25 \text{ V}, f = 1.0 \text{ MHz},$ $T_{J} = 25 \text{ °C}$	-	0.30	-	nF
Reverse transfer capacitance	C _{res}	- 1,1 - 25	-	0.20	-	
SC data	I _{SC}	$t_{sc} \le 10 \ \mu s, \ V_{GE} = 15 \ V, \ T_J = 125 \ ^{\circ}C, \ V_{CC} = 900 \ V, \ V_{CEM} \le 1200 \ V$	-	270	-	Α
Internal gate resistance	R _{gint}		-	10	-	Ω
Stray inductance	L _{CE}		-	=	30	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V_{F}	I _E = 50 A	T _J = 25 °C	-	2.05	2.45	V
Diode forward voltage	VF	IF = 30 A	T _J = 125 °C	-	2.10	ı	
Diada rayaraa raaayany aharga	0	Q _{rr}	T _J = 25 °C	-	3.32	-	
Diode reverse recovery charge	Q _{rr}		T _J = 125 °C	-	6.51	-	μC
Diada mada waxaya waxayam ayawant	I _{rr}	I_{rr}	T _J = 25 °C	-	33.8	-	Α
Diode peak reverse recovery current			T _J = 125 °C	-	46.9	-	
Diada waxaa	٦		T _J = 25 °C	-	1.28	-	ml
Diode reverse recovery energy	⊏rec		T _J = 125 °C	-	2.03	-	- mJ



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature	TJ		-	-	150	°C
Storage temperature range	T _{STG}		- 40	-	125	
Junction to case IGBT (per 1/2 module)	-		-	-	0.28	
Diode (per 1/2 module)	R _{thJC}		-	-	0.49	K/W
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-	
Mounting torque		Power terminal screw: M5	2.5 to 5.0		Nm	
		Mounting screw: M6	3.0 to 5.0		INIII	
Weight of module				150		g

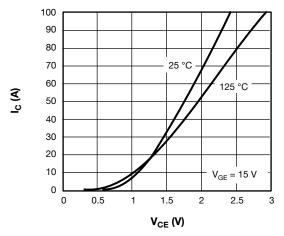


Fig. 1 - Typical Output Characteristics

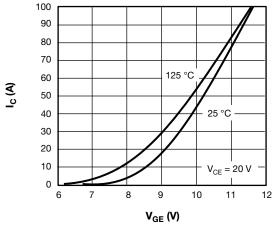


Fig. 2 - Typical Transfer Characteristics

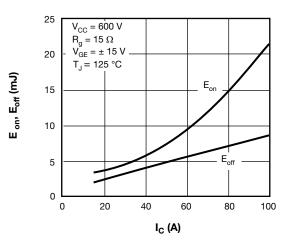


Fig. 3 - Switching Loss vs. I_C

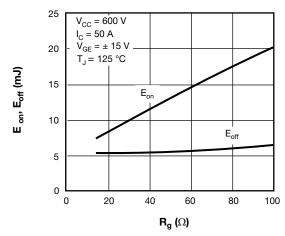


Fig. 4 - Switching Loss vs. Ra

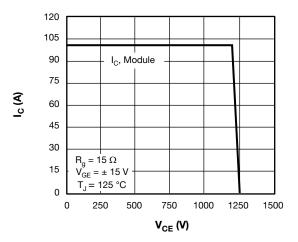


Fig. 5 - RBSOA

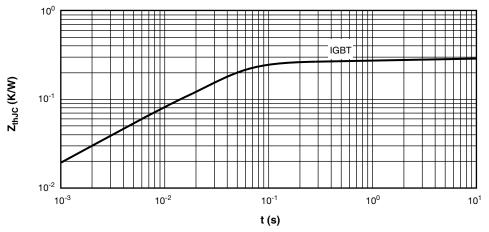


Fig. 6 - IGBT Transient Thermal Impedance

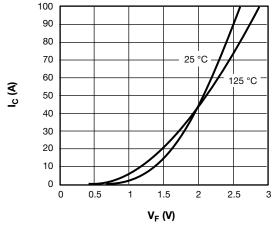


Fig. 7 - Diode Forward Characteristics

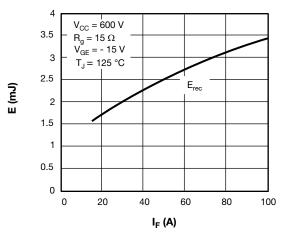


Fig. 8 - Diode Switching Loss vs. I_C

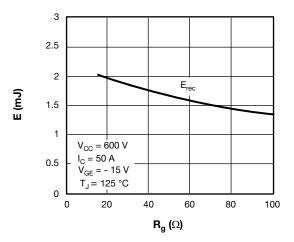


Fig. 9 - Diode Switching Loss vs. R_g

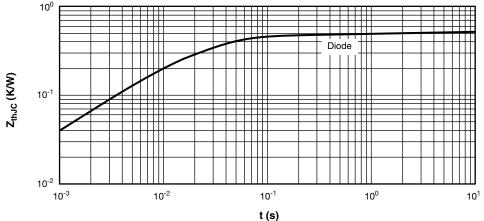
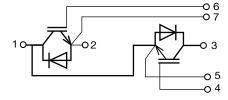


Fig. 10 - Diode Transient Thermal Impedance

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			



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