VS-GB75TP120U

Vishay Semiconductors



Molding Type Module IGBT, 2 in 1 Package, 1200 V, 75 A



INT-A-PAK

1200 V

75 A

3.2 V

8 kHz to 30 kHz

INT-A-PAK

Half bridge

PRIMARY CHARACTERISTICS

V_{CES}

 I_C at $T_C = 80 \ ^{\circ}\overline{C}$

V_{CE(on)} (typical)

at I_C = 75 A, 25 °C Speed

Package

Circuit configuration

FEATURES

- High short circuit capability, self limiting to 6 x I_C
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient
- Rugged with ultrafast performance
- Square RBSOA
- Low inductance case
- · Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Switching mode power supplies
- Inductive heating
- UPS
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	N/	
Gate to emitter voltage	V _{GES}		± 20	V	
Collector current		T _C = 25 °C	105		
	I _C	T _C = 80 °C	75		
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	150	А	
Diode continuous forward current	I _F		75	-	
Diode maximum forward current	I _{FM}		150		
Maximum power dissipation	PD	T _J = 150 °C	500	W	
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	
l ² t-value, diode	l ² t	V _R = 0 V, t = 10 ms, T _J = 125 °C	1170	A ² s	

Note

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
Collector to emitter voltage	V _{CE(on)}	V_{GE} = 15 V, I_C = 75 A, T_J = 25 °C	-	3.2	-	v	
		V_{GE} = 15 V, I_{C} = 75 A, T_{J} = 125 °C	-	3.7	-		
Gate to emitter threshold voltage	V _{GE(th)}	V_{CE} = V_{GE} , I_C = 3 mA, T_J = 25 °C	4.5	5.1	5.5		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	2.0	mA	
Gate to emitter leakage current	I _{GES}	V_{GE} = V_{GES} , V_{CE} = 0 V, T_J = 25 °C	-	-	400	nA	

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1

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SWITCHING CHARACTERISTICS	5					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	160	-	
Rise time	t _r		-	80	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 75 \text{ A}, \text{ R}_{g} = 15 \Omega,$	-	420	-	- ns - mJ
Fall time	t _f	$V_{GE} = \pm 15 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$	-	110	-	
Turn-on switching loss	Eon		-	5.7	-	
Turn-off switching loss	E _{off}		-	1.9	-	mj
Turn-on delay time	t _{d(on)}		-	140	-	
Rise time	t _r		-	90	-	ns
Turn-off delay time	t _{d(off)}		-	460	-	
Fall time	t _f		-	150	-	
Turn-on switching loss	E _{on}		-	6.8	-	
Turn-off switching loss	E _{off}		-	3.2	-	mJ
Input capacitance	Cies		-	4.3	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 30 V, f = 1.0 MHz, T _{.1} = 25 °C	-	0.40	-	nF
Reverse transfer capacitance	C _{res}	1]=25 C	-	0.16	-	
SC data	I _{SC}	$\label{eq:tsc} \begin{array}{l} t_{sc} \leq 10 \; \mu s, V_{GE} = 15 \; V, T_J = 125 \; ^{\circ}C, \\ V_{CC} = 900 \; V, V_{CEM} \leq 1200 \; V \end{array}$	-	235	-	А
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.75	-	mΩ

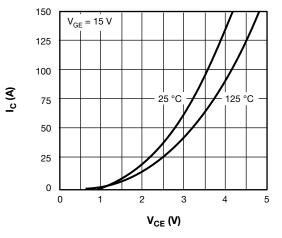
DIODE ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V _F	I _F = 75 A	T _J = 25 °C	-	1.9	2.3	v
Diode forward voltage			T _J = 125 °C	-	2.0	2.4	
Diode reverse recovery charge	Q _{rr}	$I_{F} = 75 \text{ A}, V_{R} = 600 \text{ V}, \\ dI_{F}/dt = -2000 \text{ A}/\mu\text{s}, \\ V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	100	-	μC
			T _J = 125 °C	-	125	-	
Diode peak reverse recovery current	Irr		T _J = 25 °C	-	80	-	А
			T _J = 125 °C	-	100	-	
Diode reverse recovery energy	E _{rec}		T _J = 25 °C	-	3.0	-	ml
			T _J = 125 °C	-	6.0	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature range	TJ		-40	-	150	°C
Storage temperature range	T _{STG}		-40	-	125	U
Junction to case IGBT (per 1/2 module)	R _{thJC}		-	-	0.25	K/W
Diode (per 1/2 module)			-	-	0.40	
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-	
Mounting torque		Power terminal screw: M5	2.5 to 5.0		Nm	
Mounting torque		Mounting screw: M6 3.0 to 5.0)		
Weight of module				160		g

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Fig. 1 - Typical Output Characteristics

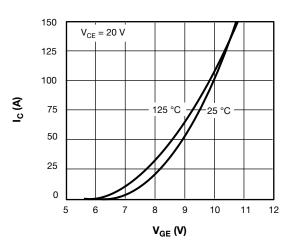


Fig. 2 - Typical Transfer Characteristics

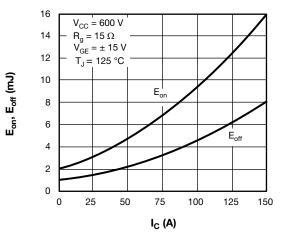


Fig. 3 - Total Switching Loss vs. ${\rm I}_{\rm C}$

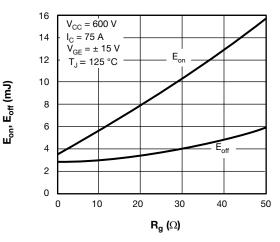
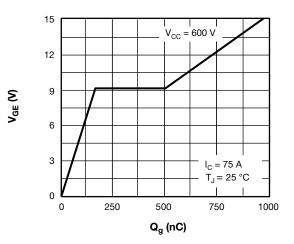


Fig. 4 - Total Switching Loss vs. R_a





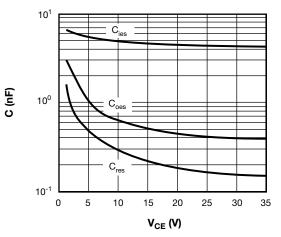


Fig. 6 - Typical Capacitance vs. Collector to Emitter Current

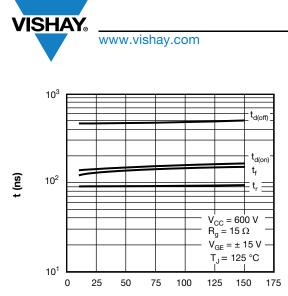
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3

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I_C (A)

Fig. 7 - Typical Switching Times vs. I_C

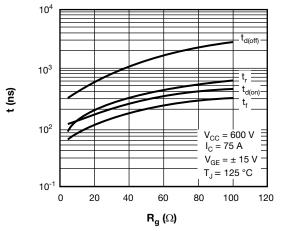


Fig. 8 - Typical Switching Times vs.Gate Resistance Ra

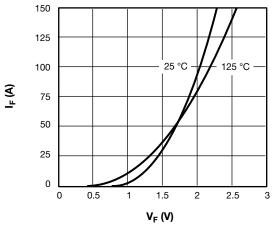


Fig. 9 - Diode Typical Forward Characteristics

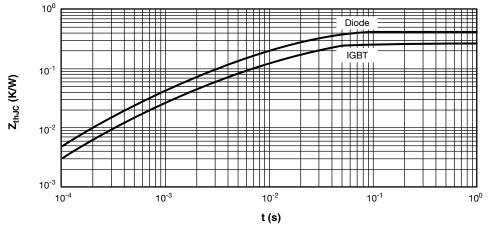


Fig. 10 - Transient Thermal Impedance

 Revision: 20-Sep-17
 4
 Document Number: 94822

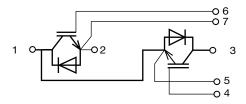
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CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95524				



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