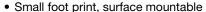


High Performance Schottky Rectifier, 1.0 A



PRODUCT SUMMARY				
Package	SMB			
I _{F(AV)}	1.0 A			
V_{R}	30 V			
V _F at I _F	0.30 V			
I _{RM} max.	20 mA at 125 °C			
T _J max.	125 °C			
Diode variation	Single die			
E _{AS}	3.0 mJ			

FEATURES







• High frequency operation

 Guard ring for enhanced ruggedness and long term reliability

HALOGEN FREE

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-MBRS130L-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN			
I _{F(AV)}	Rectangular waveform	1.0	Α		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 μs sine	230	А		
V _F	1.0 A _{pk} , T _J = 125 °C	0.30	V		
TJ	Range	-55 to +125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRS130L-M3	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 112 °C, rectangular waveform		1.0	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	230	Α
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	40	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}$, $I_{AS} = 1 \text{A}$, $L = 6 \text{mH}$		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1.0	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		1 A	T _J = 25 °C	0.420	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	2 A		0.470	
waximum forward voltage drop	V FM (*)	1 A	T _{.1} = 125 °C	0.300	V
		2 A	1 J = 125 C	0.370	
		T _J = 25 °C		1	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 100 °C	V_R = Rated V_R	10	mA
		T _J = 125 °C		20	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width $<300~\mu s,$ duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +125	°C
Maximum storage temperature range	T _{Stg}		-55 to +150	C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation See fig. 4	25	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	C/VV
Approximate weight			0.10	g
Approximate weight			0.003	OZ.
Marking device		Case style SMB (similar to DO-214AA) 13L		BL

Notes

 $^{^{(1)} \}quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB

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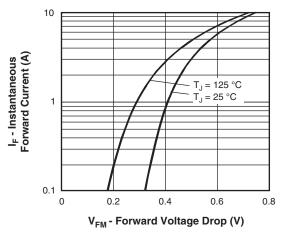


Fig. 1 - Maximum Forward Voltage Drop Characteristics

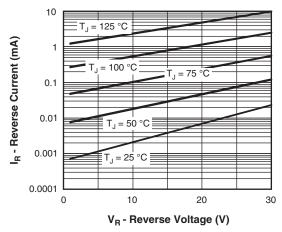


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

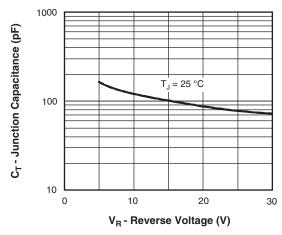
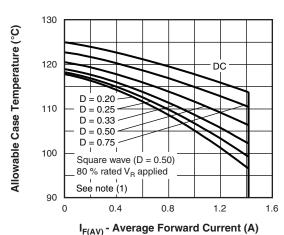
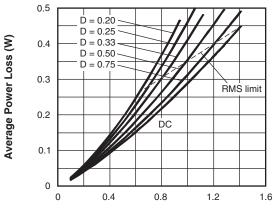


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



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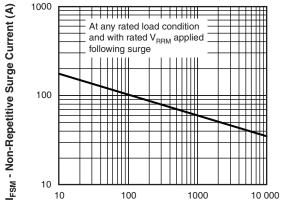
Fig. 4 - Maximum Average Forward Current vs. Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs.

Average Forward Current



t_p - Square Wave Pulse Duration (μs)

Fig. 6 - Maximum Peak Surge Forward Current vs.
Pulse Duration

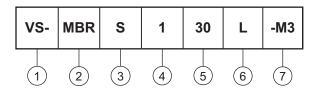
Note

(1) Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Schottky MBR series
- 3 S = SMB
- Current rating (1 = 1 A)
- 5 Voltage rating (30 = 30 V)
- 6 L = low forward voltage
- 7 -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBRS130L-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

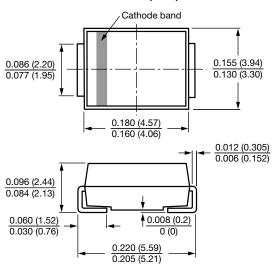
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95401</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			



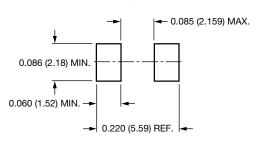
SMB

DIMENSIONS in inches (millimeters)

DO-214AA (SMB)



Mounting Pad Layout





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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000

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