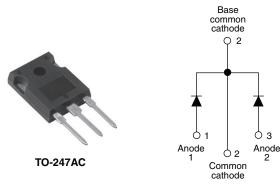


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Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY						
Package	TO-247AC					
I _{F(AV)}	2 x 15 A					
V _R	60 V					
V _F at I _F	0.56 V					
I _{RM} max.	100 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Common cathode					
E _{AS}	13 mJ					

FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-STPS30L60CW... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	30	A						
V _{RRM}		60	V						
I _{FSM}	t _p = 5 μs sine	1020	А						
V _F	15 Apk, $T_J = 125 \ ^{\circ}C$ (per leg)	0.56	V						
TJ		- 55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-STPS30L60CWPbF	VS-STPS30L60CW-N3	UNITS				
Maximum DC reverse voltage	V _R	60	60	V				
Maximum working peak reverse voltage	V _{RWM}	00	00	v				

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS			
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 112 \text{ °C}$	30				
Maximum peak one cycle non-repetitive surge current per leg		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1020	А		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	265			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.	13	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero Frequency limited by T _J maximum	1.50	А			

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS				
		15 A	T _{.1} = 25 °C	0.60			
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1j=25 0	0.80	v		
See fig. 1	V FM \''	15 A	T.I = 125 °C	0.56			
		30 A	1j=125 0	0.70			
•• • • • •	I _{RM} ⁽¹⁾	$T_J = 25 \ ^\circ C$		0.48			
Maximum reverse leakage current per leg See fig. 2		T 105 %O	$V_R = Rated V_R$	50 (typical)	mA		
600 lig. 2		T _J = 125 °C		100			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal ran	720	pF			
Typical series inductance per leg	L _S	Measured lead to lead 5 n	7.5	nH			
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs			

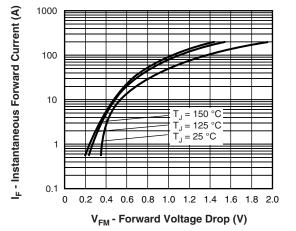
Note

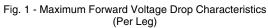
 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

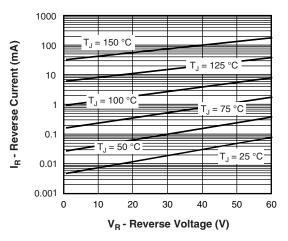
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4					
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.10	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24				
Approvimate weight				6	g			
Approximate weight				0.21	oz.			
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm			
Mounting torque —	maximum		Non-Iupricated threads	12 (10)	(lbf · in)			
Marking device			Case style TO-247AC (JEDEC)	STPS30	L60CW			

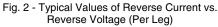


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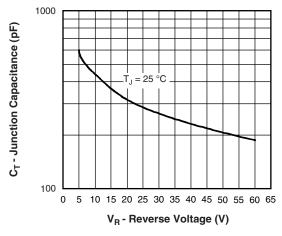
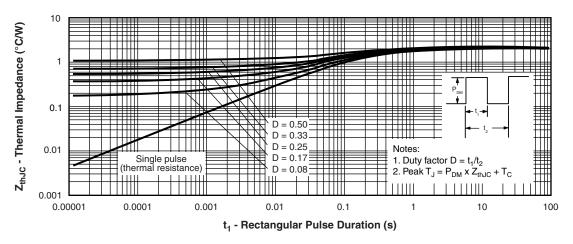


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

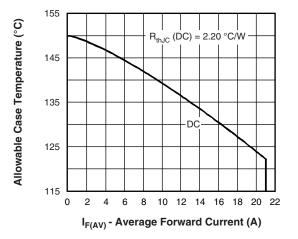


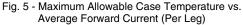


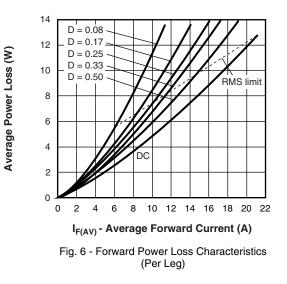
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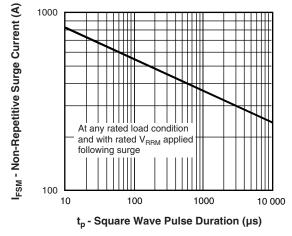


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

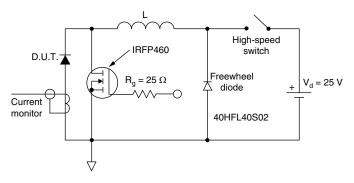
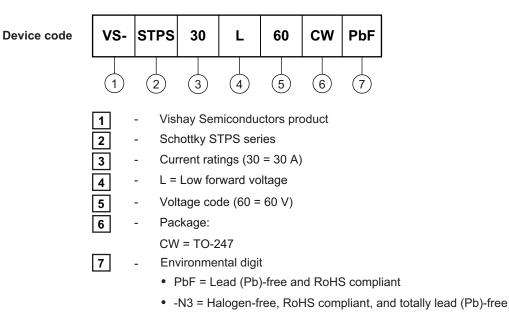


Fig. 8 - Unclamped Inductive Test Circuit



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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-STPS30L60CWPbF	25	500	Antistatic plastic tube					
VS-STPS30L60CW-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?95223							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					

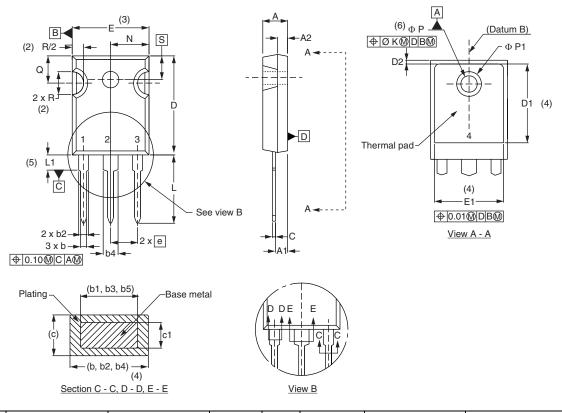
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Vishay Semiconductors

TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	2.	54	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension c

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