

## Features

- N-Channel
- Enhancement mode
- Very low on-resistance
- Fast Switching
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant

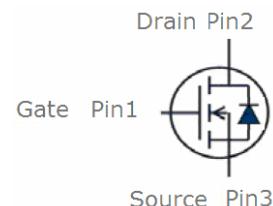
$V_{DS}$	30	V
$R_{DS(on),typ.}$	5	mΩ
$I_D$	72	A

TO-252



Halogen-Free

Part ID	Package Type	Marking	Tape and reel information
VS3072AD	TO-252	3072AD	2500pcs/reel



Maximum ratings, at  $T_j=25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
<b>Common Ratings (Tc=25°C Unless Otherwise Noted)</b>				
$V_{GS}$	Gate-Source Voltage	±20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	30	V	
$T_J$	Maximum Junction Temperature	175	°C	
$T_{STG}$	Storage Temperature Range	-55 to 175	°C	
$I_S$	Diode Continuous Forward Current	$T_c=25^\circ\text{C}$	72	A
<b>Mounted on Large Heat Sink</b>				
$I_D$	Continuous Drain current@ $VGS=10\text{V}$	$T_c=25^\circ\text{C}$	72	A
		$T_c=100^\circ\text{C}$	43	A
$I_{DM}$	Pulse Drain Current Tested ①	$T_c=25^\circ\text{C}$	184	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	50	W
$R_{JJC}$	Thermal Resistance-Junction to Case	3.0	°C/W	
$R_{JJA}$	Thermal Resistance Junction-Ambient((*1 in <sup>2</sup> Pad of 2-oz Copper), Max.)	50	°C/W	
<b>Drain-Source Avalanche Ratings</b>				
EAS	Avalanche Energy, Single Pulsed ②	31	mJ	

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(T <sub>c</sub> =25°C)	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T <sub>c</sub> =125°C)	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	2.0	2.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>③</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	--	4.8	5.9	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>③</sup>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =15A	--	8.0	12.5	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	--	1185	--	pF
C <sub>oss</sub>	Output Capacitance		--	485	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	60	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =15V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	18	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	4.1	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	3.6	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =15V, I <sub>D</sub> =1A, R <sub>G</sub> =3Ω, V <sub>GS</sub> =10V	--	7.5	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	10.5	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	22	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	5	--	nS
<b>Source- Drain Diode Characteristics@ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =15A, V <sub>GS</sub> =0V	--	0.81	1.20	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>j</sub> =25°C, I <sub>SD</sub> =20A, V <sub>GS</sub> =0V di/dt=100A/μs	--	15	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge		--	24	--	nC

## NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Limited by T<sub>Jmax</sub>, starting T<sub>j</sub> = 25°C, L = 0.1mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 25A, V<sub>GS</sub> = 10V. Part not recommended for use above this value

③ Pulse width ≤ 300μs; duty cycles≤ 2%.

### Typical Characteristics

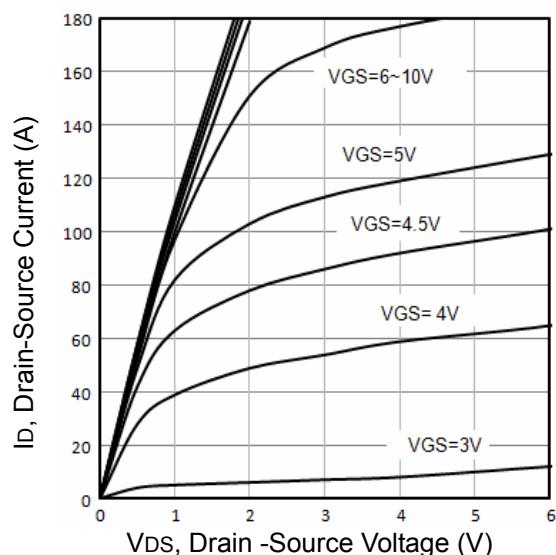


Fig1. Typical Output Characteristics

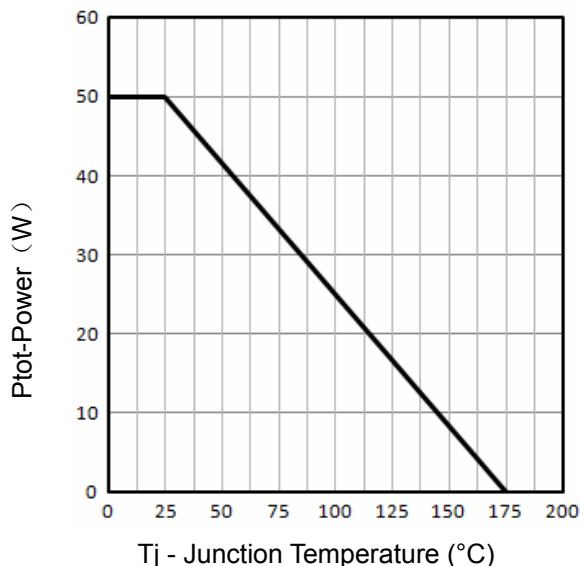


Fig2. Power Dissipation

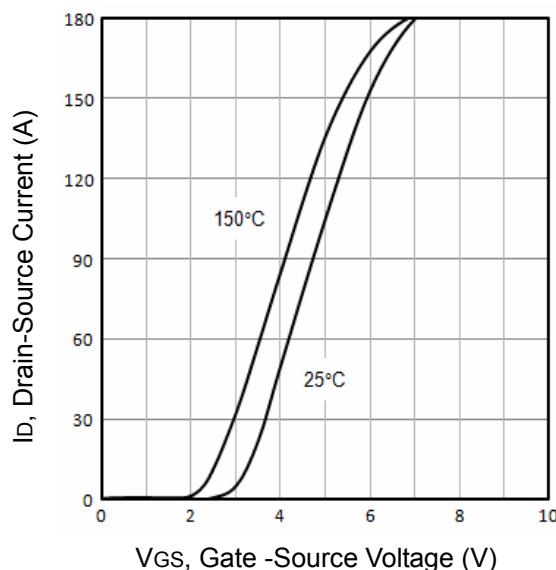


Fig3. Typical Transfer Characteristics

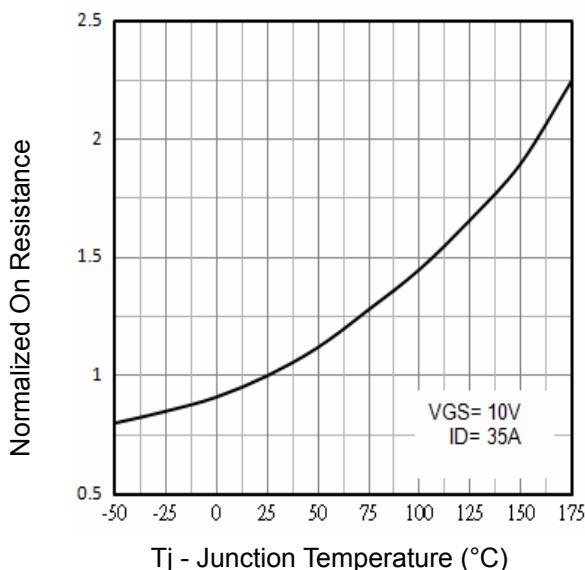


Fig4. Normalized On-Resistance Vs. Temperature

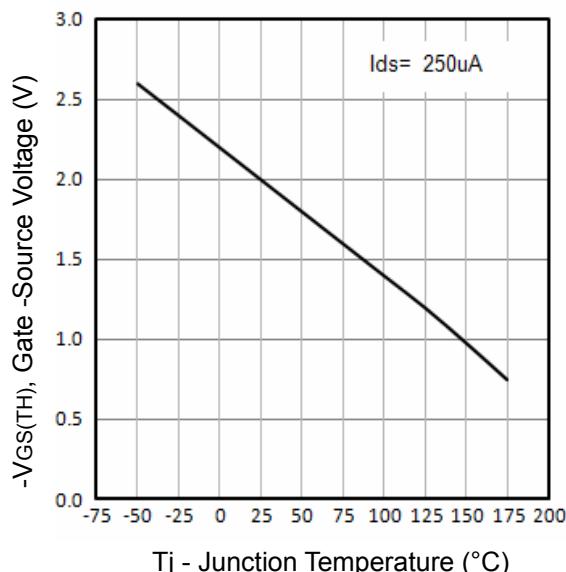


Fig5. Normalized BVDSS Vs. Temperature

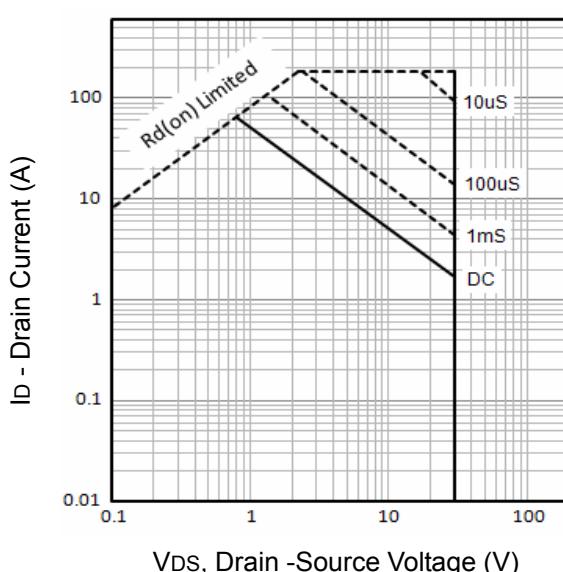


Fig6. Maximum Safe Operating Area

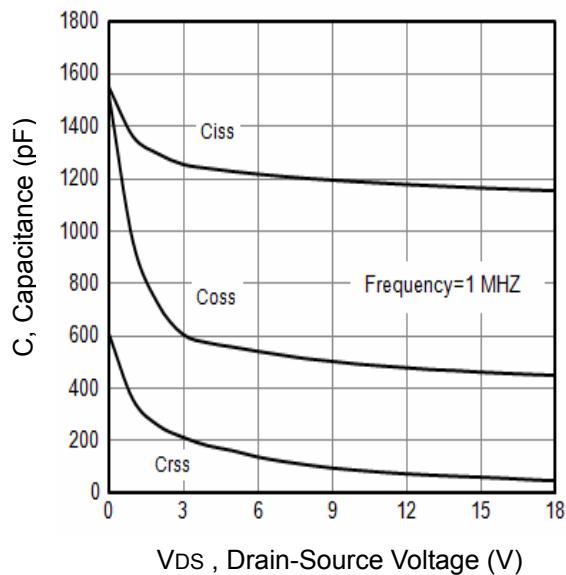


Fig7. Typical Capacitance Vs.Drain-Source Voltage

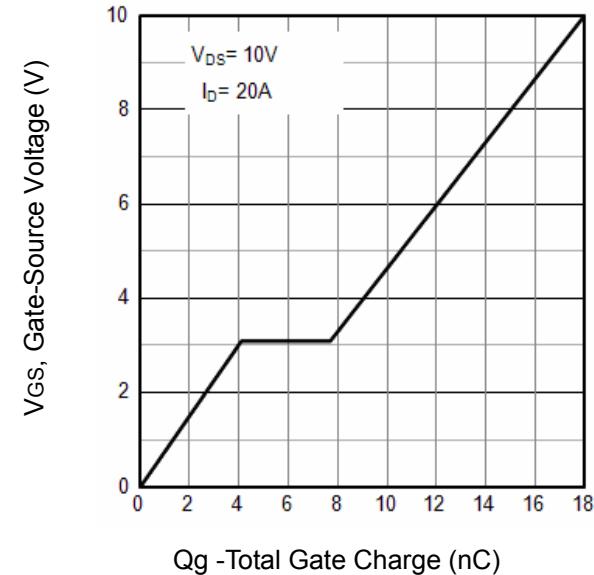


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

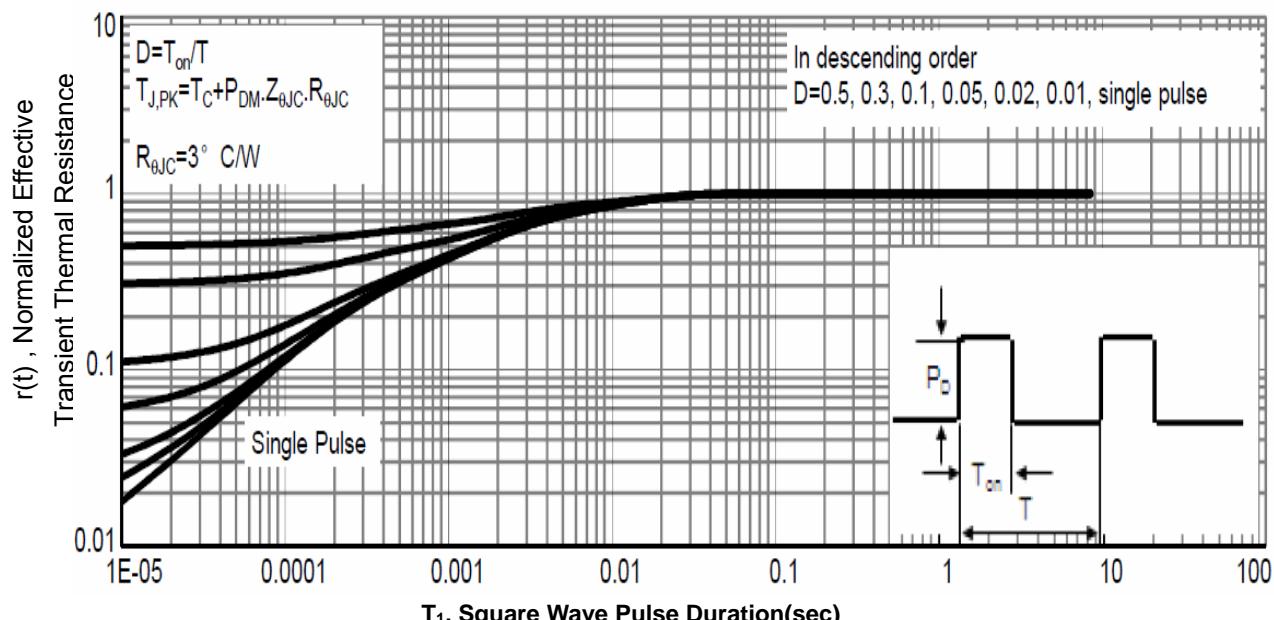


Fig9. T<sub>1</sub> ,Transient Thermal Response Curve

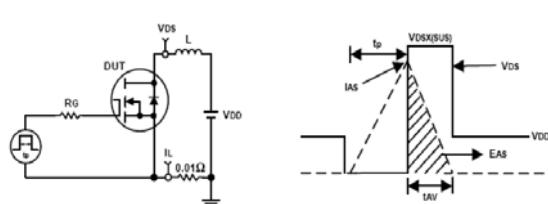


Fig10. Unclamped Inductive Test Circuit and waveforms

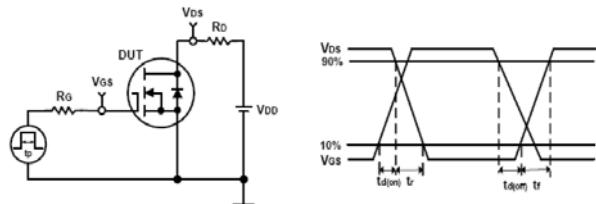
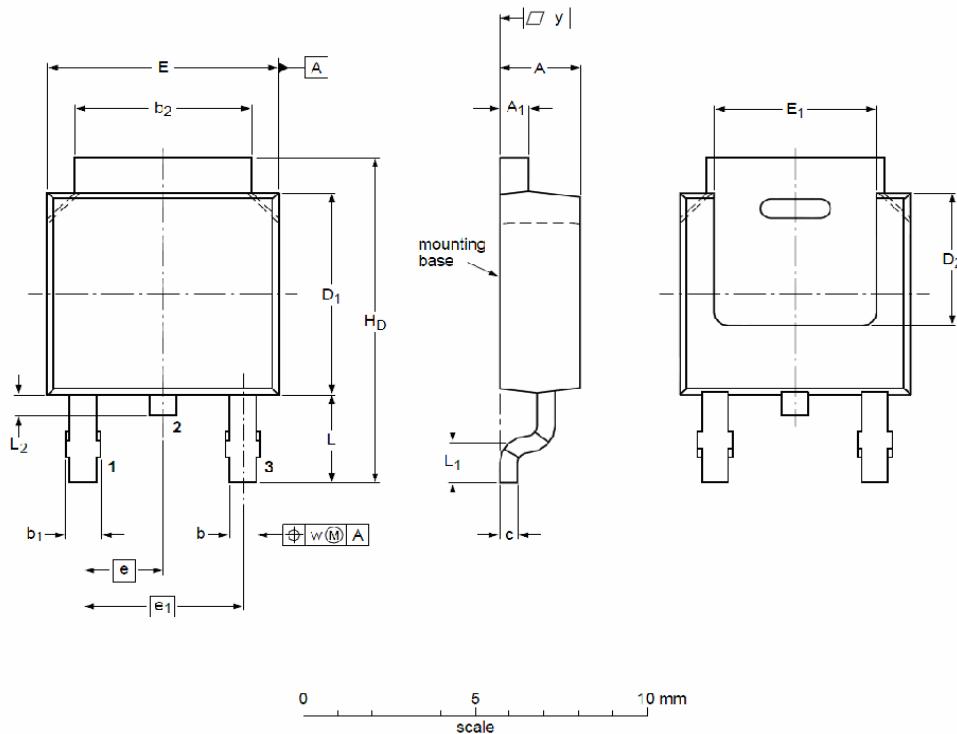


Fig11. Switching Time Test Circuit and waveforms

## TO-252 Package Outline



**DIMENSIONS ( unit : mm )**

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.22	2.30	2.38	A <sub>1</sub>	0.46	0.58	0.93
b	0.71	0.79	0.89	b <sub>1</sub>	0.90	0.98	1.10
b <sub>2</sub>	5.00	5.30	5.46	c	0.20	0.40	0.56
D <sub>1</sub>	5.98	6.05	6.22	D <sub>2</sub>	--	4.00	--
E	6.47	6.60	6.73	E <sub>1</sub>	5.10	5.28	5.45
e	--	2.28	--	e <sub>1</sub>	--	4.57	--
H <sub>D</sub>	9.60	10.08	10.40	L	2.75	2.95	3.05
L <sub>1</sub>	--	0.50	--	L <sub>2</sub>	0.80	0.90	1.10
w	--	0.20	--	y	0.20	--	--

## Customer Service

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