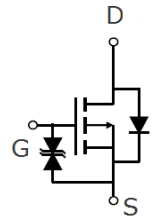
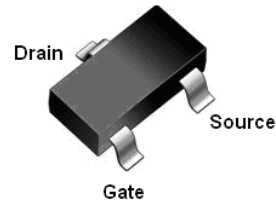


## Features

- P-Channel
- Enhancement mode
- Very low on-resistance @  $V_{GS}=-2.5\text{ V}$
- Fast Switching
- ESD Protection HBM 2KV
- Pb-free lead plating; RoHS compliant

$V_{DS}$	-20	V
$R_{DS(on),TYP} @ V_{GS}=-4.5\text{ V}$	30	m $\Omega$
$R_{DS(on),TYP} @ V_{GS}=-2.5\text{ V}$	40	m $\Omega$
$I_D$	-5	A

SOT23-3L



Part ID	Package Type	Marking	Tape and reel information
VS3415AL	SOT23-3L	V25	3000pcs/reel

## Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	-20	V
$I_S$	Diode continuous forward current	$T_C=25\text{ }^\circ\text{C}$ -5	A
$I_D$	Continuous drain current @ $V_{GS}=10\text{ V}$	$T_C=25\text{ }^\circ\text{C}$ -5	A
		$T_C=70\text{ }^\circ\text{C}$ -4	A
$I_{DM}$	Pulse drain current tested ①	$T_C=25\text{ }^\circ\text{C}$ -20	A
$P_D$	Maximum power dissipation	$T_C=25\text{ }^\circ\text{C}$ 1.5	W
$V_{GS}$	Gate-Source voltage	$\pm 8$	V
ESD	HBM	2	KV
$T_{STG} T_J$	Storage and operating temperature range	-55 to 150	$^\circ\text{C}$
<b>Thermal Characteristics</b>			
$R_{\theta JC}$	Thermal Resistance-Junction to Case	14.5	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	85	$^\circ\text{C/W}$

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-20	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(T <sub>c</sub> =25°C)	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T <sub>c</sub> =125°C)	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	--	--	±10	μA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.45	-0.65	-0.95	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>②</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	--	30	40	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-3A	--	40	50	mΩ
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1A	--	60	90	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-12V, V <sub>GS</sub> =0V, f=1MHz	--	780	--	pF
C <sub>oss</sub>	Output Capacitance		--	88	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	18	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-10V, I <sub>D</sub> =-2.5A, V <sub>GS</sub> =-4.5V	--	10	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	1.1	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	2.1	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-10V, I <sub>D</sub> =-2.5A, R <sub>G</sub> =3Ω, V <sub>GS</sub> =-4.5V	--	23	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	110	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	50	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	100	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =-3A, V <sub>GS</sub> =0V	--	-0.84	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>j</sub> =25°C, I <sub>sd</sub> =-3A, V <sub>GS</sub> =0V	--	23	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=-100A/μs		52		nC

**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature.  
 ② Pulse width ≤ 300μs; duty cycles ≤ 2%.

**Typical Characteristics**

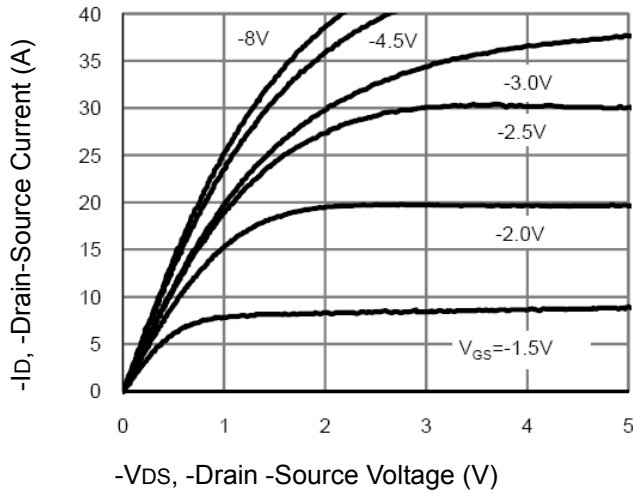


Fig1. Typical Output Characteristics

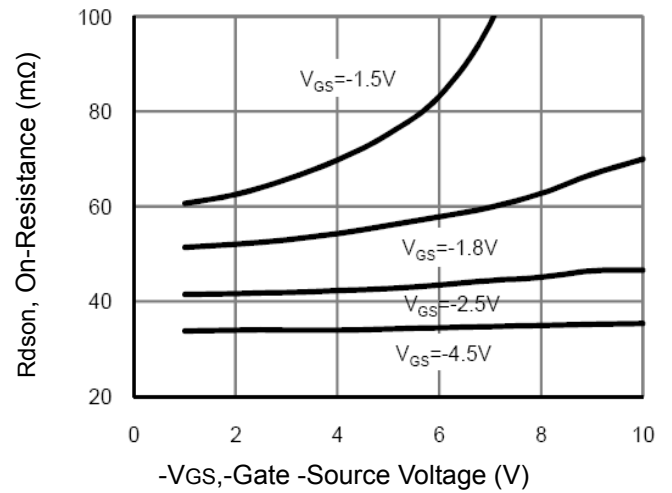


Fig2. On-Resistance vs Drain Current and Gate Voltage

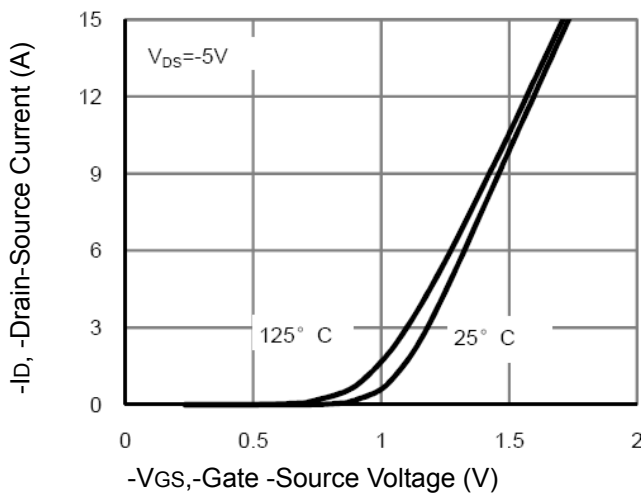


Fig3. Typical Transfer Characteristics

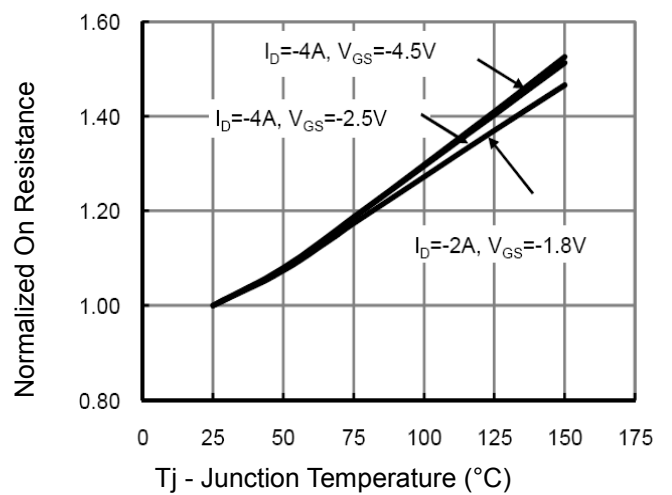


Fig4. Normalized On-Resistance Vs. Temperature

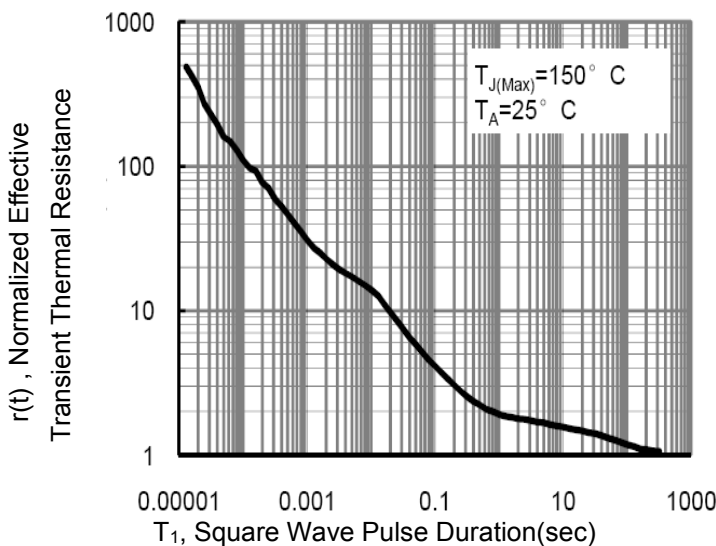


Fig5. T1, Transient Thermal Response Curve

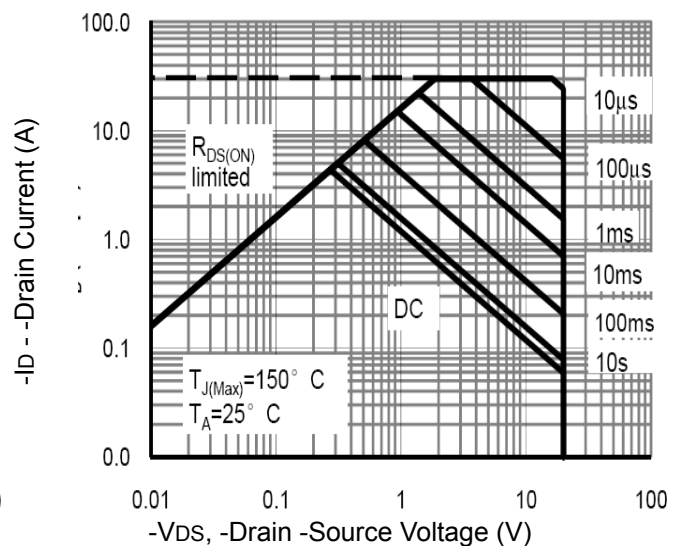


Fig6. Maximum Safe Operating Area

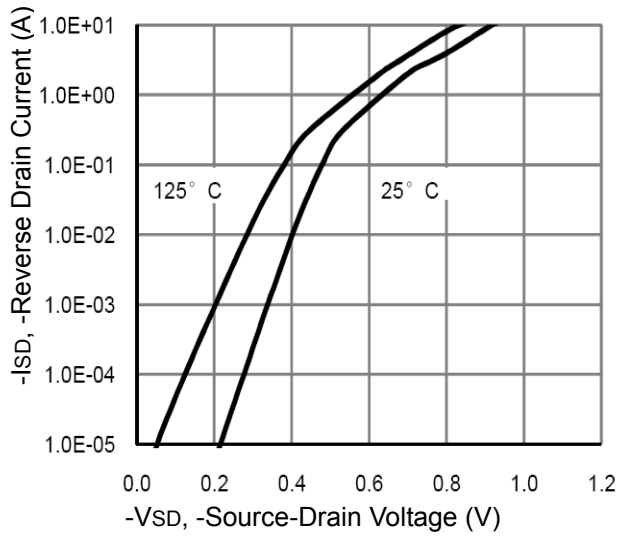


Fig7. Typical Source-Drain Diode Forward Voltage

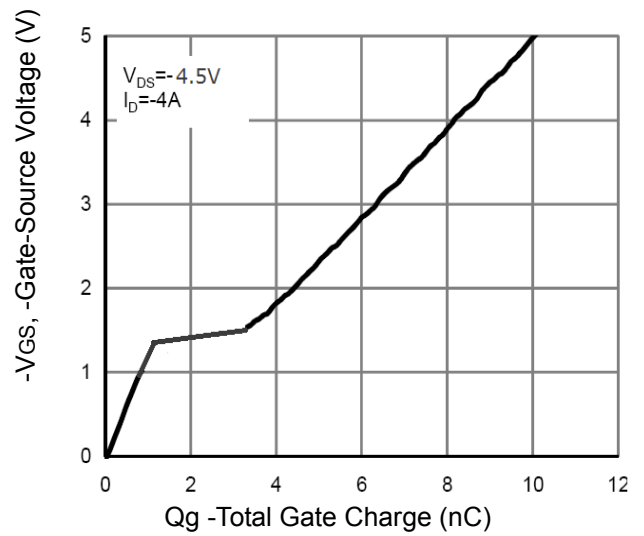


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

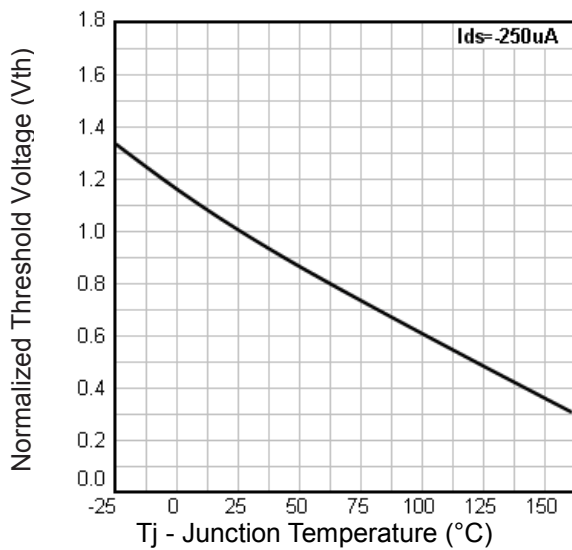


Fig9. Normalized Threshold Voltage Vs. Temperature

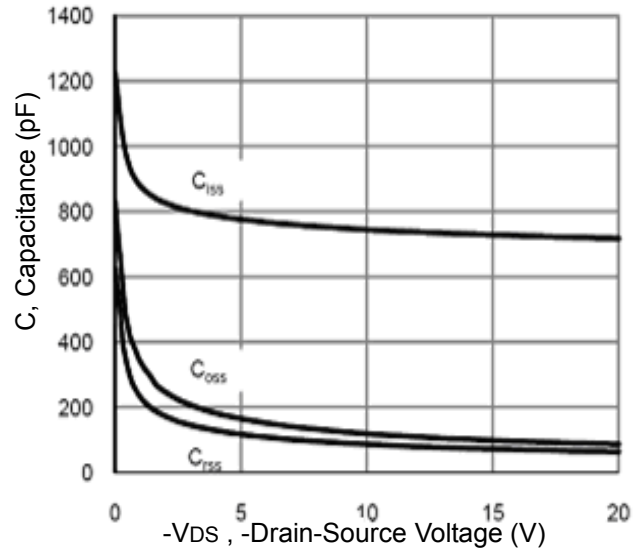


Fig10. Typical Capacitance Vs. Drain-Source Voltage

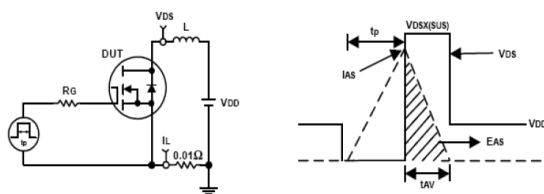


Fig11. Unclamped Inductive Test Circuit and waveforms

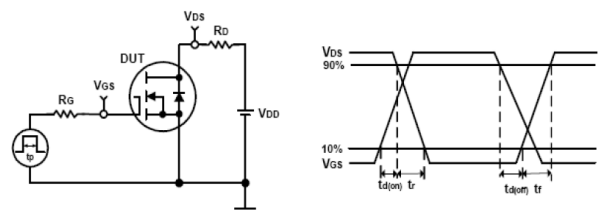
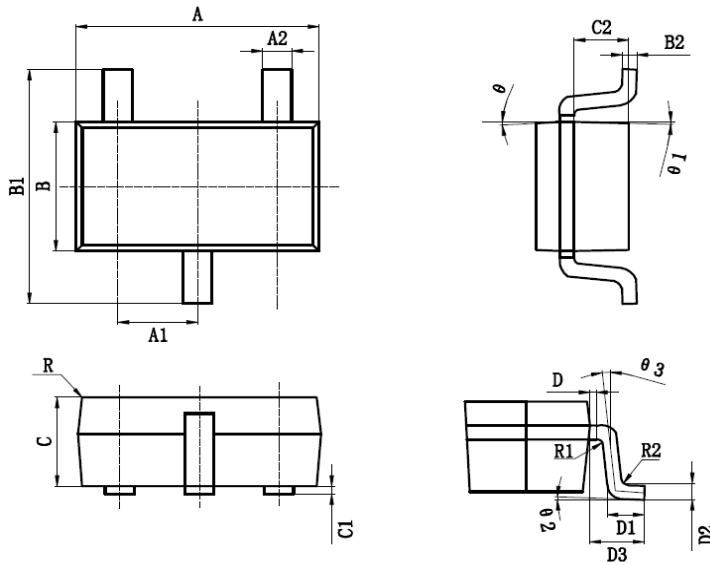


Fig12. Switching Time Test Circuit and waveforms

**SOT23-3L Mechanical Data**


Symbol	Size	Min.(mm)	Max.(mm)	Symbol	Size	Min.(mm)	Max.(mm)
<b>A</b>		2.82	3.02	<b>D1</b>		0.40	0.50
<b>A1</b>		0.90	1.00	<b>D2</b>		0.254TYP	
<b>A2</b>		0.35	0.45	<b>D3</b>		<b>0.60</b>	<b>0.70</b>
<b>B</b>		1.52	1.72	<b>θ</b>		9° TYP4	
<b>B1</b>		2.80	3.00	<b>θ 1</b>		10° TYP4	
<b>B2</b>		0.119	0.135	<b>θ 2</b>		0° ~ 8°	
<b>C</b>		1.05	1.15	<b>θ 3</b>		6° TYP	
<b>C1</b>		0.03	0.13	R		<0.2TYP4	
C2		0.60	0.70	R1		0.08TYP	
D		0.03	0.13	R2		0.08TYP	

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