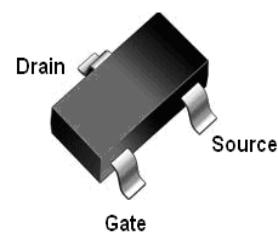


## Features

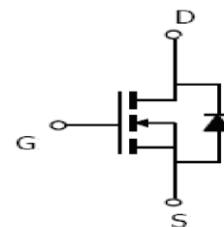
- N-Channel, 5V Logic Level Control
- Enhancement mode
- Low on-resistance  $R_{DS(on)}$  @  $V_{GS}=4.5$  V
- Fast Switching
- Pb-free lead plating; RoHS compliant

$V_{DS}$	30	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	19	$m\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	27	$m\Omega$
$I_D$	6	A

SOT23



Part ID	Package Type	Marking	Tape and reel information
VS3640AC	SOT23	VS03	3000pcs/reel



## Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	30	V
$I_s$	Diode continuous forward current	$T_c=25^\circ C$	A
$I_D$	Continuous drain current @ $V_{GS}=10V$	$T_c=25^\circ C$	A
		$T_c=100^\circ C$	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^\circ C$	A
$P_d$	Maximum power dissipation	$T_c=25^\circ C$	W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$T_{STG} T_j$	Storage and operating temperature range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	80	°C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	125	°C/W

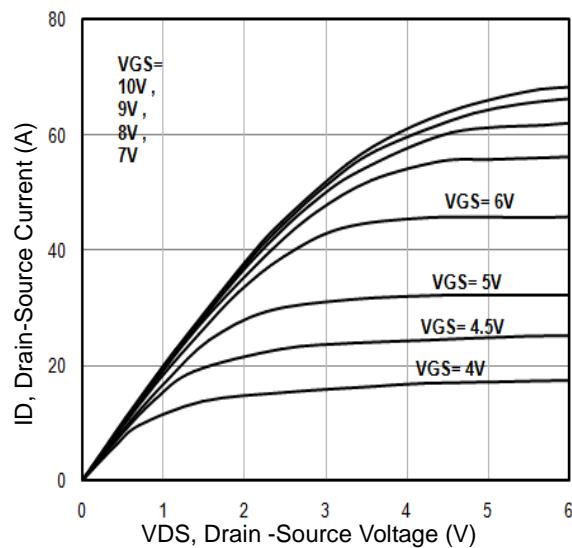
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	30	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
	Zero Gate Voltage Drain Current( $T_j=125^\circ\text{C}$ )	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1.0	2	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=10\text{V}, I_D=5\text{A}$	--	19	24	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}, I_D=4\text{A}$	--	27	34	$\text{m}\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	455	--	pF
$C_{\text{oss}}$	Output Capacitance		--	75	--	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	55	--	pF
$R_g$	Gate Resistance	$f=1\text{MHz}$		2.3		$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=15\text{V}, I_D=5\text{A}, V_{\text{GS}}=10\text{V}$	--	11	--	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	3	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	4	--	nC
<b>Switching Characteristics</b>						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=15\text{V}, I_D=5\text{A}, R_{\text{G}}=3\Omega, V_{\text{GS}}=10\text{V}$	--	7	--	nS
$t_r$	Turn-on Rise Time		--	10	--	nS
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	22	--	nS
$t_f$	Turn-Off Fall Time		--	7	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{\text{SD}}$	Forward on voltage	$I_{\text{SD}}=5\text{A}, V_{\text{GS}}=0\text{V}$	--	0.84	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=5\text{A}, \frac{di}{dt}=500\text{A}/\mu\text{s}$	--	9.5	--	nS
$Q_{\text{rr}}$	Reverse Recovery Charge			12		nC

**NOTE:**

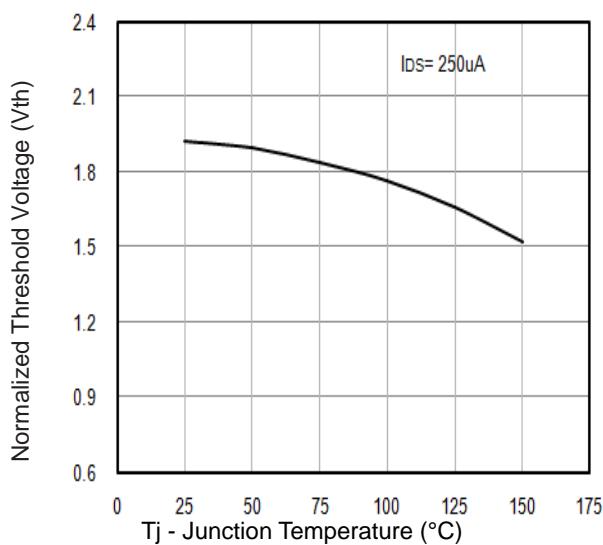
① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .

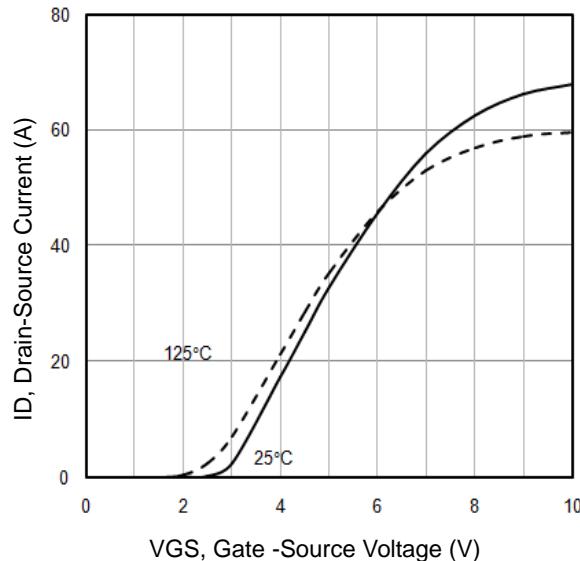
### Typical Characteristics



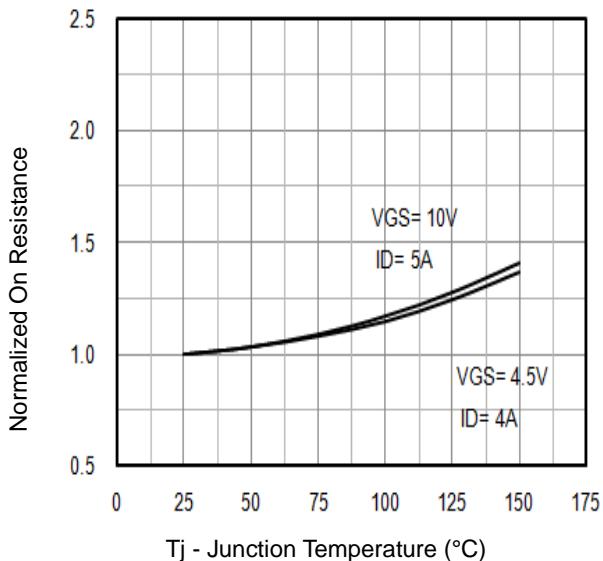
**Fig1.** Typical Output Characteristics



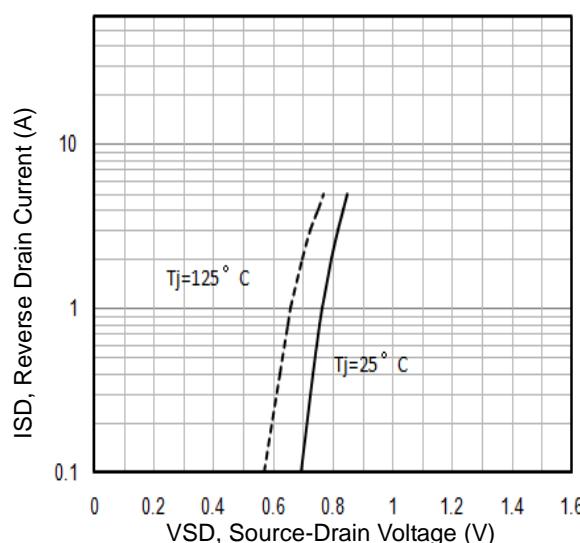
**Fig2.**  $V_{GS(TH)}$  Gate -Source Voltage Vs. $T_j$



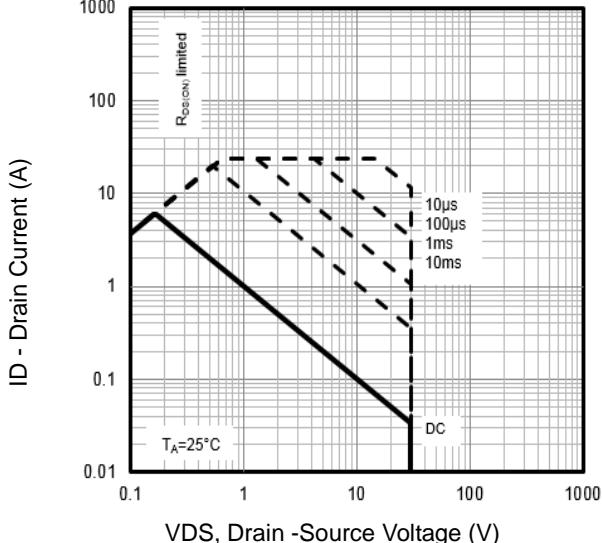
**Fig3.** Typical Transfer Characteristics



**Fig4.** Normalized On-Resistance Vs.  $T_j$

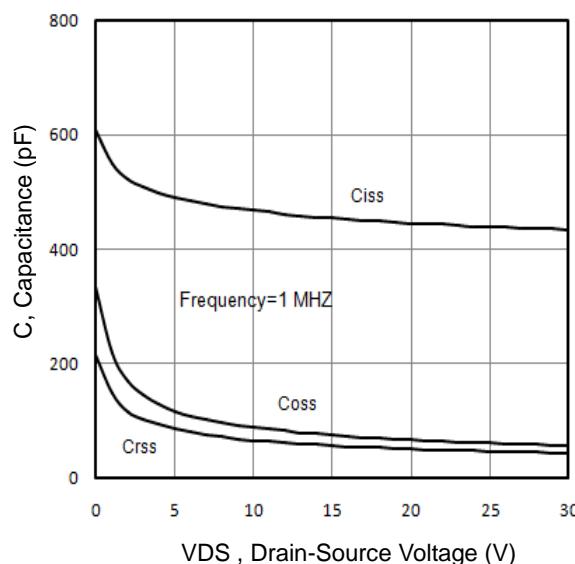


**Fig5.** Typical Source-Drain Diode Forward Voltage

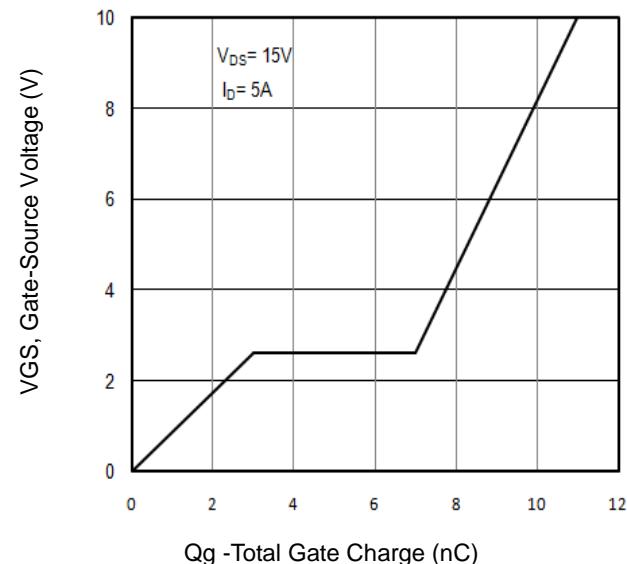


**Fig6.** Maximum Safe Operating Area

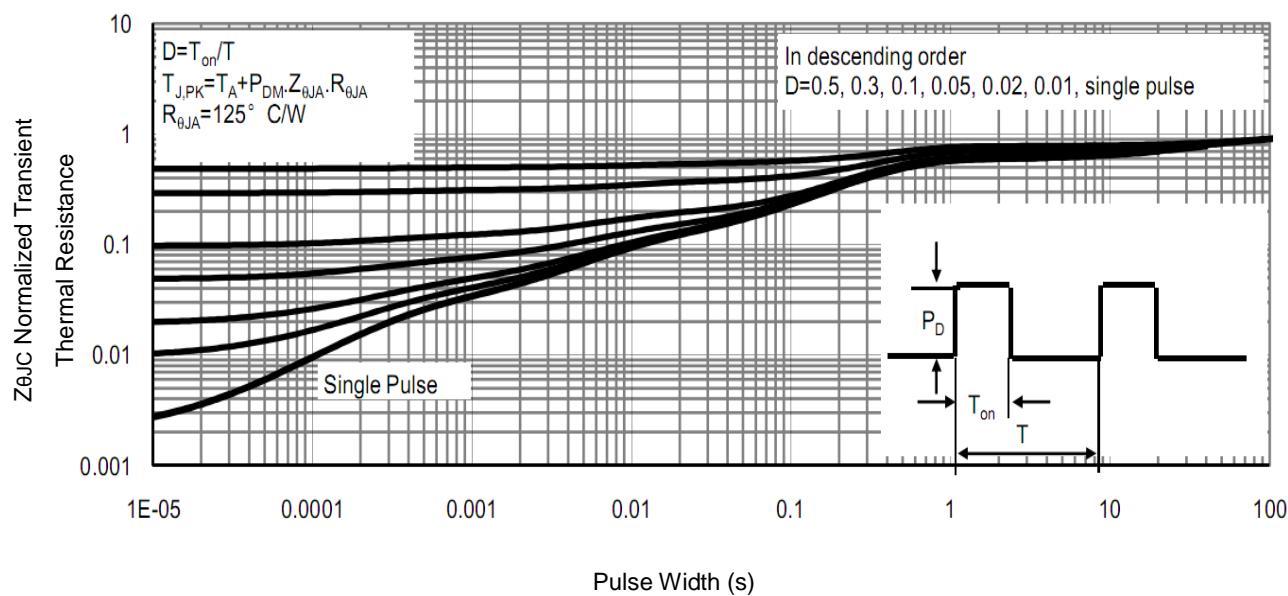
## Typical Characteristics



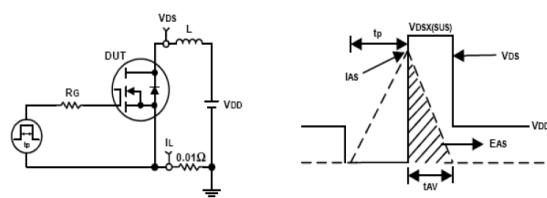
**Fig7.** Typical Capacitance Vs.Drain-Source Voltage



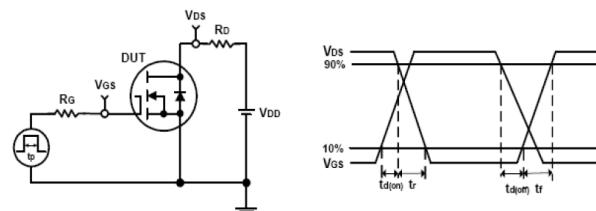
**Fig8.** Typical Gate Charge Vs.Gate-Source Voltage



**Fig9 .** Normalized Maximum Transient Thermal Impedance

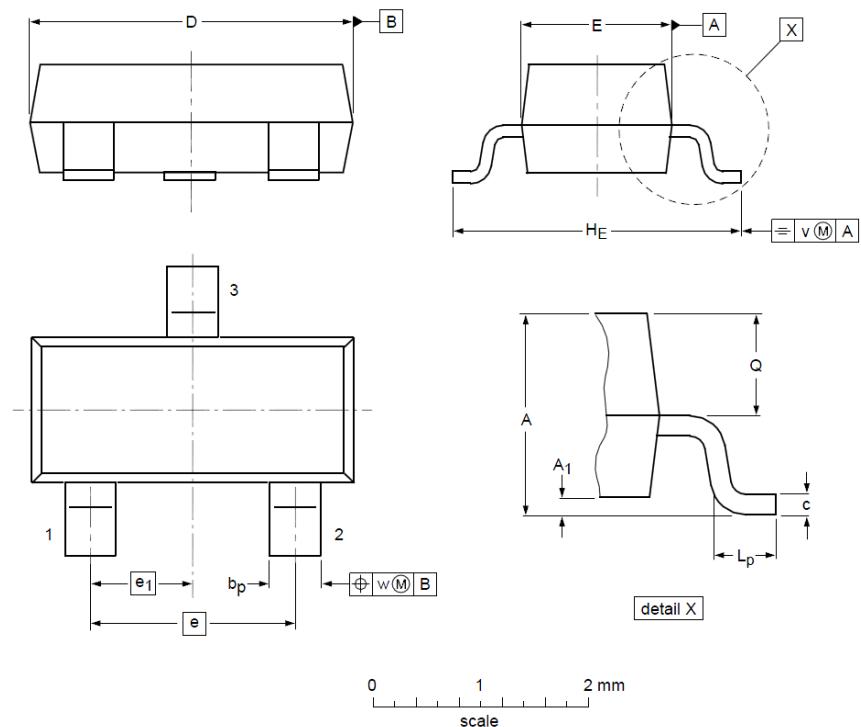


**Fig10.** Unclamped Inductive Test Circuit and waveforms



**Fig11.** Switching Time Test Circuit and waveforms

## SOT23 Package Outline Data



Label	DIMENSIONS ( unit: mm )		
	Min	Typ	Max
<b>A</b>	0.90	1.03	1.10
<b>A<sub>1</sub></b>	0.01	0.05	0.10
<b>b<sub>p</sub></b>	0.38	0.42	0.48
<b>c</b>	0.09	0.13	0.15
<b>D</b>	2.80	2.92	3.00
<b>E</b>	1.20	1.33	1.40
<b>e</b>	--	1.90	--
<b>e<sub>1</sub></b>	--	0.95	--
<b>H<sub>E</sub></b>	2.10	2.40	2.50
<b>L<sub>p</sub></b>	0.40	0.50	0.60
<b>Q</b>	0.45	0.49	0.55
<b>v</b>	--	0.20	--
<b>w</b>	--	0.10	--

### Notes:

1. Follow JEDEC TO-236, variation AB.
2. Dimension "D" does NOT include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.25mm per side.
3. Dimension "E" does NOT include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25mm per side.

## Customer Service

### Sales and Service:

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