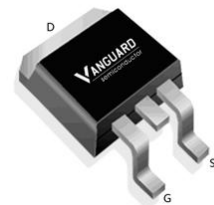


## Features

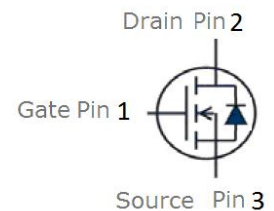
- Enhancement mode
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=10V$
- Fast Switching and High efficiency
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant



$V_{DS}$	100	V
$R_{DS(on),TYP}@ V_{GS}=10V$	4.2	m $\Omega$
$I_D$	200	A

**TO-263**


Part ID	Package Type	Marking	Tape and reel information
VS4110ATD	TO-263	4110ATD	1000pcs/Reel



## Maximum ratings, at $T_A=25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	100	V
$V_{GS}$	Gate-Source voltage	$\pm 25$	V
$I_S$	Diode continuous forward current	$T_C=25^\circ\text{C}$	200 A
$I_D$	Continuous drain current @ $V_{GS}=10V$	$T_C=25^\circ\text{C}$	200 A
		$T_C=100^\circ\text{C}$	142 A
$I_{DM}$	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	800 A
$I_{DSM}$	Continuous drain current @ $V_{GS}=10V$	$T_A=25^\circ\text{C}$	15 A
		$T_A=70^\circ\text{C}$	12 A
EAS	Avalanche energy, single pulsed ②	900	mJ
$P_D$	Maximum power dissipation	$T_C=25^\circ\text{C}$	375 W
		$T_C=100^\circ\text{C}$	188 W
$P_{DSM}$	Maximum power dissipation ③	$T_A=25^\circ\text{C}$	2 W
		$T_A=70^\circ\text{C}$	1.3 W
$T_{STG}, T_J$	Storage and Junction Temperature Range	-55 to 175	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.4	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	$^\circ\text{C/W}$

**Electrical Characteristics**

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(T <sub>J</sub> =25°C)	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T <sub>J</sub> =125°C)	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±25V,V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	2.5	3	3.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance ④	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	--	4.2	5.5	mΩ
		T <sub>J</sub> =100°C	--	5.6	--	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub>= 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V, f=1MHz	7410	8715	10020	pF
C <sub>oss</sub>	Output Capacitance		685	805	925	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		530	620	710	pF
R <sub>g</sub>	Gate Resistance	f=1MHz	--	1.6	--	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =50V,I <sub>D</sub> =40A, V <sub>GS</sub> =10V	--	172	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	36	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	62	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =50V, I <sub>D</sub> =40A, R <sub>G</sub> =3Ω, V <sub>GS</sub> =10V	--	26	--	ns
t <sub>r</sub>	Turn-on Rise Time		--	66	--	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		--	95	--	ns
t <sub>f</sub>	Turn-Off Fall Time		--	66	--	ns
<b>Source- Drain Diode Characteristics@ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =40A,V <sub>GS</sub> =0V	--	0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> =25°C,I <sub>sd</sub> =40A, V <sub>GS</sub> =0V	--	52	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	--	114	--	nC

NOTE: ① Repetitive rating; pulse width limited by max junction temperature.

② Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.5mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 60A, V<sub>GS</sub> =10V. Part not recommended for use above this value

③ The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C.

④ Pulse width ≤ 380μs; duty cycle≤ 2%.

Typical Characteristics

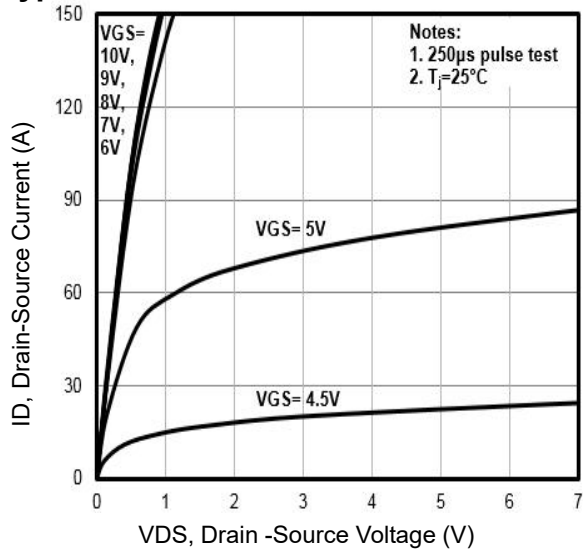


Fig1. Typical Output Characteristics

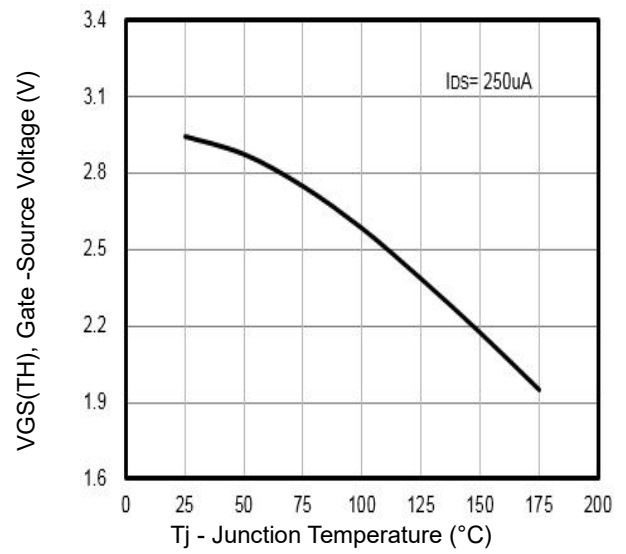


Fig2.  $V_{GS(TH)}$  Gate -Source Voltage Vs.  $T_j$

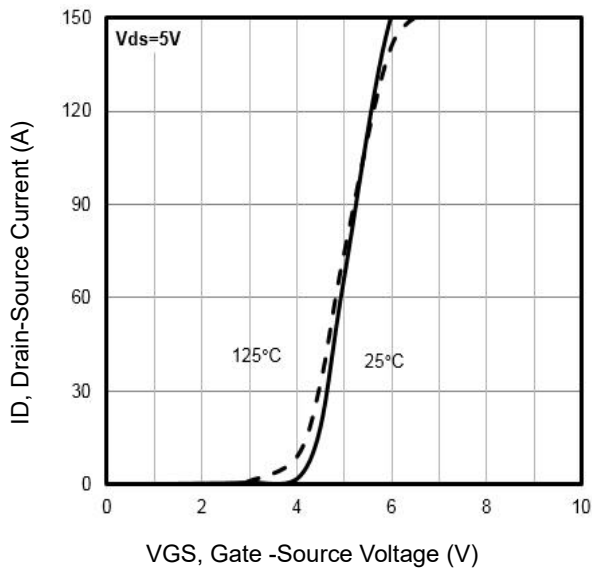


Fig3. Typical Transfer Characteristics

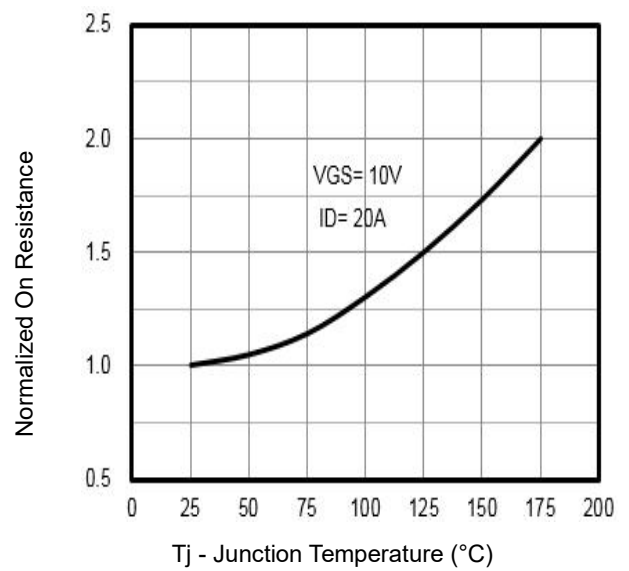


Fig4. Normalized On-Resistance Vs. Temperature

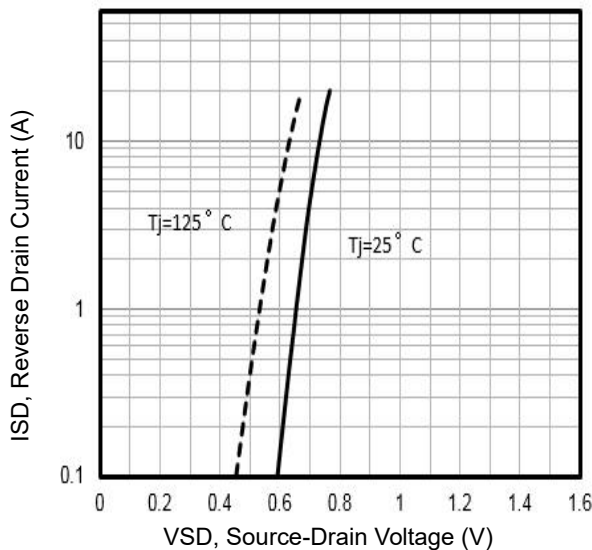


Fig5. Typical Source-Drain Diode Forward Voltage

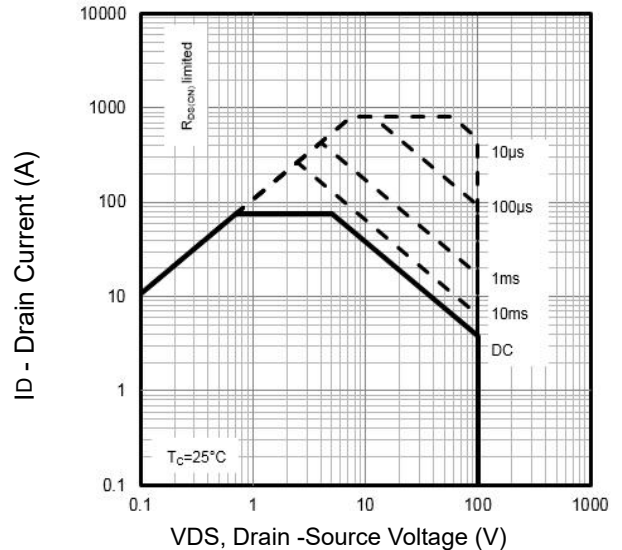


Fig6. Maximum Safe Operating Area

Typical Characteristics

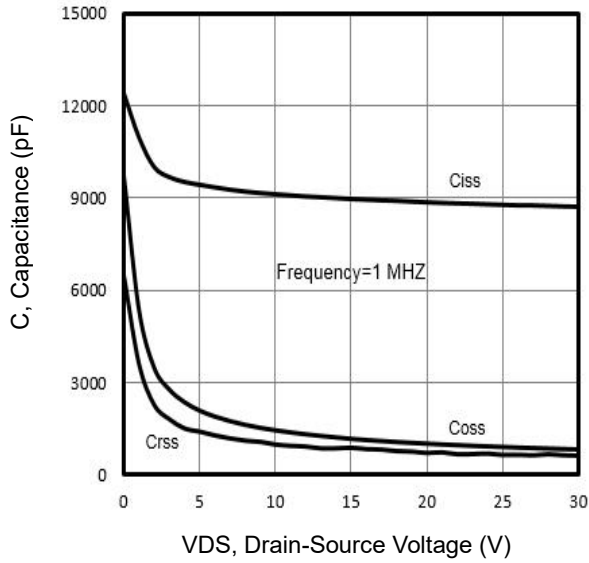


Fig7. Typical Capacitance Vs. Drain-Source Voltage

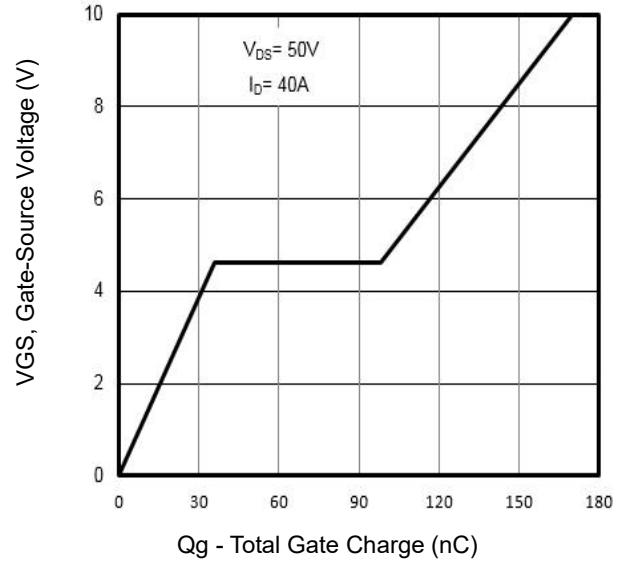


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

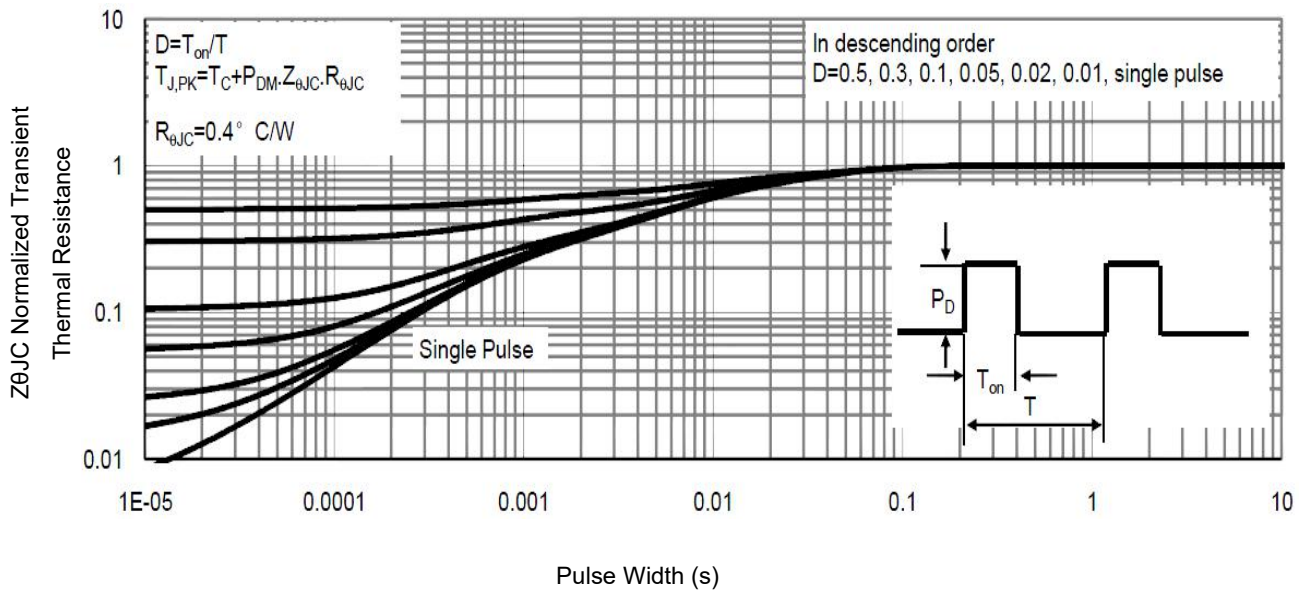


Fig9. Normalized Maximum Transient Thermal Impedance

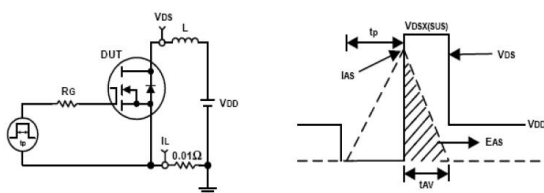


Fig10. Unclamped Inductive Test Circuit and waveforms

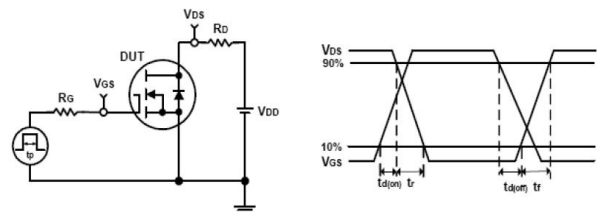
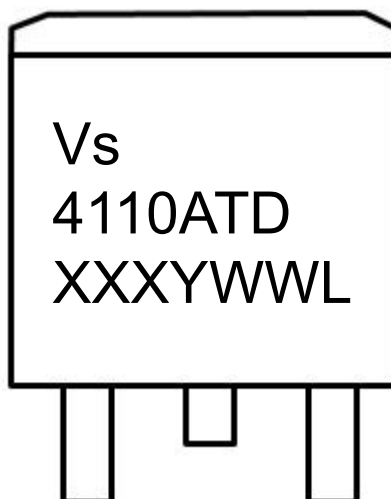


Fig11. Switching Time Test Circuit and waveforms

**Marking Information**



1st line: Vanguard Code (Vs)

2nd line: Part Number (4110ATD)

3rd line: Date code (XXXYWW)

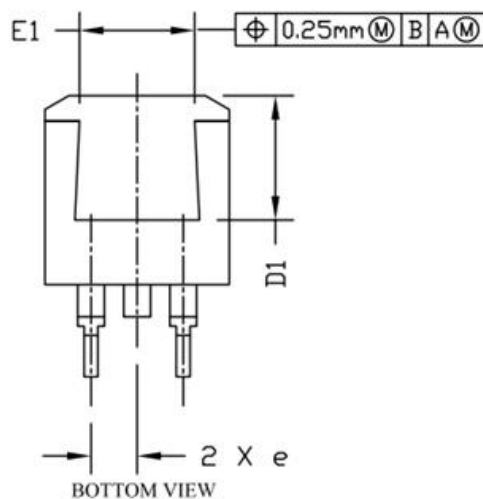
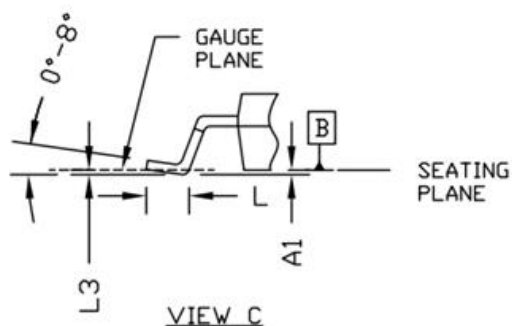
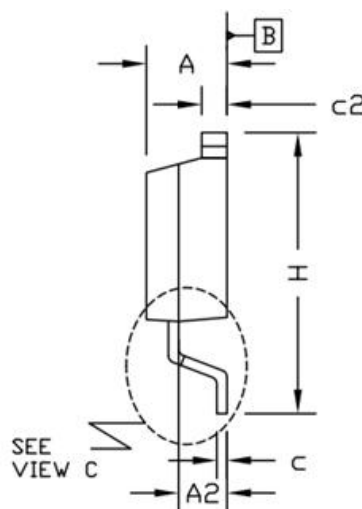
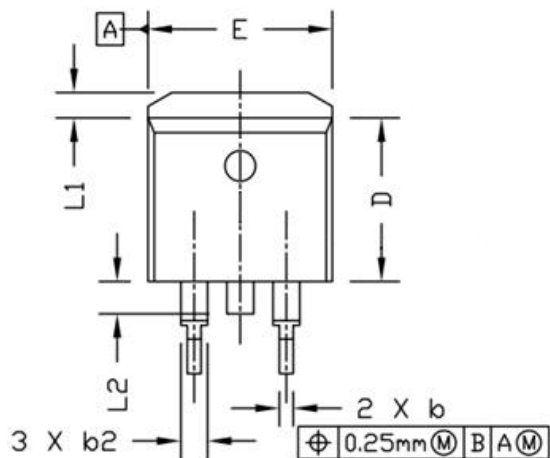
XXX: Wafer Lot Number Code , code changed with Lot Number

Y: Year Code, (e.g. E=2017, F=2018, G=2019, H=2020, etc)

WW: Week Code (01 to 53)

L: Class (A or B)

TO-263 Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	4.400	4.570	4.700
A1	0.000	0.100	0.200
A2	2.300	2.400	2.500
b	0.700	0.800	0.900
b2	1.200	1.270	1.360
c	0.381	0.500	0.737
c2	1.220	1.300	1.350
D	8.600	9.200	9.300
D1	6.860		
e	2.540 BSC		
E	9.780	9.880	10.260
E1	6.225		
H	14.700	15.100	15.500
L	2.000	2.550	2.750
L1	1.000	1.200	1.400
L2	1.300	1.600	1.700
L3	0.255 BSC		

Notes:

1. Refer to JEDEC TO-263 variation AB
2. Dimension "D" & "E" do NOT include mold flash, mold flash shall not exceed 0.127mm per side.

Customer Service

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