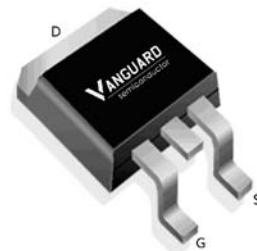


Features

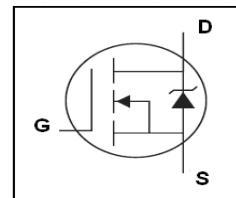
- N-Channel, 5V Logic Level Control
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant

V_{DS}	40	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	1.6	mΩ
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	2.2	mΩ
I_D	290	A

TO-263



Part ID	Package Type	Marking	Tape and reel information
VS4604DM	TO-263	4604DM	1000pcs/Reel



Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	40	V
I_s	Diode continuous forward current	$T_c=25^\circ\text{C}$	A
I_D	Continuous drain current @ $V_{GS}=10$ V	$T_c=25^\circ\text{C}$	A
		$T_c=100^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_c=25^\circ\text{C}$	A
EAS	Avalanche energy, single pulsed ②	156	mJ
P_D	Maximum power dissipation	$T_c=25^\circ\text{C}$	W
V_{GS}	Gate-Source voltage	±20	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.7	°C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	°C/W



Typical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.3	1.6	2.4	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=60\text{A}$	--	1.6	2.5	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=40\text{A}$	--	2.2	3	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	9500	10515	11500	pF
C_{oss}	Output Capacitance		800	890	1000	pF
C_{rss}	Reverse Transfer Capacitance		680	760	850	pF
R_g	Gate Resistance	$f=1\text{MHz}$		1.9		Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=60\text{A}, V_{\text{GS}}=10\text{V}$	--	180	--	nC
Q_{gs}	Gate-Source Charge		--	24	--	nC
Q_{gd}	Gate-Drain Charge		--	30	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=60\text{A}, R_{\text{G}}=3\Omega, V_{\text{GS}}=10\text{V}$	--	20	--	nS
t_r	Turn-on Rise Time		--	22	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	109	--	nS
t_f	Turn-Off Fall Time		--	23	--	nS
Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=60\text{A}, V_{\text{GS}}=0\text{V}$	--	0.8	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=60\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	--	21	--	nS
Q_{rr}	Reverse Recovery Charge			39		nC

NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by $T_{j\text{max}}$, starting $T_j = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_g = 25\Omega$, $I_{AS} = 25\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.



Vanguard
Semiconductor

VS4604DM

40V/290A N-Channel Advanced Power MOSFET

Typical Characteristics

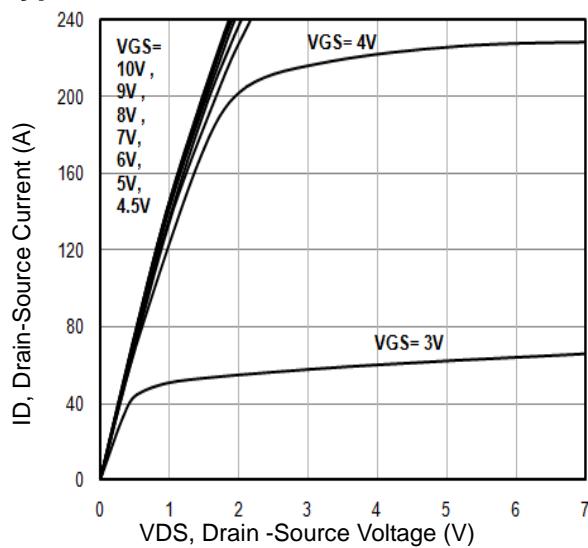


Fig1. Typical Output Characteristics

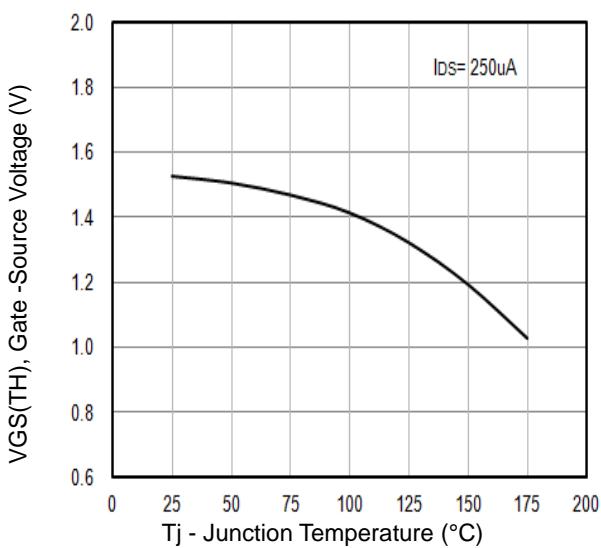


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

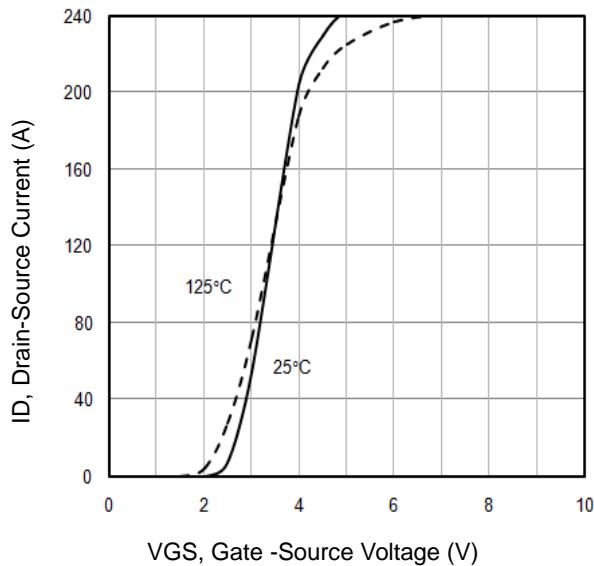


Fig3. Typical Transfer Characteristics

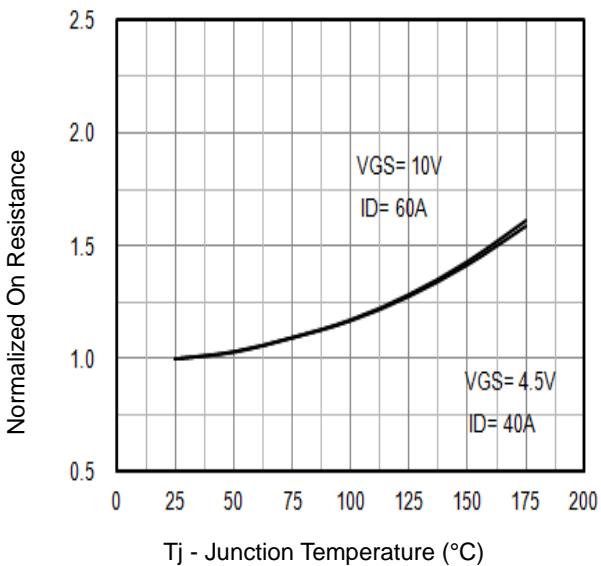


Fig4. Normalized On-Resistance Vs. Temperature

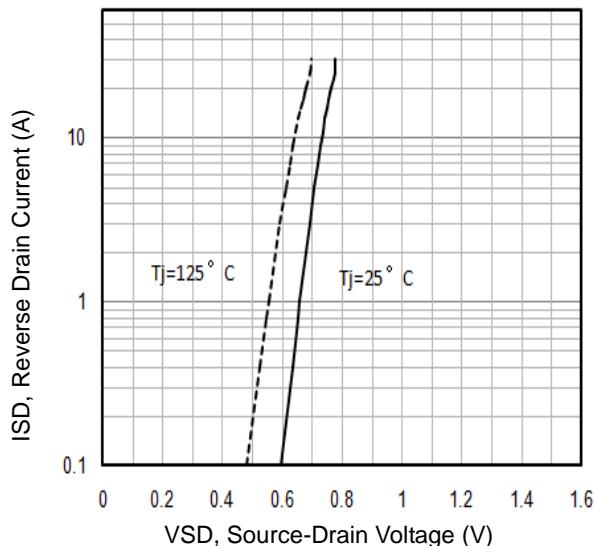


Fig5. Typical Source-Drain Diode Forward Voltage

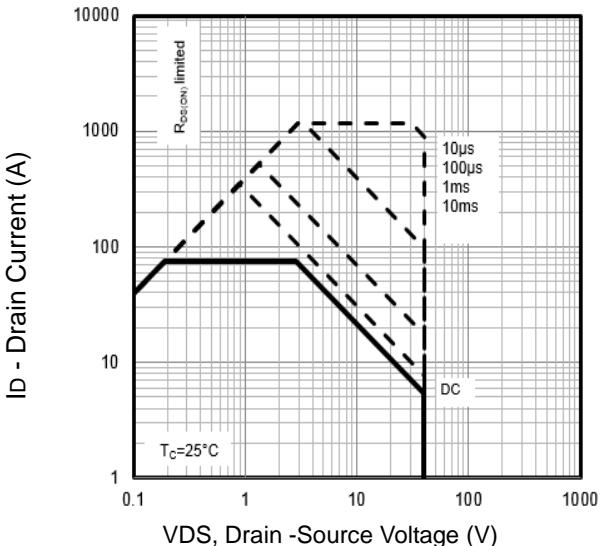
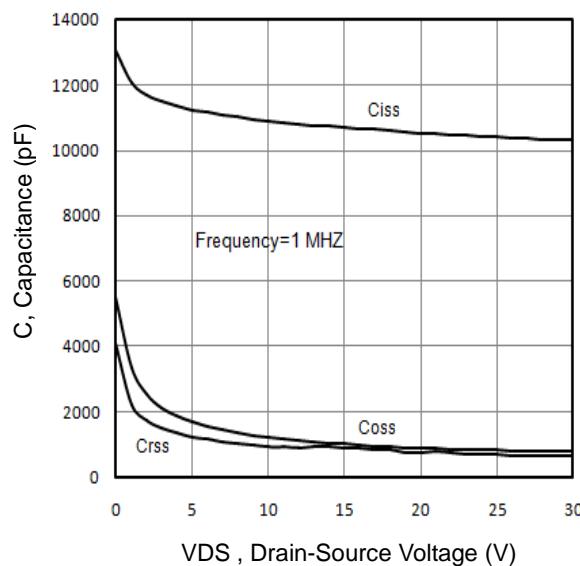


Fig6. Maximum Safe Operating Area

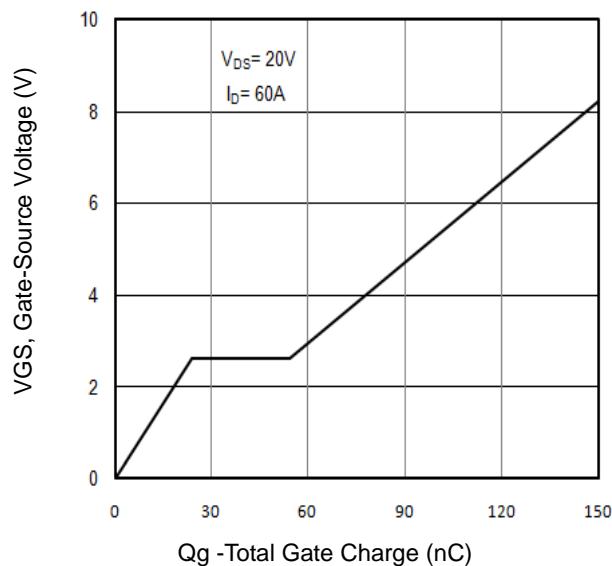


Typical Characteristics



VDS , Drain-Source Voltage (V)

Fig7. Typical Capacitance Vs.Drain-Source Voltage



Qg -Total Gate Charge (nC)

Fig8. Typical Gate Charge Vs.Gate-Source Voltage

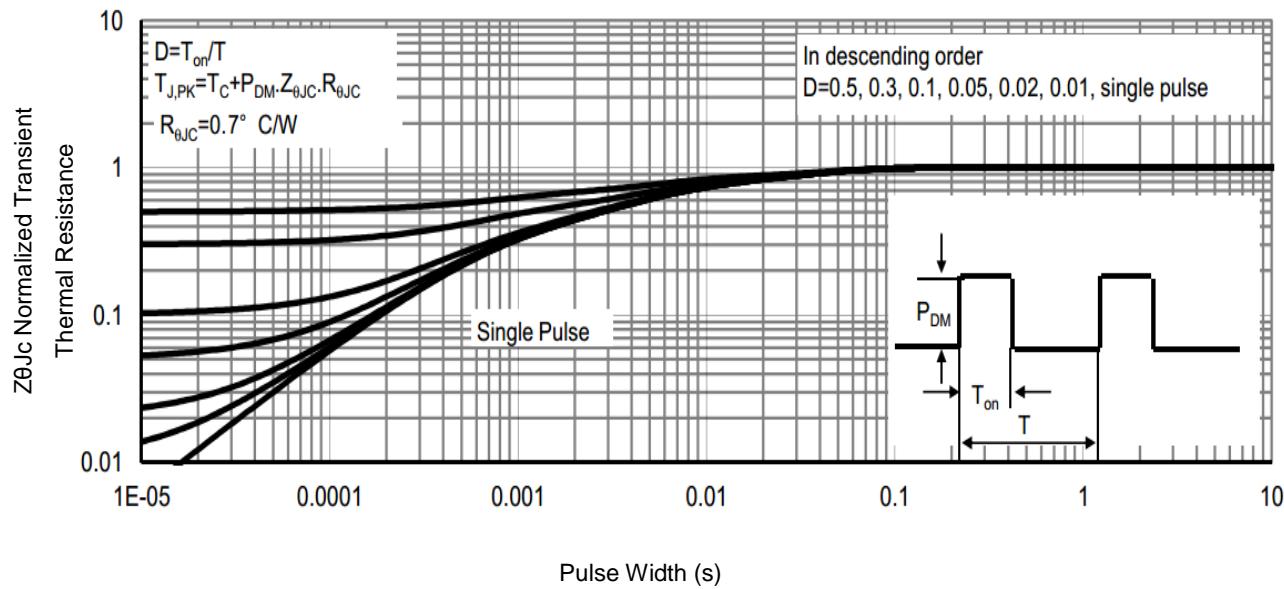


Fig9 . Normalized Maximum Transient Thermal Impedance

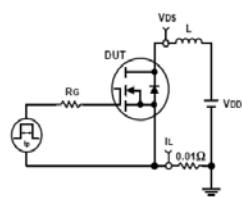


Fig10. Unclamped Inductive Test Circuit and waveforms

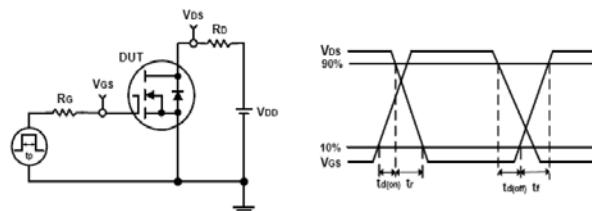
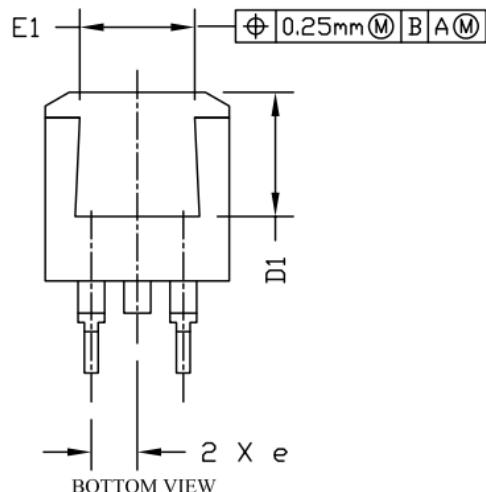
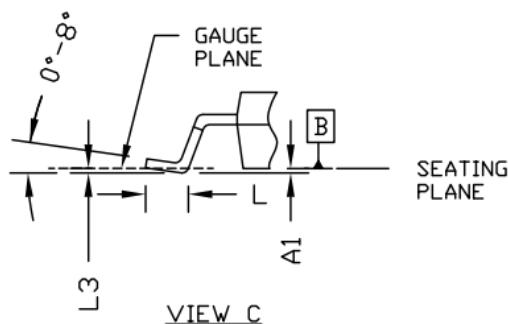
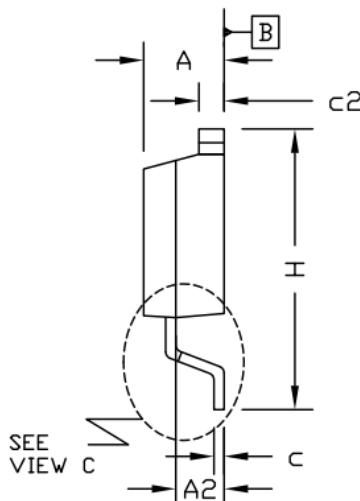
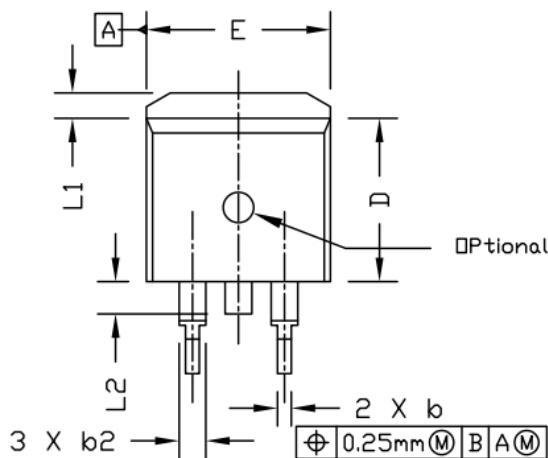


Fig11. Switching Time Test Circuit and waveforms



TO-263 Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	4.400	4.570	4.700
A1	0.000	0.100	0.200
A2	2.300	2.400	2.500
b	0.700	0.800	0.900
b2	1.200	1.270	1.360
c	0.381	0.500	0.737
c2	1.220	1.300	1.350
D	8.600	9.200	9.300
D1	6.860		
e	2.540 BSC		
E	9.780	9.880	10.260
E1	6.225		
H	14.700	15.100	15.500
L	2.000	2.550	2.750
L1	1.000	1.200	1.400
L2	1.300	1.600	1.700
L3	0.255 BSC		

Notes:

1. Refer to JEDEC TO-263 variation AB
2. Dimension "D" & "E" do NOT include mold flash, mold flash shall not exceed 0.127mm per side.

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