

## Features

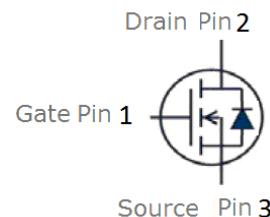
- N-Channel, 5V Logic Level Control
- Enhancement mode
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=4.5$  V
- VitoMOS® Technology
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant



$V_{DS}$	55	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	9.5	$m\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	13.5	$m\Omega$
$I_D$	40	A

**TO-251**


Part ID	Package Type	Marking	Tape and reel information
VS5812AI	TO-251	5812AI	75pcs/Tube



## Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	55	V
$I_s$	Diode continuous forward current	$T_c=25^\circ C$	A
$I_D$	Continuous drain current@ $V_{GS}=10V$	$T_c=25^\circ C$	A
		$T_c=100^\circ C$	A
$I_{DM}$	Pulse drain current tested ①	$T_c=25^\circ C$	A
EAS	Avalanche energy, single pulsed ②	30	mJ
$P_d$	Maximum power dissipation	$T_c=25^\circ C$	W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	2.95	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	100	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	55	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}}=55\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	$\mu\text{A}$
	Zero Gate Voltage Drain Current( $T_j=125^\circ\text{C}$ )	$V_{\text{DS}}=55\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	$\pm 100$	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	--	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance <sup>③</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=15\text{A}$	--	9.5	13	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance <sup>③</sup>	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=10\text{A}$	--	13.5	17	$\text{m}\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	1030	--	pF
$C_{\text{oss}}$	Output Capacitance		--	105	--	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	85	--	pF
$R_g$	Gate Resistance	$f=1\text{MHz}$		3.5		$\Omega$
$Q_g$	Total Gate Charge	$V_{\text{DS}}=30\text{V}, I_{\text{D}}=15\text{A}, V_{\text{GS}}=10\text{V}$	--	23	--	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	5	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	5.2	--	nC
<b>Switching Characteristics</b>						
$t_{d(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=15\text{A}, R_{\text{G}}=6.8\Omega, V_{\text{GS}}=10\text{V}$	--	15	--	nS
$t_r$	Turn-on Rise Time		--	11	--	nS
$t_{d(\text{off})}$	Turn-Off Delay Time		--	43	--	nS
$t_f$	Turn-Off Fall Time		--	8	--	nS
<b>Source- Drain Diode Characteristics@ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{\text{SD}}$	Forward on voltage	$I_{\text{SD}}=15\text{A}, V_{\text{GS}}=0\text{V}$	--	0.82	1.2	V
$t_{rr}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=15\text{A}, \frac{di}{dt}=500\text{A}/\mu\text{s}$	--	21	--	nS
$Q_{rr}$	Reverse Recovery Charge			90		nC

**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by  $T_{j\text{max}}$ , starting  $T_j = 25^\circ\text{C}$ ,  $L = 0.5\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 11\text{A}$ ,  $V_{GS} = 10\text{V}$ . Part not recommended for use above this value
- ③ Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .



## Typical Characteristics

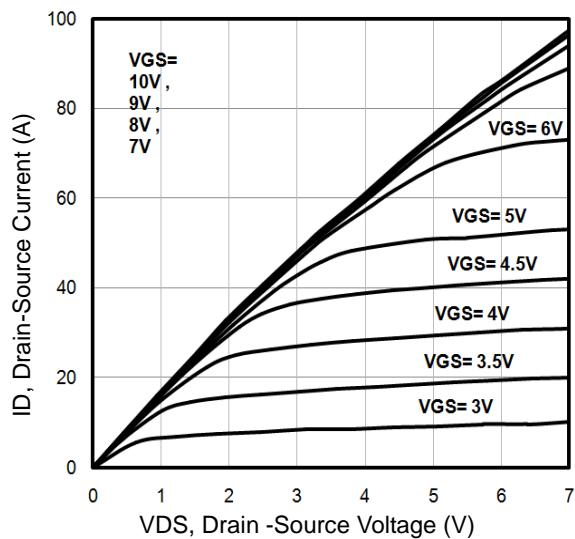


Fig1. Typical Output Characteristics

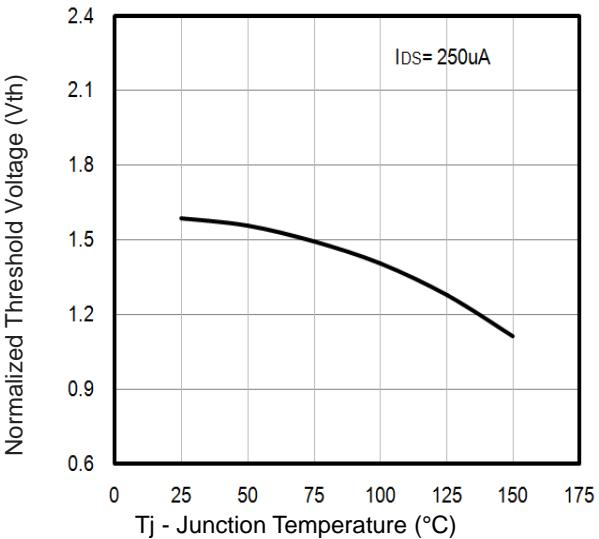


Fig2.  $V_{GS(TH)}$  Gate-Source Voltage Vs.  $T_j$

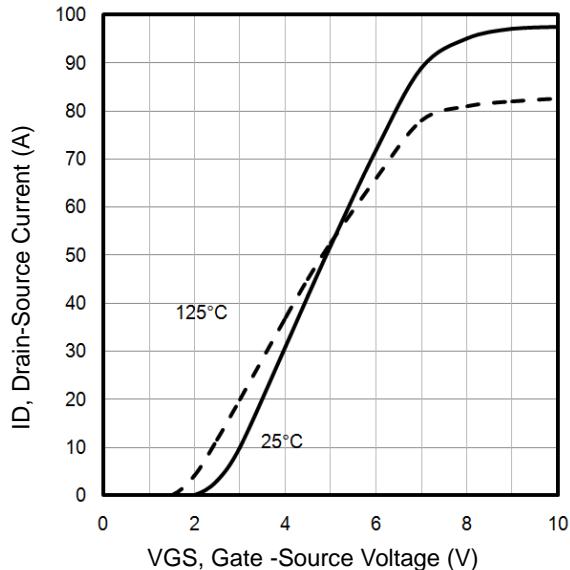


Fig3. Typical Transfer Characteristics

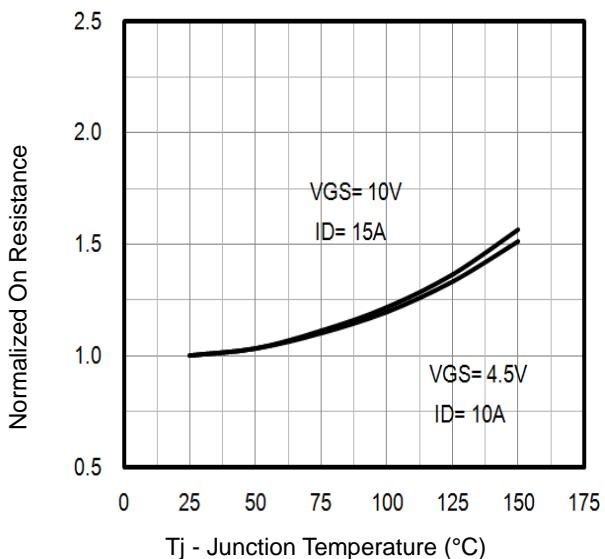


Fig4. Normalized On-Resistance Vs.  $T_j$

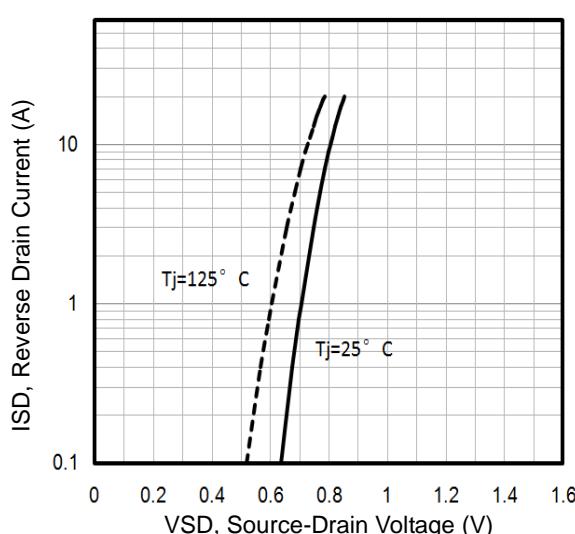


Fig5. Typical Source-Drain Diode Forward Voltage

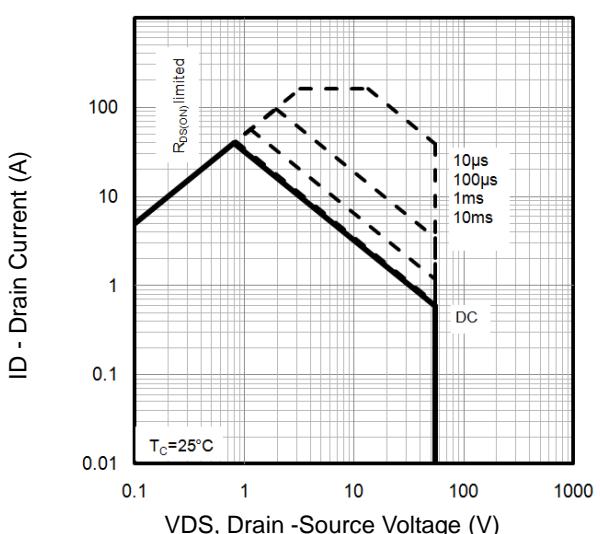


Fig6. Maximum Safe Operating Area



## Typical Characteristics

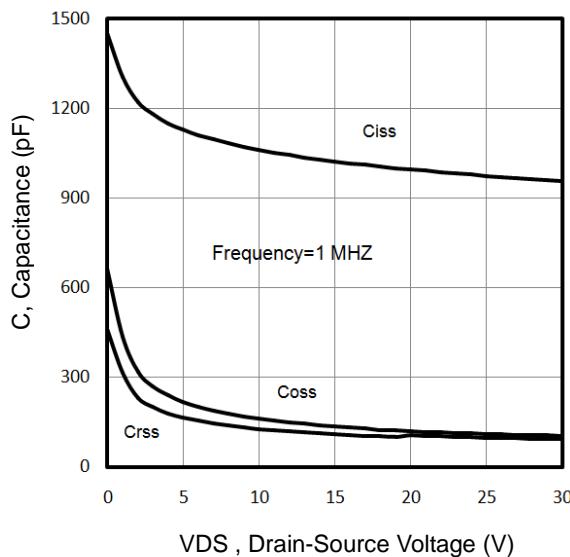


Fig7. Typical Capacitance Vs.Drain-Source Voltage

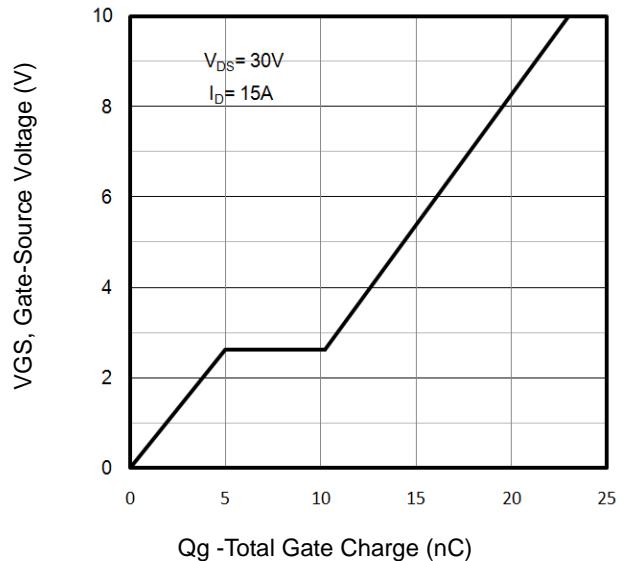


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

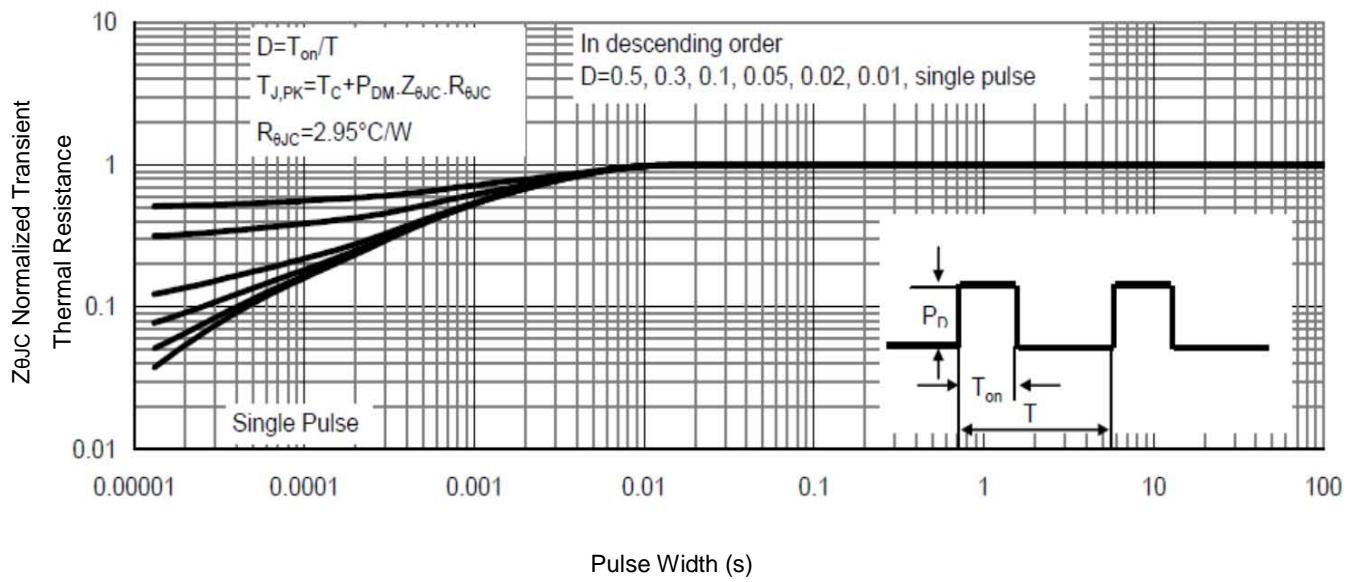


Fig9 . Normalized Maximum Transient Thermal Impedance

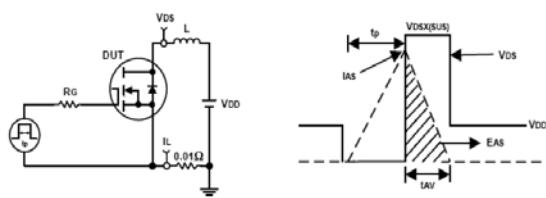


Fig10. Unclamped Inductive Test Circuit and waveforms

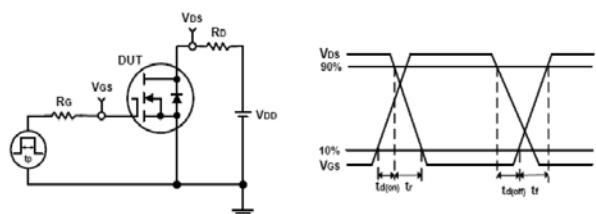
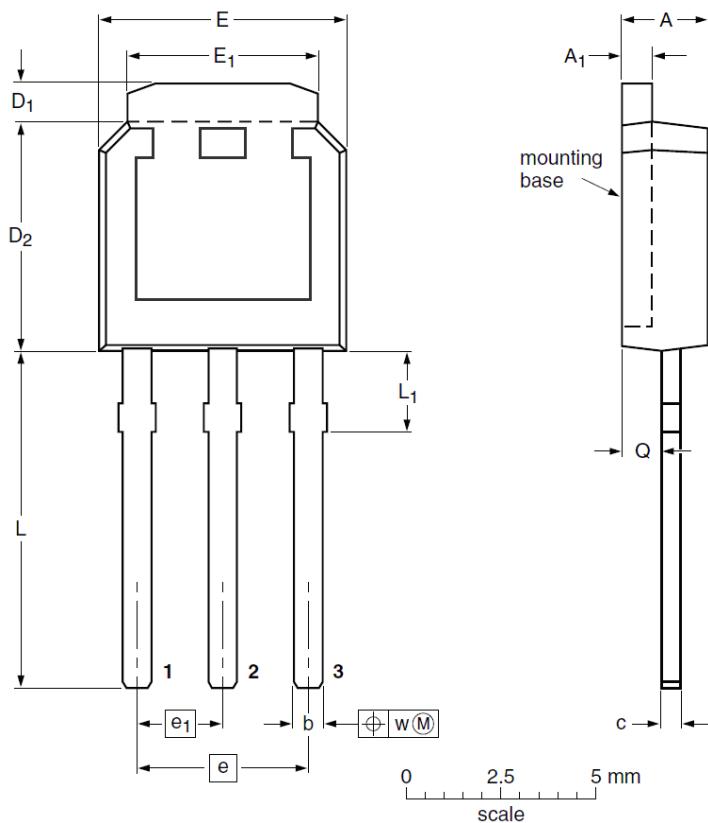


Fig11. Switching Time Test Circuit and waveforms

## TO-251 Package Outline



### DIMENSIONS (unit : mm)

Label	Min	Typ	Max	Label	Min	Typ	Max
<b>A</b>	2.22	2.30	2.38	<b>A<sub>1</sub></b>	0.46	0.55	0.93
<b>b</b>	0.71	0.78	0.89	<b>c</b>	0.46	0.51	0.56
<b>D<sub>1</sub></b>	0.96	1.02	1.10	<b>D<sub>2</sub></b>	5.98	6.05	6.22
<b>E</b>	6.47	6.60	6.73	<b>E<sub>1</sub></b>	5.20	5.33	5.55
<b>e</b>	--	4.57	--	<b>e<sub>1</sub></b>	--	2.28	--
<b>L</b>	9.00	9.38	9.60	<b>L<sub>1</sub></b>	--	2.70	--
<b>Q</b>	1.00	1.05	1.10	<b>w</b>	--	0.30	--

## Customer Service

### Sales and Service:

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