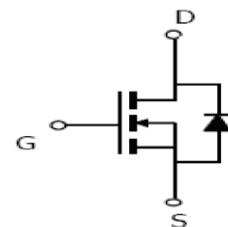


Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V
- Fast Switching
- Pb-free lead plating; RoHS compliant

V_{DS}	60	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	69	$\text{m}\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5$ V	85	$\text{m}\Omega$
I_D	3.2	A

SOT23-3L



Part ID	Package Type	Marking	Tape and reel information
VS6640AL	SOT23-3L	VS07	3000pcs/reel

Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	60	V
I_s	Diode continuous forward current	$T_A=25^\circ\text{C}$	A
I_D	Continuous drain current @ $V_{GS}=10$ V	$T_A=25^\circ\text{C}$	A
		$T_A=100^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_A=25^\circ\text{C}$	A
P_D	Maximum power dissipation	$T_A=25^\circ\text{C}$	W
V_{GS}	Gate-Source voltage	± 20	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	60	°C/W
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	100	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1.3	2.0	2.4	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=10\text{V}, I_D=5\text{A}$	--	69	86	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}, I_D=4\text{A}$	--	85	107	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	400	505	600	pF
C_{oss}	Output Capacitance		--	30	80	pF
C_{rss}	Reverse Transfer Capacitance		--	20	60	pF
R_g	Gate Resistance	f=1MHz		5.8		Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}, I_D=5\text{A}, V_{\text{GS}}=10\text{V}$	--	11	--	nC
Q_{gs}	Gate-Source Charge		--	4	--	nC
Q_{gd}	Gate-Drain Charge		--	3.5	--	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}, I_D=5\text{A}, R_G=3\Omega, V_{\text{GS}}=10\text{V}$	--	7.5	--	nS
t_r	Turn-on Rise Time		--	4.5	--	nS
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	22.5	--	nS
t_f	Turn-Off Fall Time		--	9	--	nS
Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=5\text{A}, V_{\text{GS}}=0\text{V}$	--	0.9	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=5\text{A}, \frac{di}{dt}=500\text{A}/\mu\text{s}$	--	10	--	nS
Q_{rr}	Reverse Recovery Charge			15		nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics

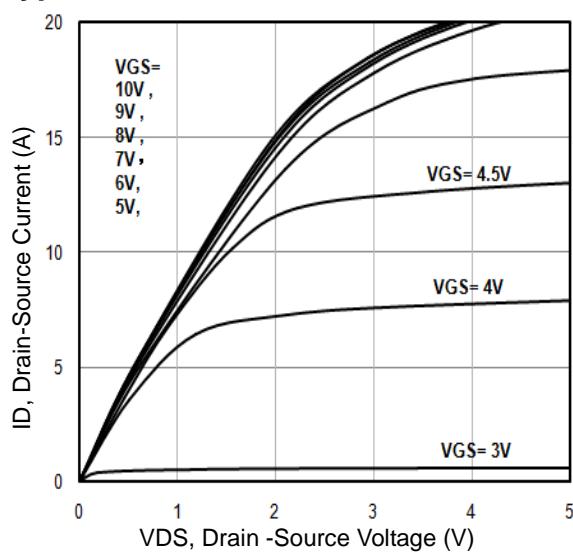


Fig1. Typical Output Characteristics

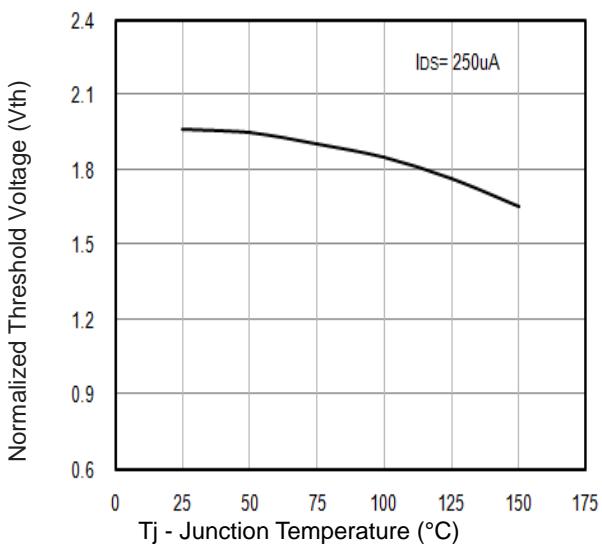


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

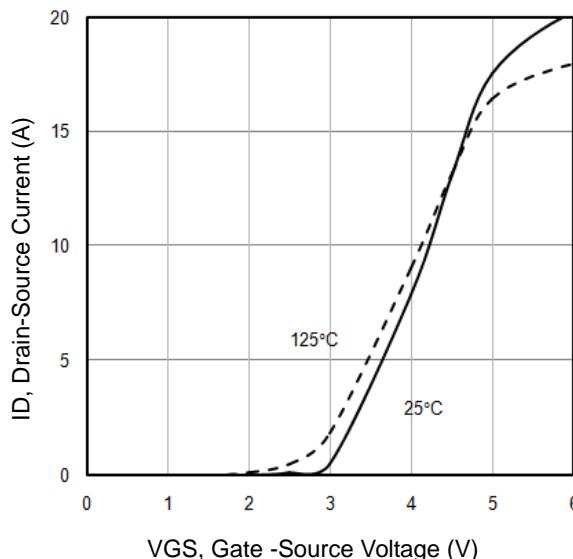


Fig3. Typical Transfer Characteristics

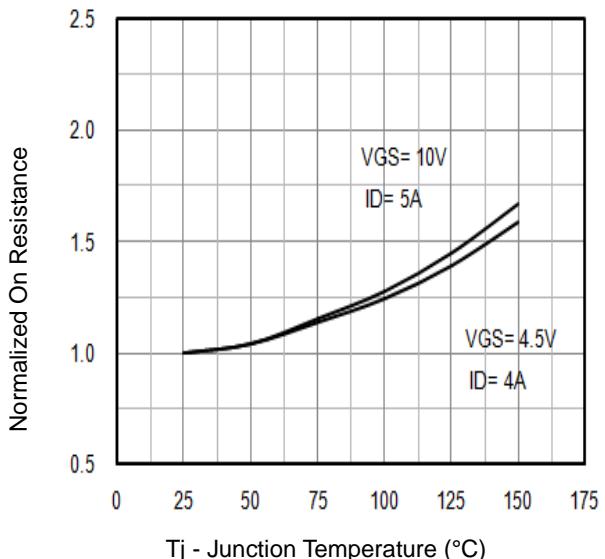


Fig4. Normalized On-Resistance Vs. T_j

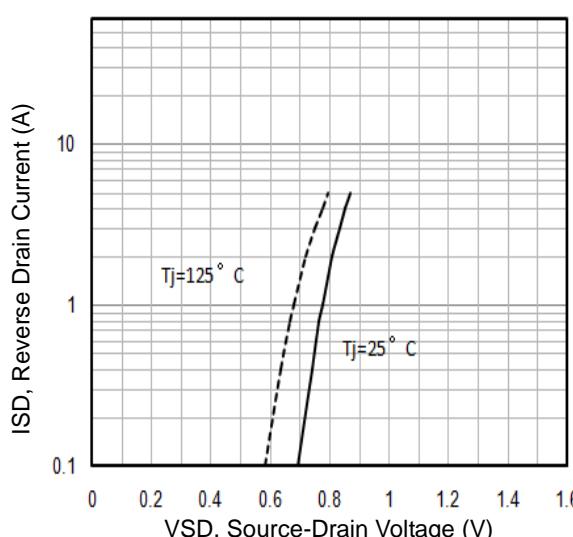


Fig5. Typical Source-Drain Diode Forward Voltage

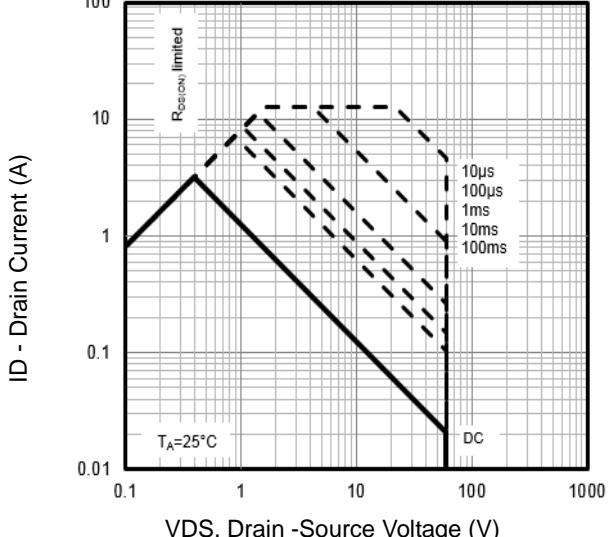


Fig6. Maximum Safe Operating Area

Typical Characteristics

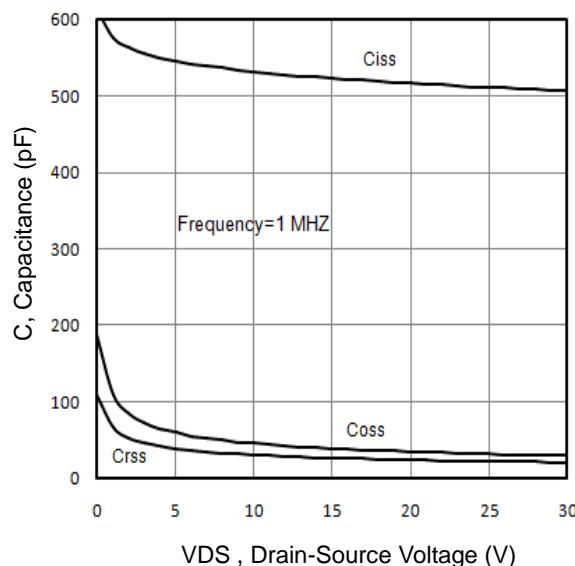


Fig7. Typical Capacitance Vs.Drain-Source Voltage

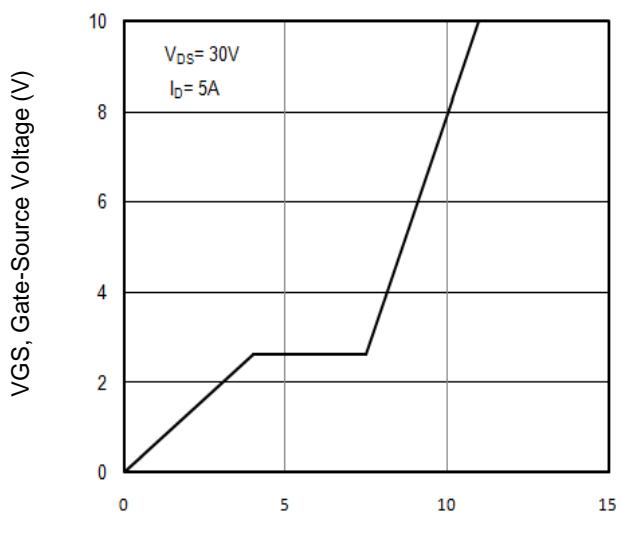


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

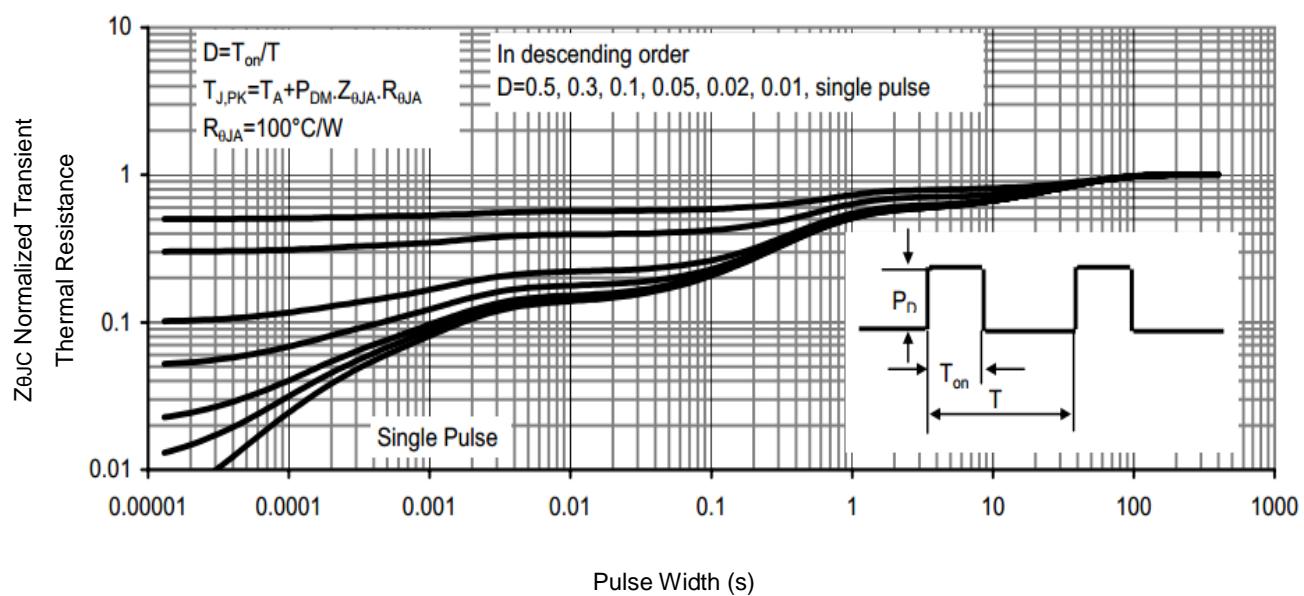


Fig9 . Normalized Maximum Transient Thermal Impedance

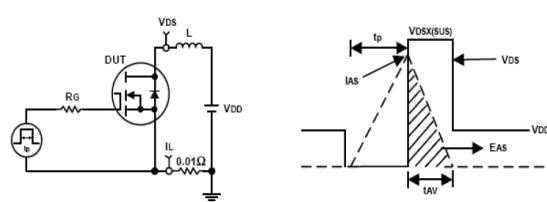


Fig10. Unclamped Inductive Test Circuit and waveforms

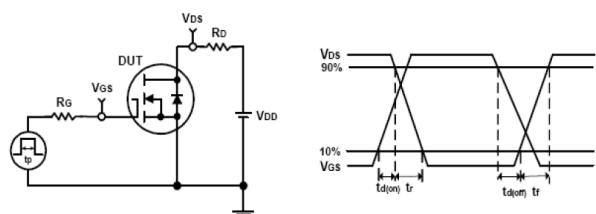
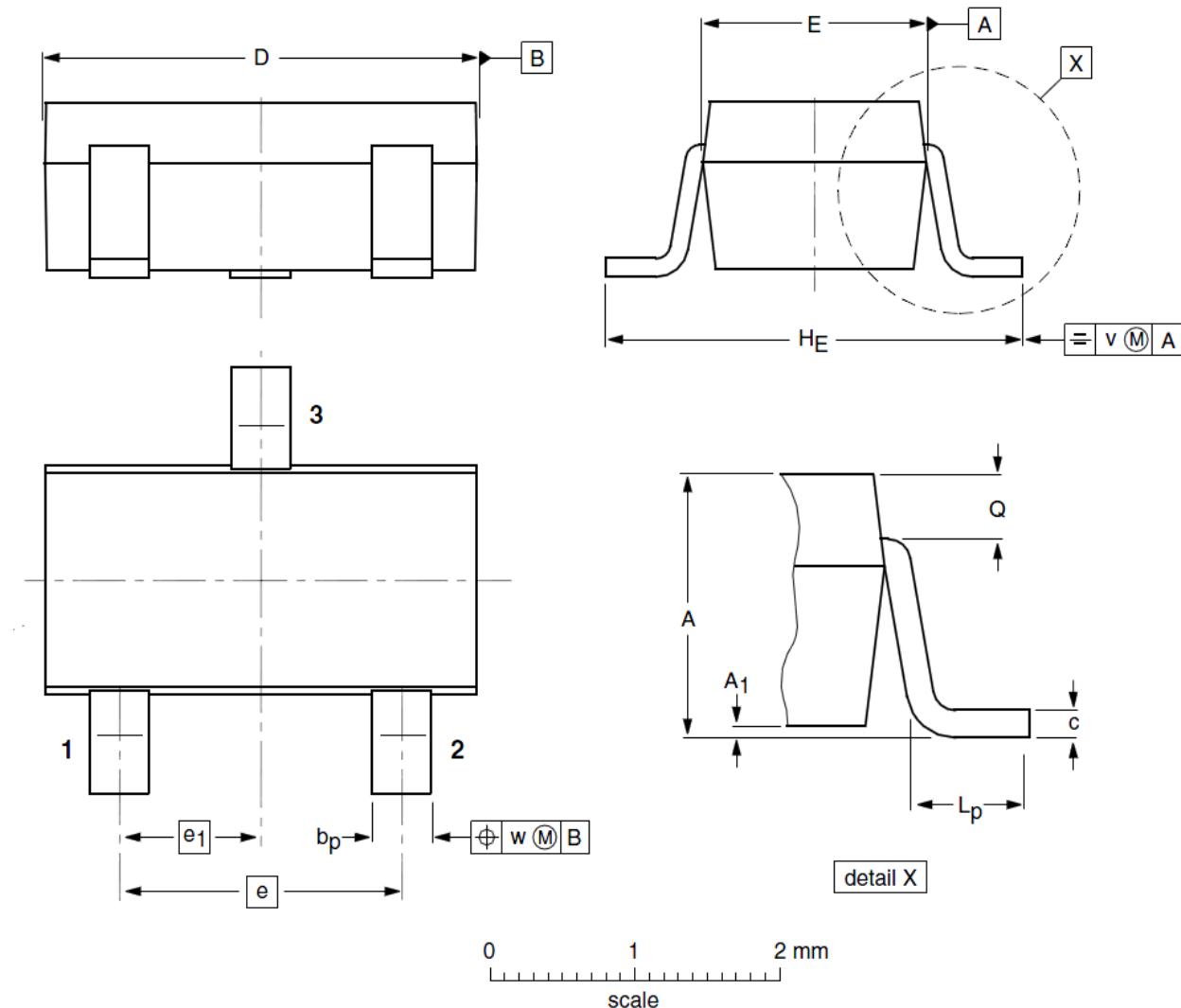


Fig11. Switching Time Test Circuit and waveforms

SOT23-3L Package Outline Data


Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	0.90	1.07	1.25
A₁	0.01	0.06	0.10
b_p	0.30	0.35	0.50
c	0.10	0.15	0.20
D	2.70	2.92	3.10
E	1.30	1.60	1.70
e	--	1.90	--
e₁	--	0.95	--
H_E	2.50	2.80	3.00
L_p	0.30	0.40	0.60
Q	0.23	0.29	0.33
v	--	0.20	--
w	--	0.20	--

Customer Service

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