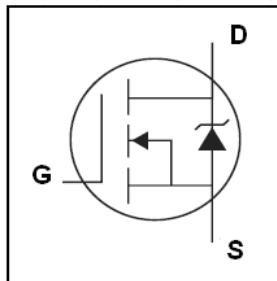


Features

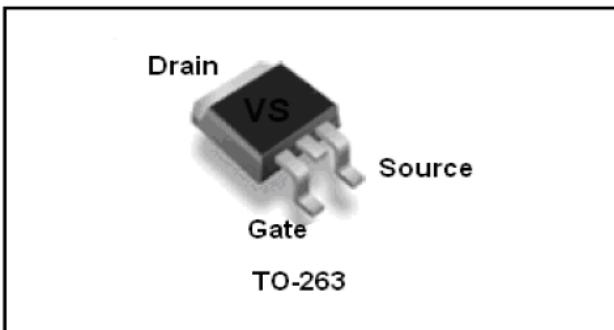
- ◆ Low On-Resistance
- ◆ Fast Switching
- ◆ 100% Avalanche Tested
- ◆ Repetitive Avalanche Allowed up to Tjmax
- ◆ Lead-Free, RoHS Compliant

Description

VS6888ATD designed by the trench processing techniques to achieve extremely low on-resistance. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating . These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.



$V_{DSS} \geq 65V$
 $R_{DS(on)} = 6.0m\Omega$
 $I_D = 88A$



Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (TA) is 25°C, unless otherwise specified.

Symbol	Parameter	Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)			
V_{GS}	Gate-Source Voltage	± 20	V
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	65	V
T_J	Maximum Junction Temperature	175	°C
T_{STG}	Storage Temperature Range	-55 to 175	°C
I_S	Diode Continuous Forward Current	$T_c = 25^\circ C$	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested ①	$T_c = 25^\circ C$	330	A
I_D	Continuous Drain current@ $V_{GS}=10V$	$T_c = 25^\circ C$	88	A
P_D	Maximum Power Dissipation	$T_c = 25^\circ C$	160	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		0.9	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		62.5	°C/W

Drain-Source Avalanche Ratings

EAS	Avalanche Energy, Single Pulsed ②	330	mJ
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Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	65	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=65\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=65\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	2	3	4	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=10\text{V}$, $I_D=35\text{A}$	--	6	8	$\text{m}\Omega$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=25\text{V}$, $I_D=30\text{A}$	--	65	--	S
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	2860	--	pF
C_{oss}	Output Capacitance		--	400	--	pF
C_{rss}	Reverse Transfer Capacitance		--	230	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}$, $I_D=40\text{A}$, $V_{\text{GS}}=10\text{V}$	--	70	--	nC
Q_{gs}	Gate-Source Charge		--	13	--	nC
Q_{gd}	Gate-Drain Charge		--	22	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}$, $I_D=1\text{A}$, $R_G=6.8\Omega$, $V_{\text{GS}}=10\text{V}$	--	15	--	nS
t_r	Turn-on Rise Time		--	14	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	28	--	nS
t_f	Turn-Off Fall Time		--	25	--	nS
Source- Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
I_{SD}	Source-drain current(Body Diode)	$T_c=25^\circ\text{C}$	--	--	88	A
V_{SD}	Forward on voltage	$I_{\text{SD}}=40\text{A}$, $V_{\text{GS}}=0\text{V}$	--	--	1.3	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}$, $I_{\text{sd}}=40\text{A}$, $V_{\text{GS}}=0\text{V}$ $dI/dt=100\text{A}/\mu\text{s}$	--	65	--	nS
Q_{rr}	Reverse Recovery Charge		--	85	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by $T_{J\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 28\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics

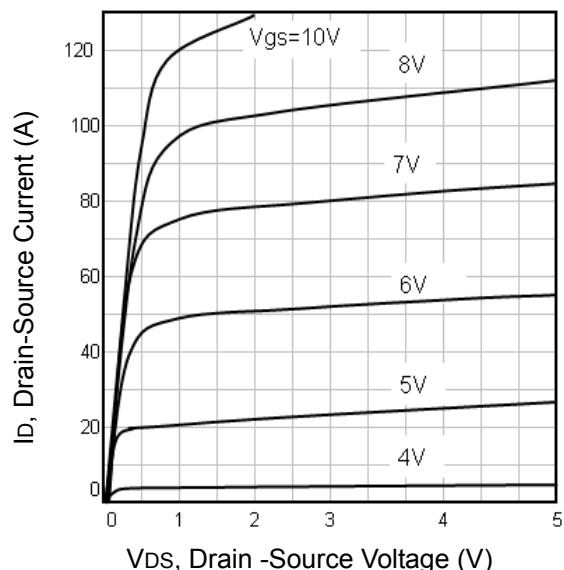


Fig1. Typical Output Characteristics

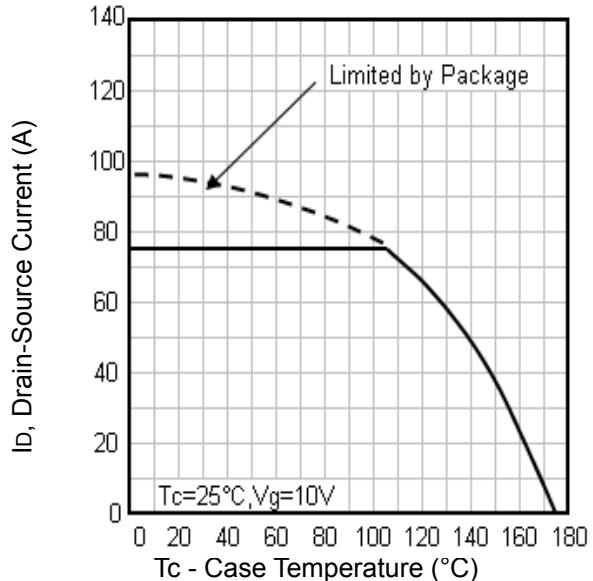


Fig2. Maximum Drain Current Vs. Case Temperature

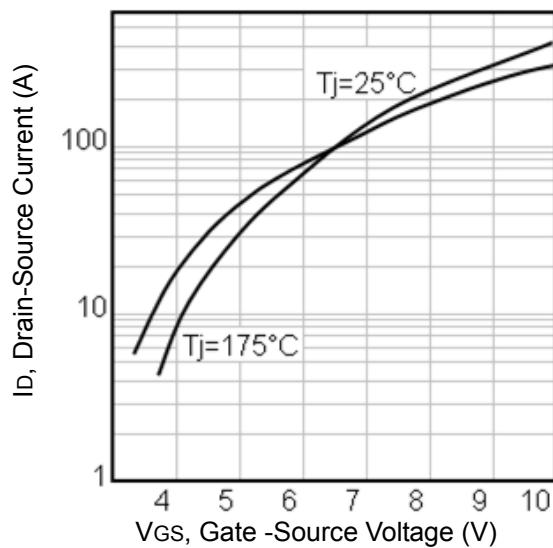


Fig3. Typical Transfer Characteristics

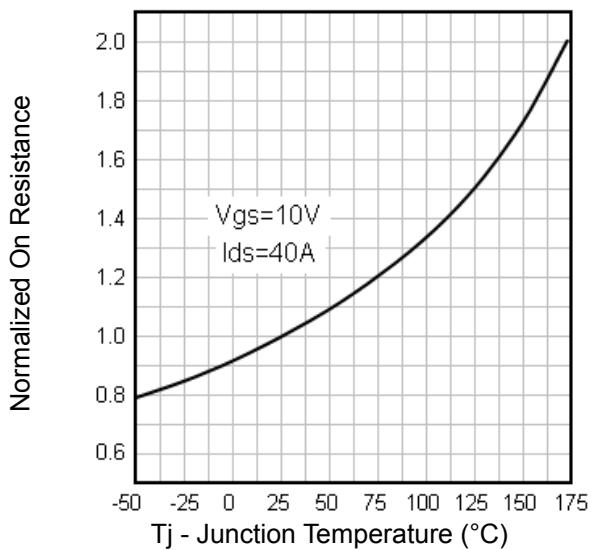


Fig4. Normalized On-Resistance Vs. Temperature

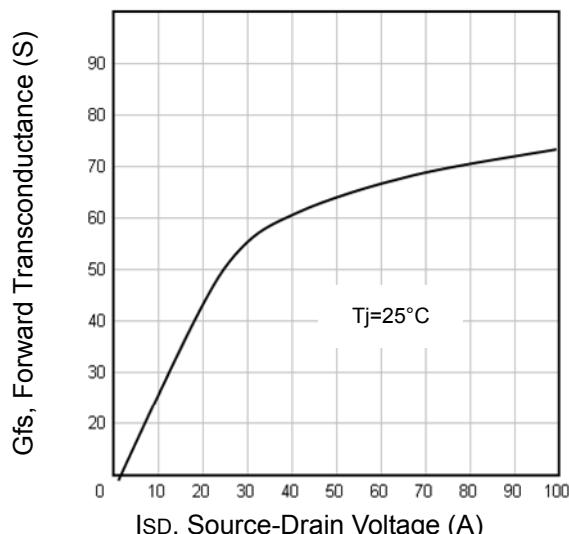


Fig5. Typical Forward Transconductance Vs. Drain Current

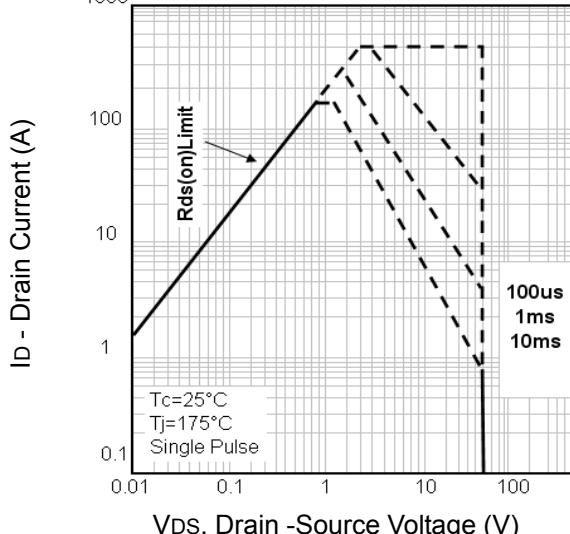


Fig6. Maximum Safe Operating Area

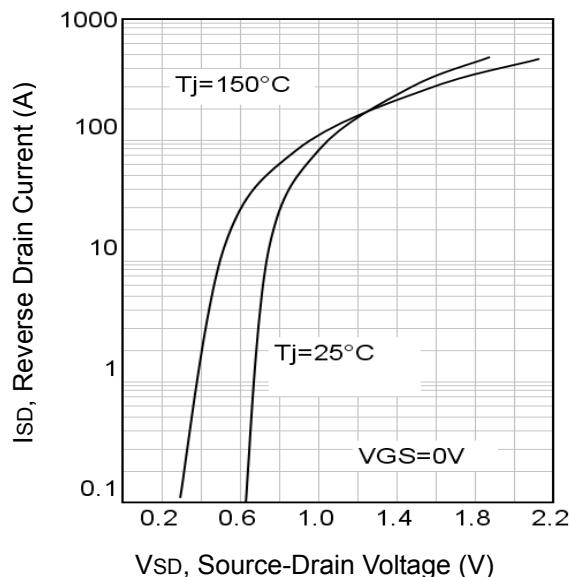


Fig7. Typical Source-Drain Diode Forward Voltage

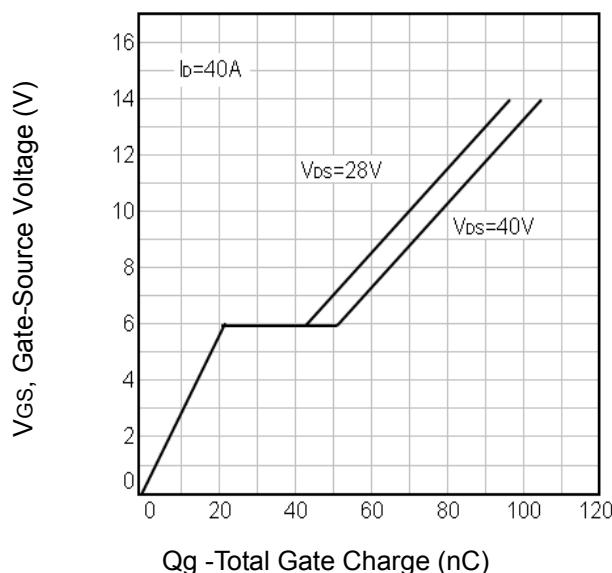


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

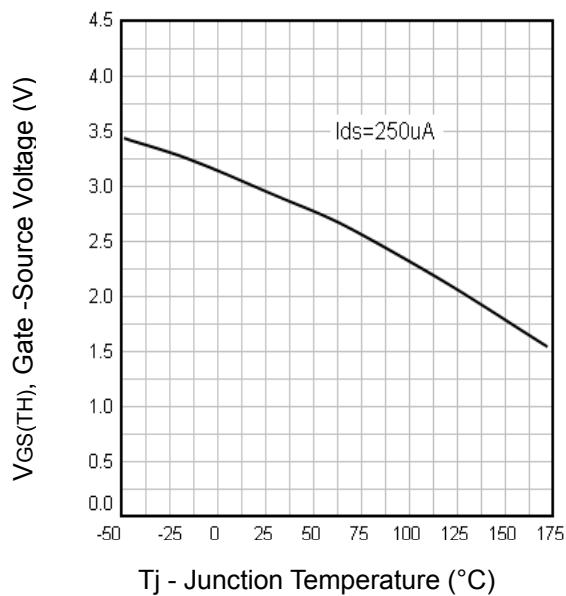


Fig9. Threshold Voltage Vs. Temperature

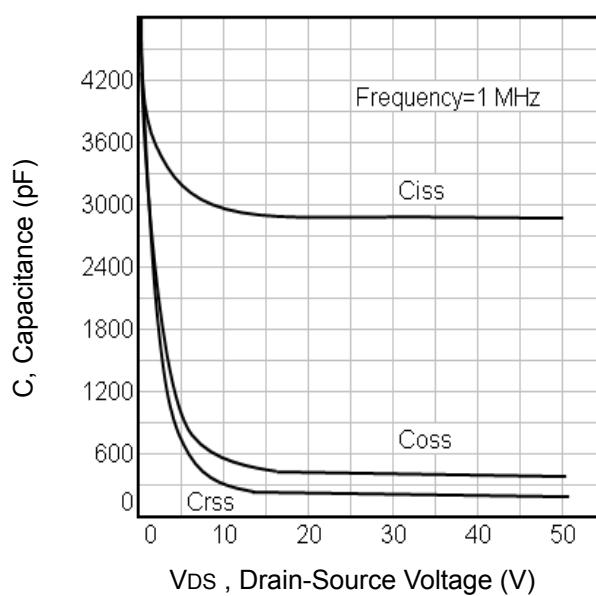


Fig10. Typical Capacitance Vs.Drain-Source Voltage

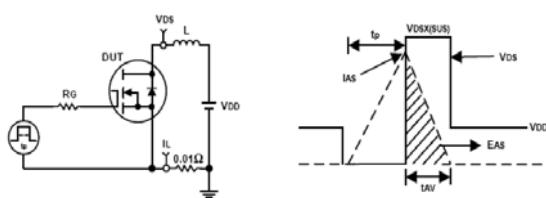


Fig11. Unclamped Inductive Test Circuit and waveforms

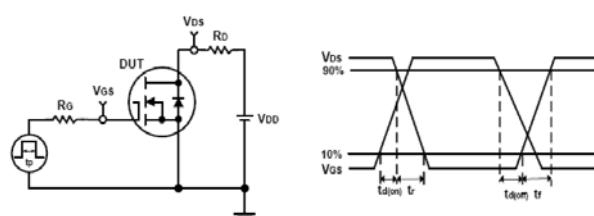
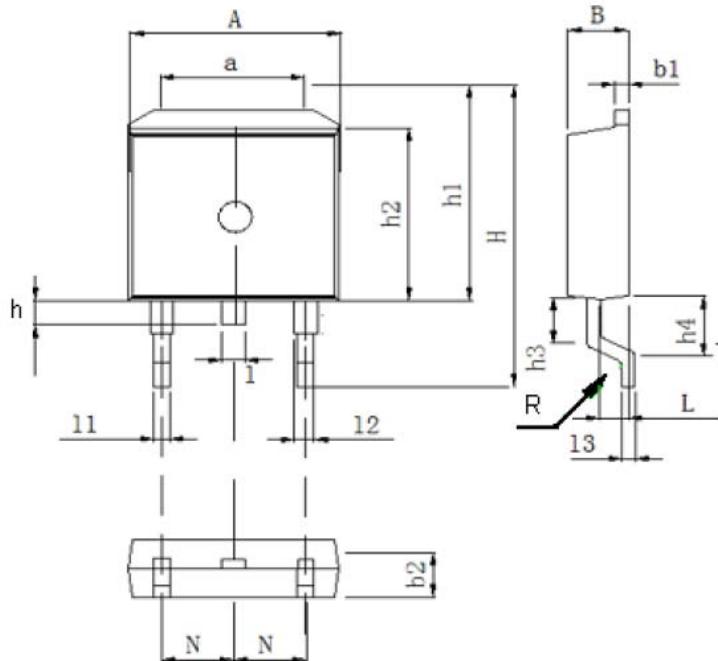


Fig12. Switching Time Test Circuit and waveforms

TO-263 Package Outline



DIM	MILLIMETERS
A	9.8±0.2
a	7.4±0.2
B	4.5±0.2
b1	1.3±0.05
b2	2.4±0.2
H	15.5±0.3
h	1.54±0.2
h1	10.5±0.2
h2	9.2±0.1
h3	1.54±0.2
h4	2.7±0.2
L	2.4±0.2
1	1.3±0.1
11	0.8±0.1
12	1.3±0.1
13	0.5±0.1
N	2.45

Order Information

Product	Marking	Package	Packaging	Min Unit Quantity
VS6888ATD	VS6888ATD	TO-263	50PCS/Tube	1000PCS

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Sales and Service:

sales@vgsemi.com

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