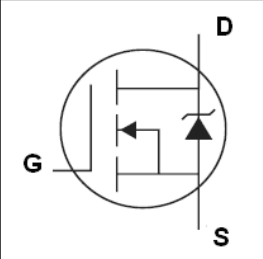


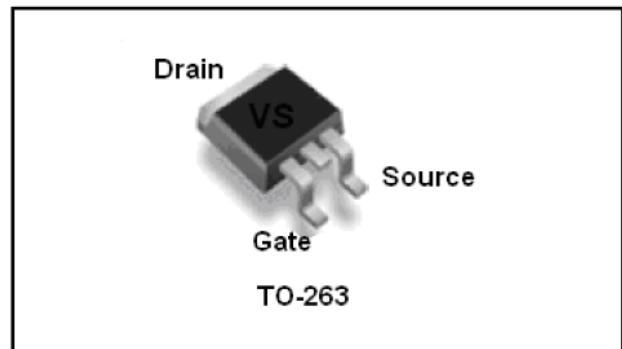
Features

- ◆ Low On-Resistance
- ◆ Fast Switching
- ◆ 100% Avalanche Tested
- ◆ Repetitive Avalanche Allowed up to T_{jmax}
- ◆ Lead-Free, RoHS Compliant

Description

VS8066ATD designed by the trench processing techniques to achieve extremely low on-resistance. fast switching speed and improved repetitive avalanche rating. These features combineto make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.

	V_{DSS}	80V
	$R_{DS(on)}$	9m Ω
	I_D	66A



Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only;and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified.

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
VGS	Gate-Source Voltage		±20	V
V(BR)DSS	Drain-Source Breakdown Voltage		80	V
TJ	Maximum Junction Temperature		150	°C
TSTG	Storage TemperatureRange		-55 to 150	°C
IS	Diode Continuous Forward Current	TC=25°C	60	A
Mounted on Large Heat Sink				
IDM	Pulse Drain Current Tested ①	TC=25°C	260	A
ID	Continuous Draincurrent@VGS=10V	TC=25°C	66	A
PD	Maximum Power Dissipation	TC=25°C	130	W
RθJC	Thermal Resistance-Junction to Case		0.96	°C/W
RθJA	Thermal Resistance Junction-Ambient		62.5	°C/W
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed ②		225	mJ

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	80	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(Tc=25°C)	V _{DS} =80V, V _{GS} =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)		--	--	100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
R _{DS(ON)}	Drain-Source On-State Resistance③	V _{GS} =10V, b=40A	--	9	11	mΩ
Dynamic Electrical Characteristics @ T _J = 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V, f=1MHz	--	2780	--	pF
C _{oss}	Output Capacitance		--	295	--	pF
C _{rss}	Reverse Transfer Capacitance		--	175	--	pF
Q _g	Total Gate Charge	V _{DS} =40V, I _D =30A, V _{GS} =10V	--	55	--	nC
Q _{gs}	Gate-Source Charge		--	14	--	nC
Q _{gd}	Gate-Drain Charge		--	16	--	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =40V, I _D =1A, R _G =6.8Ω, V _{GS} =10V	--	13.6	--	nS
t _r	Turn-on Rise Time		--	10	--	nS
t _{d(off)}	Turn-Off Delay Time		--	48	--	nS
t _f	Turn-Off Fall Time		--	20	--	nS
Source- Drain Diode Characteristics@ T _J = 25°C (unless otherwise stated)						
I _{SD}	Source-drain current(Body Diode)	T _c =25°C	--	--	66	A
V _{SD}	Forward on voltage	I _{SD} =30A, V _{GS} =0V	--	0.85	1.3	V
t _{rr}	Reverse Recovery Time	T _j =25°C, I _{sd} =30A, V _{GS} =0V di/dt=100A/μs	--	43	--	nS
Q _{rr}	Reverse Recovery Charge		--	62	--	nC

NOTE:

Repetitive rating; pulse width limited by max. junction temperature.

Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH, R_G = 25Ω, I_{AS} = 30A, V_{GS}=10V. Part not recommended for use above this value

Pulse width ≤ 300μs; duty cycle ≤ 2%.

Typical Characteristics

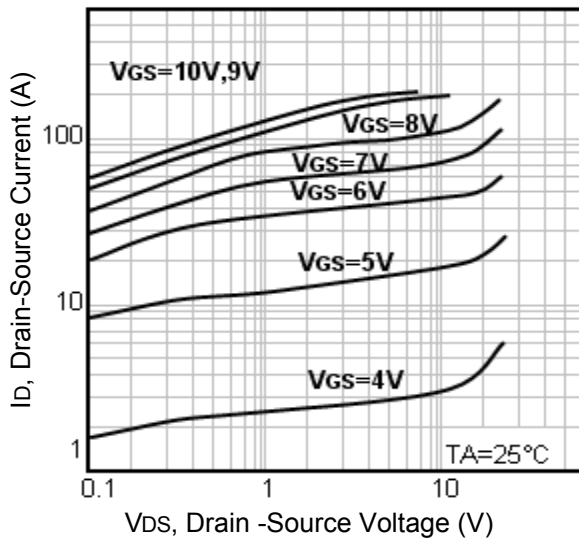


Fig1. Typical Output Characteristics

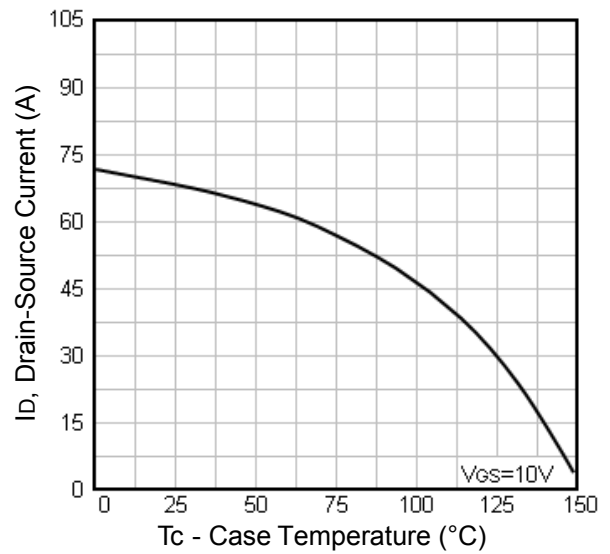


Fig2. Maximum Drain Current Vs. Case Temperature

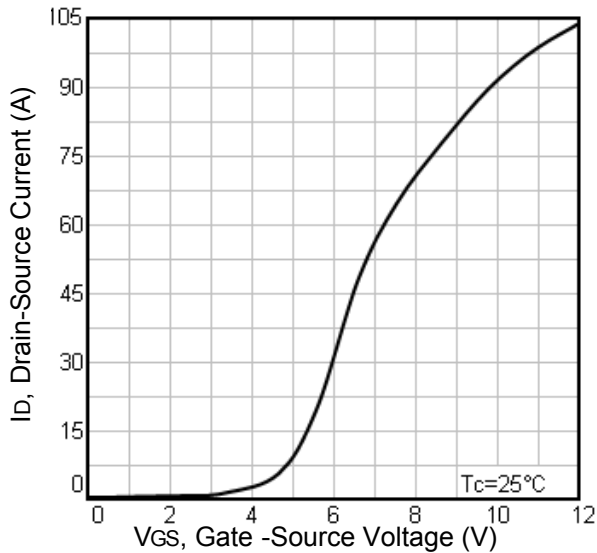


Fig3. Typical Transfer Characteristics

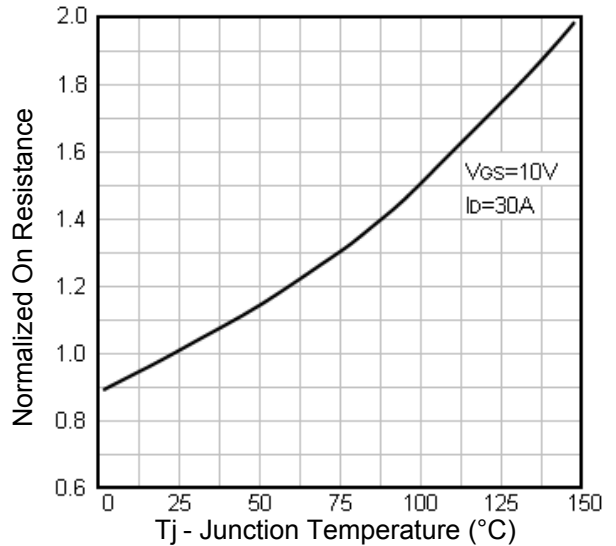


Fig4. Normalized On-Resistance Vs. Temperature

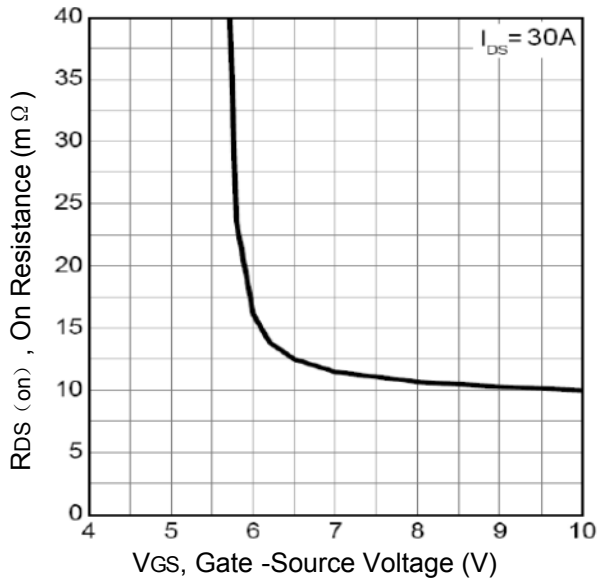


Fig5. Typical VGS Vs. On Resistance

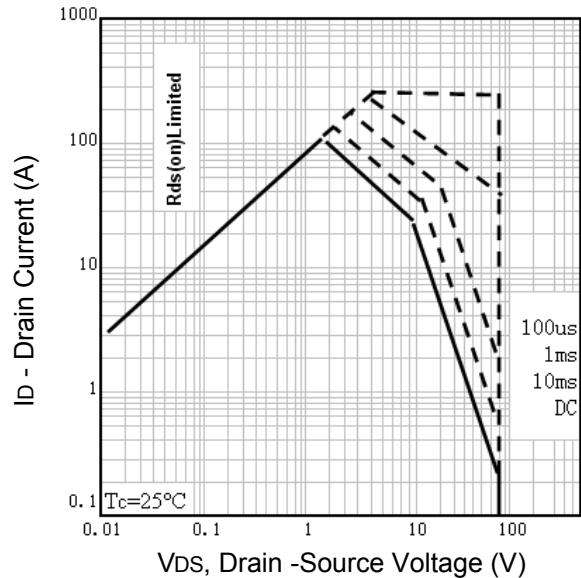


Fig6. Maximum Safe Operating Area

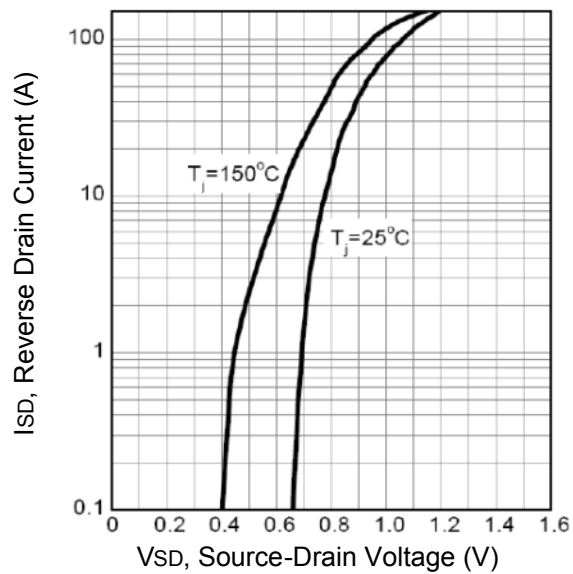


Fig7. Typical Source-Drain Diode Forward Voltage

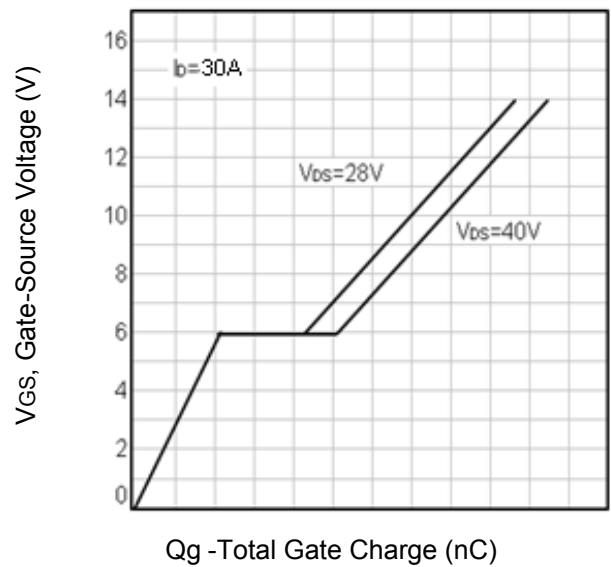


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

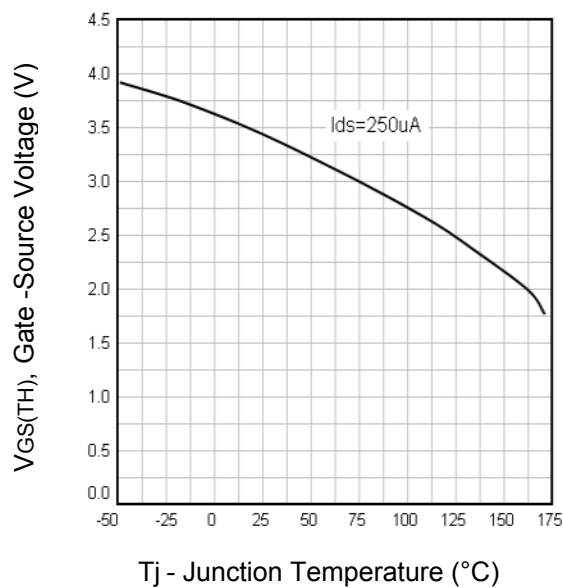


Fig9. Threshold Voltage Vs. Temperature

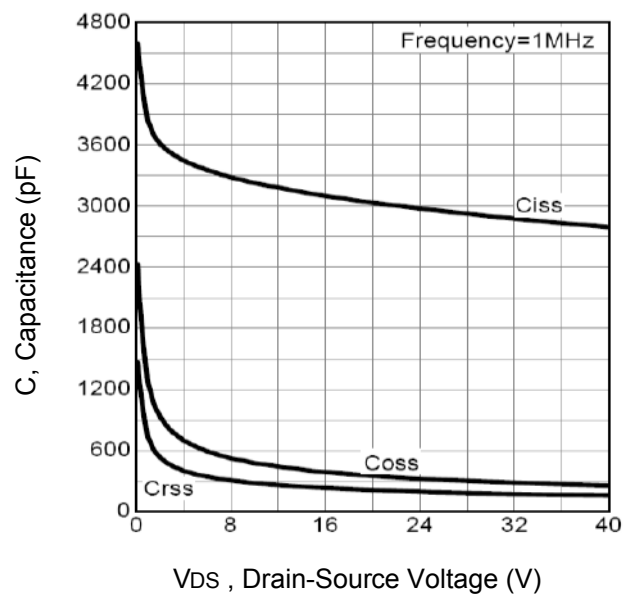


Fig10. Typical Capacitance Vs. Drain-Source Voltage

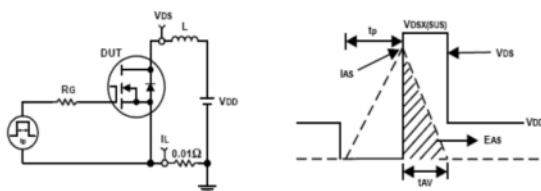


Fig11. Unclamped Inductive Test Circuit and waveforms

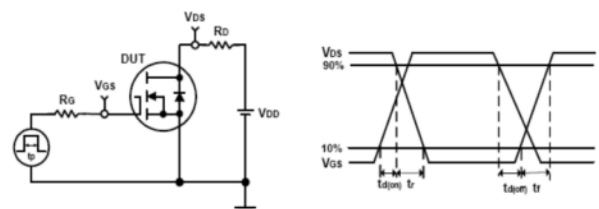
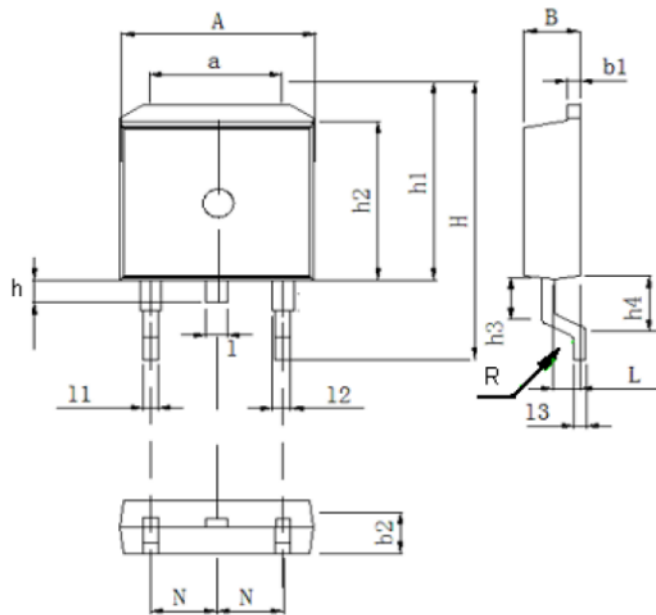


Fig12. Switching Time Test Circuit and waveforms

TO-263 Package Outline



DIM	MILLIMETERS
A	9.8 ± 0.2
a	7.4 ± 0.2
B	4.5 ± 0.2
b1	1.3 ± 0.05
b2	2.4 ± 0.2
H	15.5 ± 0.3
h	1.54 ± 0.2
h1	10.5 ± 0.2
h2	9.2 ± 0.1
h3	1.54 ± 0.2
h4	2.7 ± 0.2
L	2.4 ± 0.2
1	1.3 ± 0.1
11	0.8 ± 0.1
12	1.3 ± 0.1
13	0.5 ± 0.1
N	2.45

Order Information

Product	Marking	Package	Packaging	Min Unit Quantity
VS8066ATD	VS8066ATD	TO-263	800PCS/Reel	1600PCS

Customer Service

Sales and Service:

sales@vgsemi.com

Shen Zhen Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86 -755) -26907027

WEB: www.vgsemi.com