

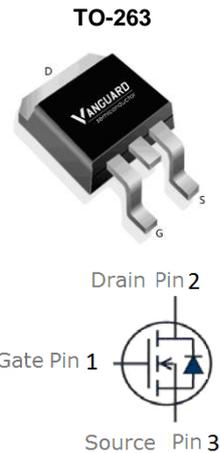
## Features

- N-Channel, 10V Logic Level Control
- Enhancement mode
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=10\text{ V}$
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant



Part ID	Package Type	Marking	Tape and reel information
VS80N08ATD	TO-263	80N08ATD	1000PCS/Reel

$V_{DS}$	80	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	7.2	m $\Omega$
$I_D$	105	A



## Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	80	V
$I_S$	Diode continuous forward current	$T_A=25^\circ\text{C}$ 105	A
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_A=25^\circ\text{C}$ 105	A
		$T_A=100^\circ\text{C}$ 75	A
$I_{DM}$	Pulse drain current tested ①	$T_A=25^\circ\text{C}$ 420	A
EAS	Avalanche energy, single pulsed ②	498	mJ
$P_D$	Maximum power dissipation	$T_A=25^\circ\text{C}$ 156	W
$V_{GS}$	Gate-Source voltage	$\pm 25$	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 175	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.96	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^\circ\text{C/W}$

**Typical Characteristics**

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	80	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=80V, V_{GS}=0V$	--	--	1	$\mu A$
	Zero Gate Voltage Drain Current( $T_j=125^\circ\text{C}$ )	$V_{DS}=80V, V_{GS}=0V$	--	--	100	$\mu A$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	--	--	$\pm 100$	nA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.4	3.0	3.6	V
$R_{DS(ON)}$	Drain-Source On-State Resistance <sup>③</sup>	$V_{GS}=10V, I_D=40A$	--	7.2	8	m $\Omega$
<b>Dynamic Electrical Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=30V, V_{GS}=0V,$ $f=1\text{MHz}$	3000	4110	5000	pF
$C_{oss}$	Output Capacitance		200	395	600	pF
$C_{rss}$	Reverse Transfer Capacitance		100	255	400	pF
$R_g$	Gate Resistance	$f=1\text{MHz}$	--	0.9	--	$\Omega$
$Q_g$	Total Gate Charge	$V_{DS}=40V, I_D=40A,$ $V_{GS}=10V$	--	70	--	nC
$Q_{gs}$	Gate-Source Charge		--	17	--	nC
$Q_{gd}$	Gate-Drain Charge		--	21	--	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD}=40V,$ $I_D=40A,$ $R_G=3\Omega,$ $V_{GS}=10V$	--	20	--	nS
$t_r$	Turn-on Rise Time		--	37	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	46	--	nS
$t_f$	Turn-Off Fall Time		--	25	--	nS
<b>Source- Drain Diode Characteristics @ <math>T_j = 25^\circ\text{C}</math> (unless otherwise stated)</b>						
$V_{SD}$	Forward on voltage	$I_{SD}=40A, V_{GS}=0V$	--	0.9	1.2	V
$t_{rr}$	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{sd}=40A,$ $V_{GS}=0V$ $di/dt=100A/\mu s$	--	55	--	nS
$Q_{rr}$	Reverse Recovery Charge				100	

**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by  $T_{Jmax}$ , starting  $T_j = 25^\circ\text{C}$ ,  $L = 0.5\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 35A$ ,  $V_{GS} = 10V$ . Part not recommended for use above this value
- ③ Pulse width  $\leq 300\mu s$ ; duty cycle  $\leq 2\%$ .

Typical Characteristics

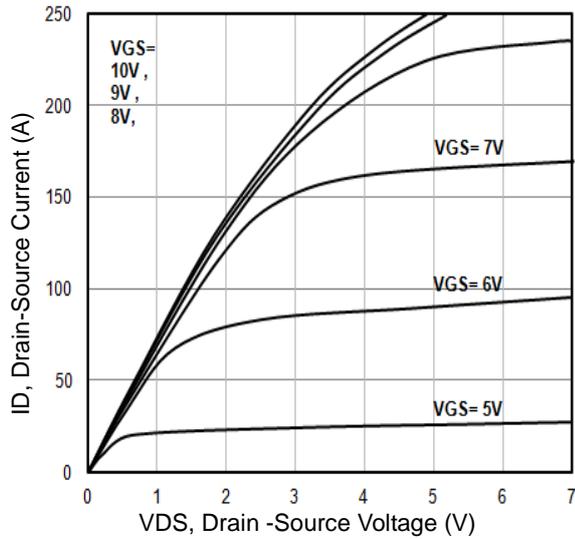


Fig1. Typical Output Characteristics

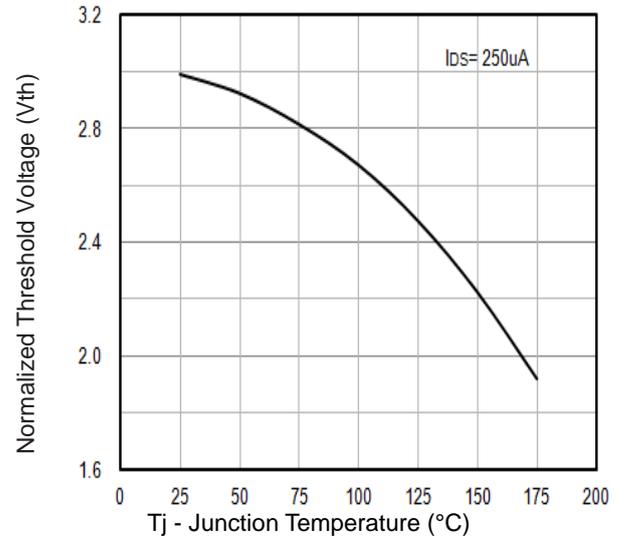


Fig2.  $V_{GS(TH)}$  Gate-Source Voltage Vs.  $T_j$

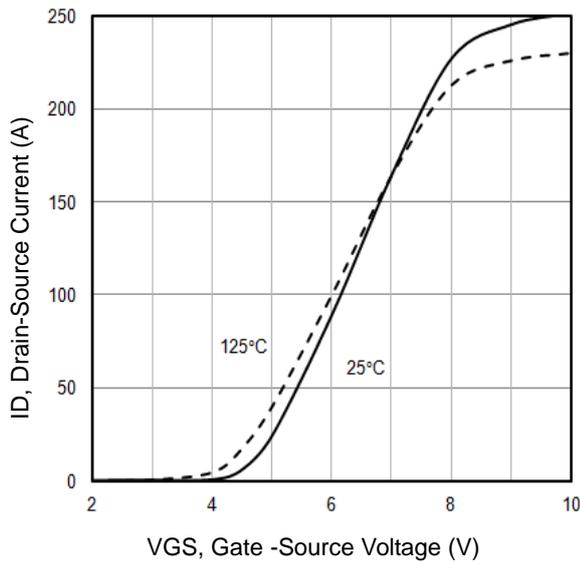


Fig3. Typical Transfer Characteristics

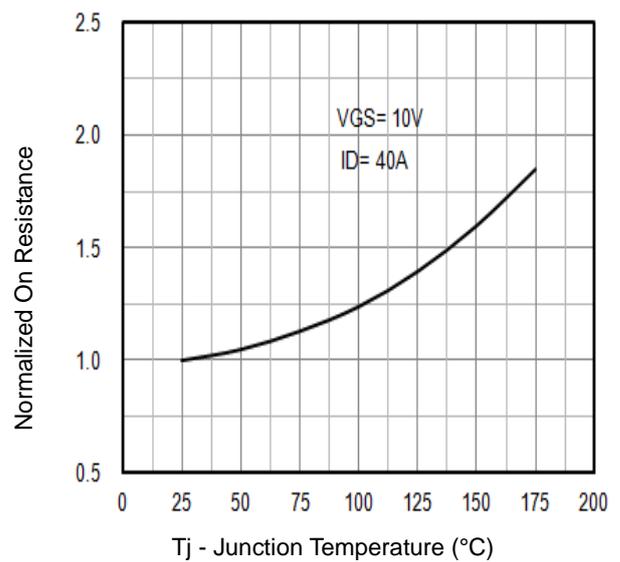


Fig4. Normalized On-Resistance Vs.  $T_j$

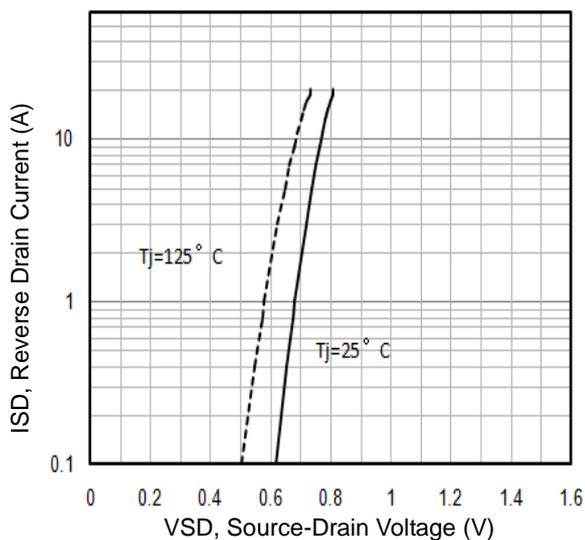


Fig5. Typical Source-Drain Diode Forward Voltage

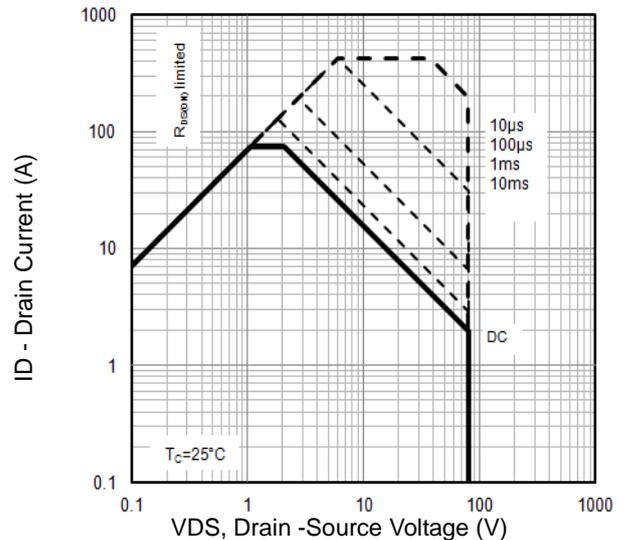


Fig6. Maximum Safe Operating Area

Typical Characteristics

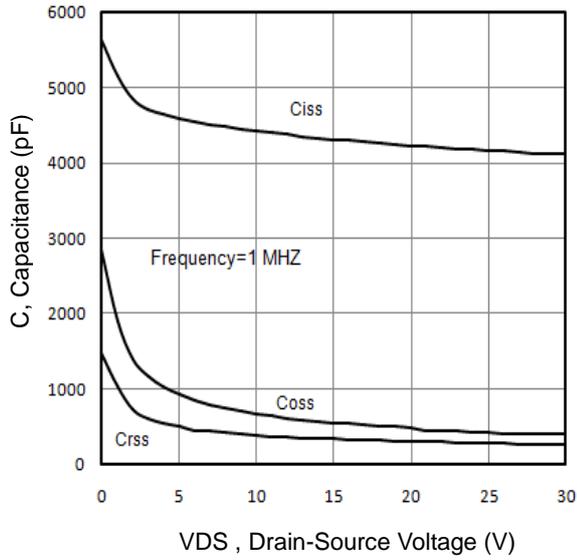


Fig7. Typical Capacitance Vs.Drain-Source Voltage

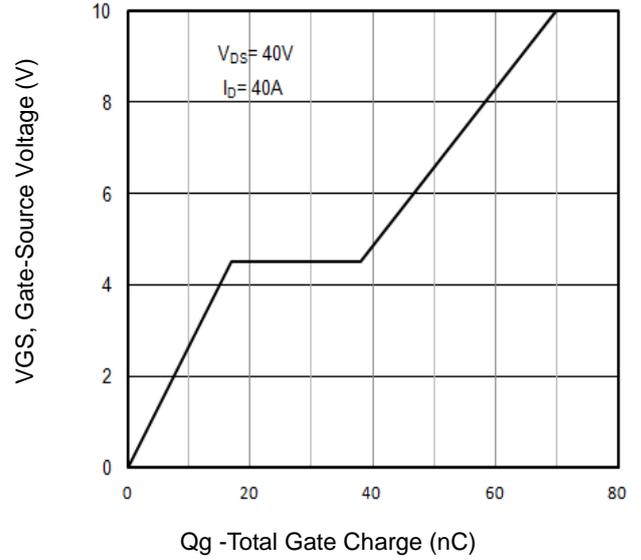


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

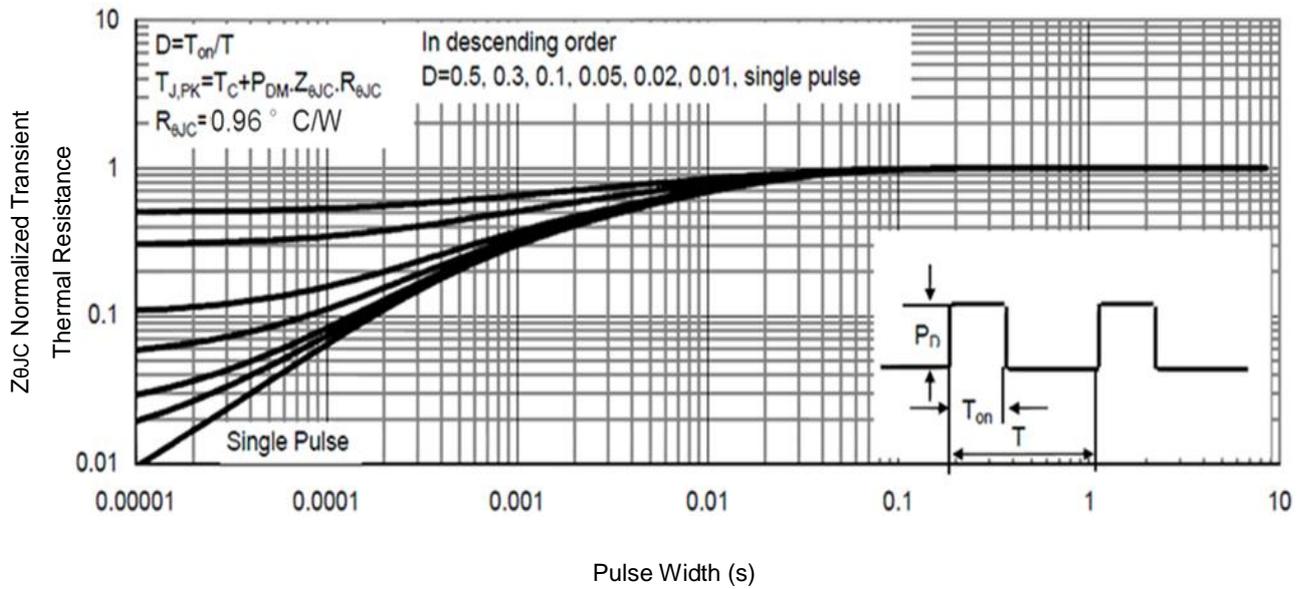


Fig9 . Normalized Maximum Transient Thermal Impedance

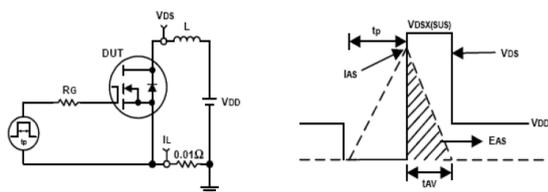


Fig10. Unclamped Inductive Test Circuit and waveforms

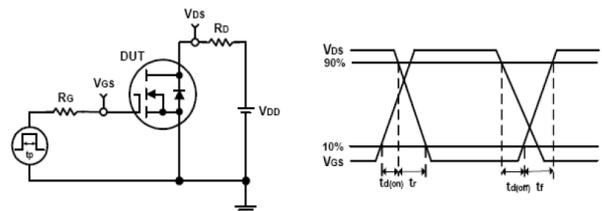
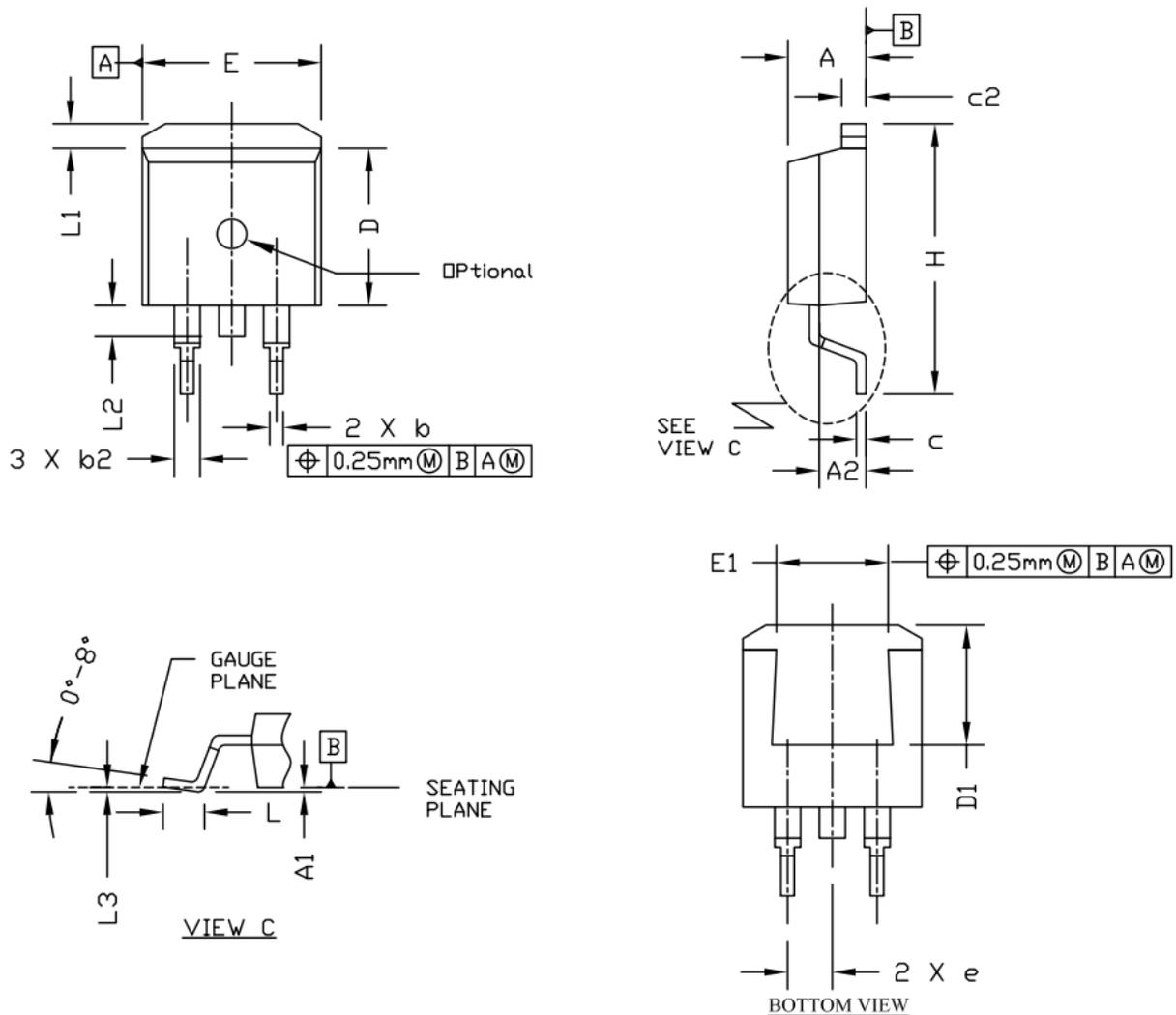


Fig11. Switching Time Test Circuit and waveforms

### TO-263 Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	4.400	4.570	4.700
A1	0.000	0.100	0.200
A2	2.300	2.400	2.500
b	0.700	0.800	0.900
b2	1.200	1.270	1.360
c	0.381	0.500	0.737
c2	1.220	1.300	1.350
D	8.600	9.200	9.300
D1	6.860		
e	2.540 BSC		
E	9.780	9.880	10.260
E1	6.225		
H	14.700	15.100	15.500
L	2.000	2.550	2.750
L1	1.000	1.200	1.400
L2	1.300	1.600	1.700
L3	0.255 BSC		

Notes:

1. Refer to JEDEC TO-263 variation AB
2. Dimension "D" & "E" do NOT include mold flash, mold flash shall not exceed 0.127mm per side.

### Customer Service

Sales and Service:

[sales@vgsemi.com](mailto:sales@vgsemi.com)

Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: [www.vgsemi.com](http://www.vgsemi.com)