Features

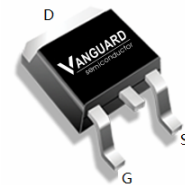| V_{DS}	70	V
$R_{DS(on),TYP}$@ V_{GS}=10 V	6.3	mΩ
$R_{DS(on),TYP}$@ V_{GS}=4.5 V	7.5	mΩ
I_D	65	A

- N-Channel，5V Logic Level Control

- Enhancement mode

- Very low on-resistance $R_{DS(on)}$ @ V_{GS}=4.5 V

- VitoMOS$^{®}$ Technology

- 100% Avalanche test

- Pb-free lead plating; RoHS compliant

TO-252

Drain Pin 2
Gate Pin 1
Source Pin 3

Part ID	Package Type	Marking	Tape and reel information
VSD007N07MS	TO-252	007N07M	2500PCS/Reel

Maximum ratings, at T_j=25°C, unless otherwise specified

Symbol	Parameter		Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage		70	V
I_S	Diode continuous forward current	T_C=25°C	65	A
I_D	Continuous drain current@V_{GS}=10V	T_C=25°C	65	A
		T_A=100°C	41	A
I_{DM}	Pulse drain current tested ①	T_C=25°C	260	A
EAS	Avalanche energy, single pulsed ②	I_D=15A	56	mJ
IAS	Avalanche current	L=0.5mH	30	A
P_D	Maximum power dissipation	T_A=25°C	60	W
V_{GS}	Gate-Source voltage		±20	V
T_{STG} T_J	Storage and operating temperature range		-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{θJC}$	Thermal Resistance-Junction to Case	2.5	°C/W
$R_{θJA}$	Thermal Resistance Junction-Ambient	60	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ Tc = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=250µA	70	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current(Tc=25℃)	V_{DS}=56V,V_{GS}=0V	--	--	0.1	µA
	Zero Gate Voltage Drain Current(Tc=125℃)	V_{DS}=56V,V_{GS}=0V	--	--	100	µA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V,V_{DS}=0V	--	--	±100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=250µA	1.0	2.0	3.0	V
$R_{DS(ON)}$	Drain-Source On-State Resistance③	V_{GS}=10V, I_D=26A	--	6.3	8.0	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance③	V_{GS}=4.5V, I_D=15A	--	7.5	10.0	mΩ
Dynamic Electrical Characteristics @ Tc= 25°C (unless otherwise stated)						
C_{iss}	Input Capacitance	V_{DS}=30V,V_{GS}=0V, f=1MHz	--	3000	--	pF
C_{oss}	Output Capacitance		--	250	--	pF
C_{rss}	Reverse Transfer Capacitance		--	205	--	pF
R_g	Gate Resistance	f=1MHz	--	1.5	--	Ω
Q_g	Total Gate Charge	V_{DS}=30V,I_D=30A, V_{GS}=10V	--	55	--	nC
Q_{gs}	Gate-Source Charge		--	15	--	nC
Q_{gd}	Gate-Drain Charge		--	10	--	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=30V, I_D=10A, R_G=6.8Ω, V_{GS}=10V	--	15	--	nS
t_r	Turn-on Rise Time		--	30	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	42	--	nS
t_f	Turn-Off Fall Time		--	12	--	nS
Source- Drain Diode Characteristics@ Tc = 25°C (unless otherwise stated)						
V_{SD}	Forward on voltage	I_{SD}=20A,V_{GS}=0V	--	0.79	1.2	V
t_{rr}	Reverse Recovery Time	T_j=25℃,I_{sd}=20A, V_{GS}=0V di/dt=200A/µs	--	55	--	nS
Q_{rr}	Reverse Recovery Charge			350		nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH,R_G = 25Ω, I_{AS} = 15A, V_{GS} =10V. Part not recommended for use above this value

③ Pulse width ≤ 300µs; duty cycle≤ 2%.
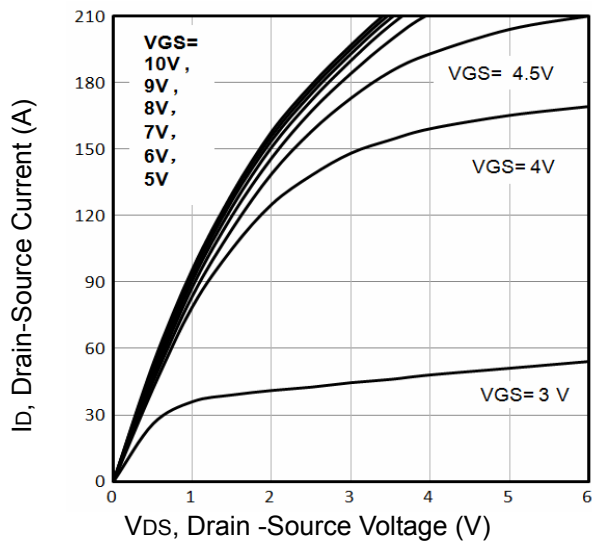
Typical Characteristics
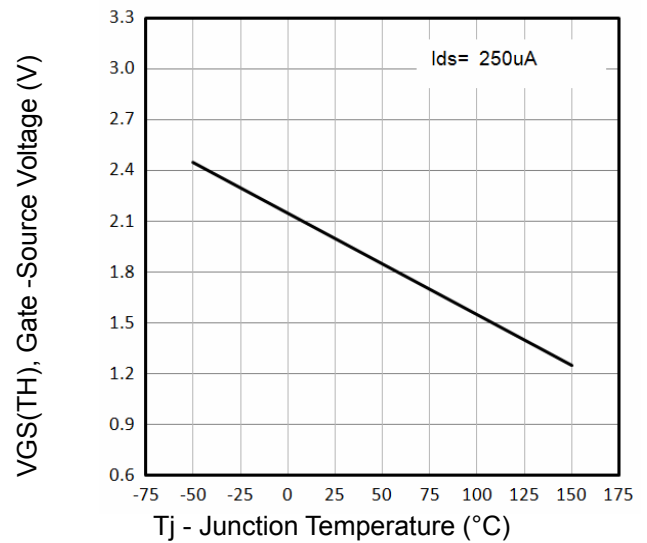

Fig1. Typical Output Characteristics

Fig2. Normalized Threshold Voltage Vs. Temperature
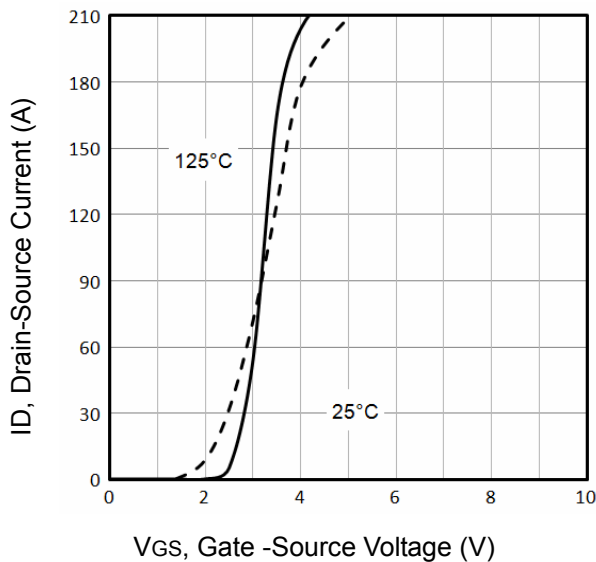

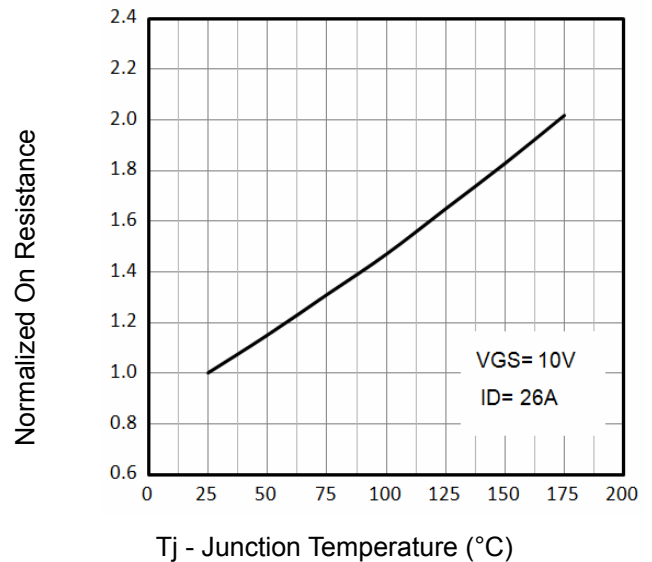**Fig3.** Typical Transfer Characteristics

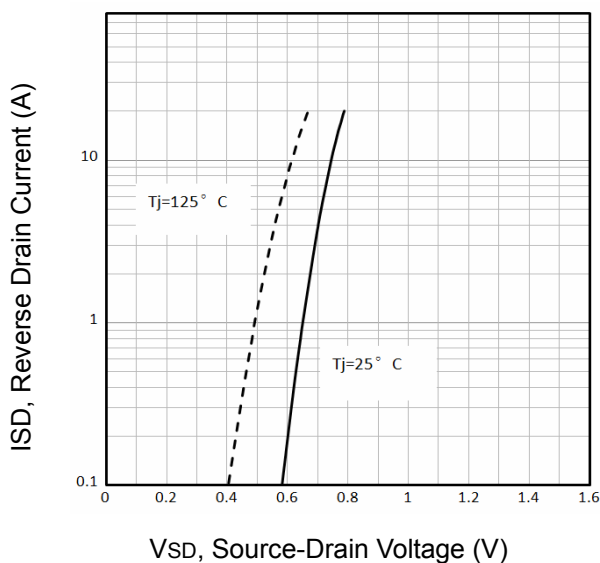**Fig4.** Normalized On-Resistance Vs. Temperature

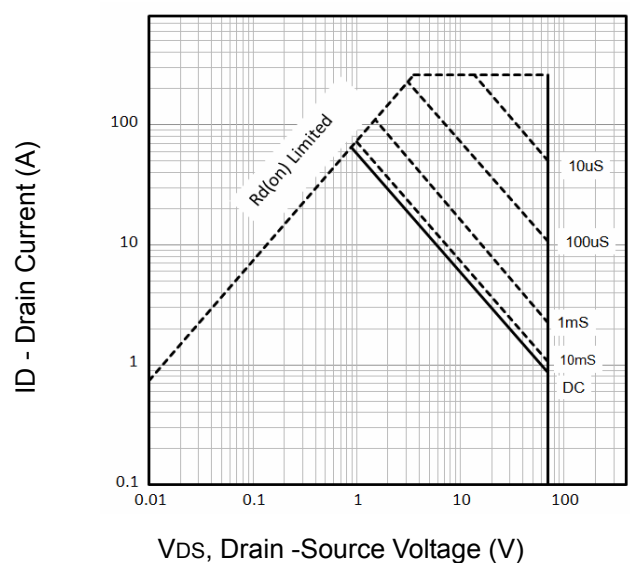**Fig5.** Typical Source-Drain Diode Forward Voltage

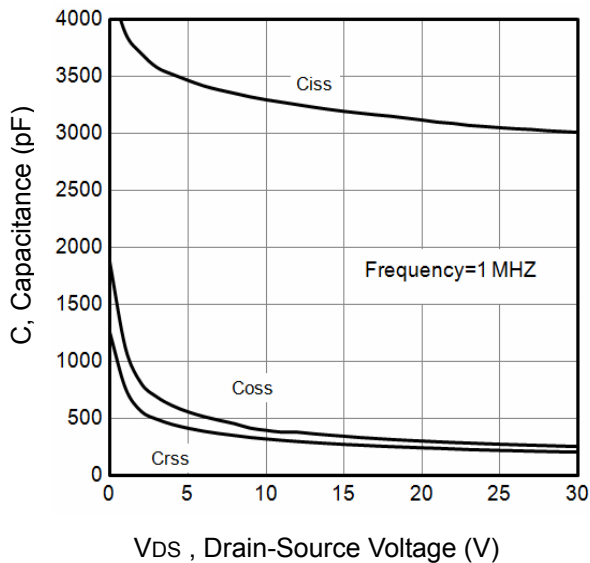**Fig6.** Maximum Safe Operating Area

Typical Characteristics
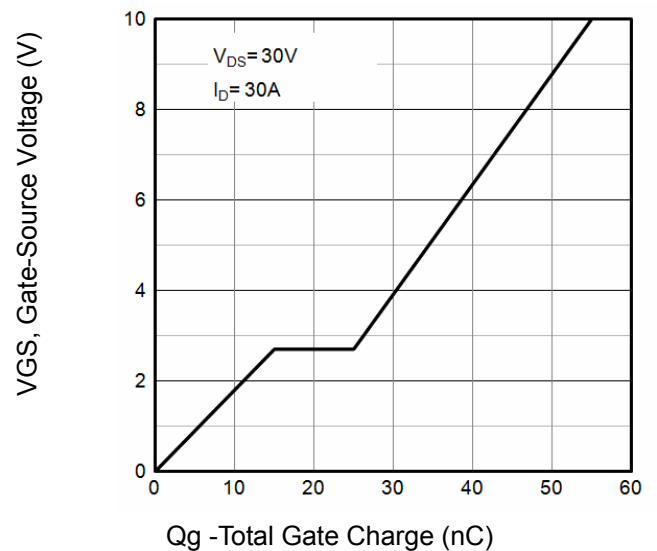

Fig7. Typical Capacitance Vs.Drain-Source

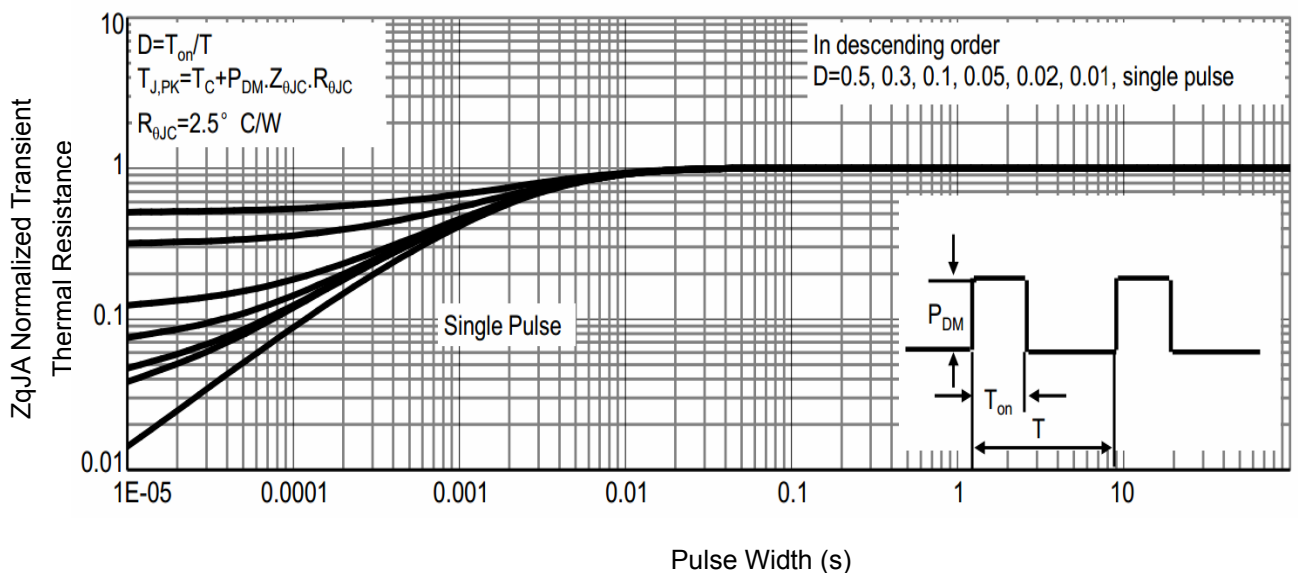**Fig8.** Typical Gate Charge Vs.Gate-Source

Pulse Width (s)

Fig9. Normalized Maximum Transient Thermal
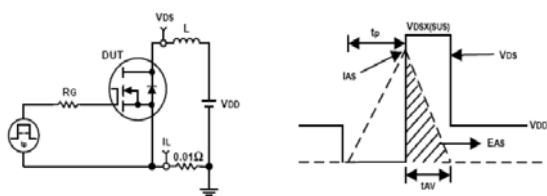

Fig10. Unclamped Inductive Test Circuit and waveforms

Fig11. Switching Time Test Circuit and waveforms

TO-252 Package Outline

DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.22	2.30	2.38	A_1	0.46	0.58	0.93
b	0.71	0.79	0.89	b_1	0.90	0.98	1.10
b_2	5.00	5.30	5.46	c	0.20	0.40	0.56
D_1	5.98	6.05	6.22	D_2	--	4.00	--
E	6.47	6.60	6.73	E_1	5.10	5.28	5.45
e	--	2.28	--	e_1	--	4.57	--
H_D	9.60	10.08	10.40	L	2.75	2.95	3.05
L_1	--	0.50	--	L_2	0.80	0.90	1.10
w	--	0.20	--	y	0.20	--	--

Customer Service

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