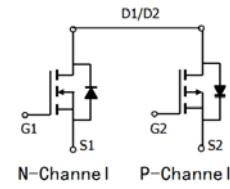


Features

- N+P Channel
- Enhancement mode
- Very low on-resistance
- Fast Switching
- Pb-free lead plating; RoHS compliant

V_{DS}	40	-40	V
$R_{DS(on),TYP} @ V_{GS}=\pm 10\text{ V}$	18	32	$\text{m}\Omega$
$R_{DS(on),TYP} @ V_{GS}=\pm 4.5\text{ V}$	24	38	$\text{m}\Omega$
I_D	31	-22	A

TO-252-4L


Part ID	Package Type	Marking	Tape and reel information
VSD030C04MC	TO-252-4L	030C04MC	2500pcs/Reel

Absolute Maximum Ratings

Symbol	Parameter	Rating		Unit
		NMOS	PMOS	

Common Ratings ($T_C=25^\circ\text{C}$ Unless Otherwise Noted)

V_{GS}	Gate-Source Voltage	± 20	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	40	-40	V	
T_J	Maximum Junction Temperature①	150		$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-50 to 150		$^\circ\text{C}$	
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	31	-22	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested②	$T_C=25^\circ\text{C}$	124	-88	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	31	-22	A
		$T_C=100^\circ\text{C}$	20	-14	
P_D	Power dissipation for Dual Operation	$T_C=25^\circ\text{C}$	35	31	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		3.5	4	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		100	100	$^\circ\text{C}/\text{W}$

Drain-Source Avalanche Ratings

EAS	Avalanche Energy, Single Pulsed	16 ^③	25 ^④	mJ
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N-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c = 25^\circ\text{C}$)	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c = 125^\circ\text{C}$)	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.8	2.3	3.2	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=12\text{A}$	--	18	25	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=8\text{A}$	--	24	30	$\text{m}\Omega$

Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)

C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	840	--	pF
C_{oss}	Output Capacitance		--	90	--	pF
C_{rss}	Reverse Transfer Capacitance		--	75	--	pF
R_g	Gate Resistance	f=1MHz	--	1.7	--	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=4\text{A}, V_{\text{GS}}=10\text{V}$	--	12	--	nC
Q_{gs}	Gate Source Charge		--	2.3	--	nC
Q_{gd}	Gate Drain Charge		--	3.9	--	nC

Switching Characteristics

$t_{d(\text{on})}$	Turn on Delay Time	$V_{\text{DD}}=20\text{V}, I_{\text{D}}=4\text{A}, R_{\text{G}}=3.3\Omega, V_{\text{GS}}=10\text{V}$	--	7	--	nS
t_r	Turn on Rise Time		--	12	--	nS
$t_{d(\text{off})}$	Turn Off Delay Time		-	21	--	nS
t_f	Turn Off Fall Time		--	9	--	nS

Source Drain Diode Characteristics

V_{SD}	Forward on voltage	$I_{\text{SD}}=12\text{A}, V_{\text{GS}}=0\text{V}$	--	0.82	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{sd}}=4\text{A}, V_{\text{GS}}=0\text{V}$	--	33	--	nS
Q_{rr}	Reverse Recovery Charge		--	20	--	nC

P-Channel Electrical Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c = 25^\circ\text{C}$)	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-1	μA
	Zero Gate Voltage Drain Current($T_c = 125^\circ\text{C}$)	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.7	-2.5	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12\text{A}$	--	32	40	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-8\text{A}$	--	38	45	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	1520	--	pF
C_{oss}	Output Capacitance		--	155	--	pF
C_{rss}	Reverse Transfer Capacitance		--	110	--	pF
R_g	Gate Resistance	f=1MHz	--	24	--	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-3\text{A}, V_{\text{GS}}=-10\text{V}$	--	29	--	nC
Q_{qs}	Gate Source Charge		--	5	--	nC
Q_{qd}	Gate Drain Charge		--	11	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn on Delay Time	$V_{\text{DD}}=-20\text{V}, I_{\text{D}}=-3\text{A}, R_{\text{G}}=3.3\Omega, V_{\text{GS}}=-10\text{V}$	--	13	--	ns
t_r	Turn on Rise Time		--	38	--	ns
$t_{\text{d(off)}}$	Turn Off Delay Time		-	67	--	ns
t_f	Turn Off Fall Time		--	21	--	ns
Source Drain Diode Characteristics						
V_{SD}	Forward on voltage	$I_{\text{SD}}=-12\text{A}, V_{\text{GS}}=0\text{V}$	--	-0.83	-1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=-3\text{A}, V_{\text{GS}}=0\text{V}$	--	49	--	nS
Q_{rr}	Reverse Recovery Charge		--	35	--	nC

Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

③ Limited by $T_{J\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 8\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value

④ Limited by $T_{J\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = -10\text{A}$, $V_{GS} = -10\text{V}$. Part not recommended for use above this value

N-Channel Typical Characteristics

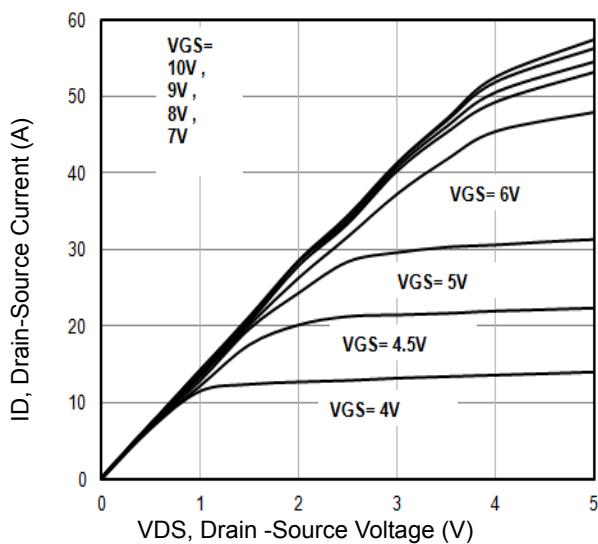


Fig1. Typical Output Characteristics

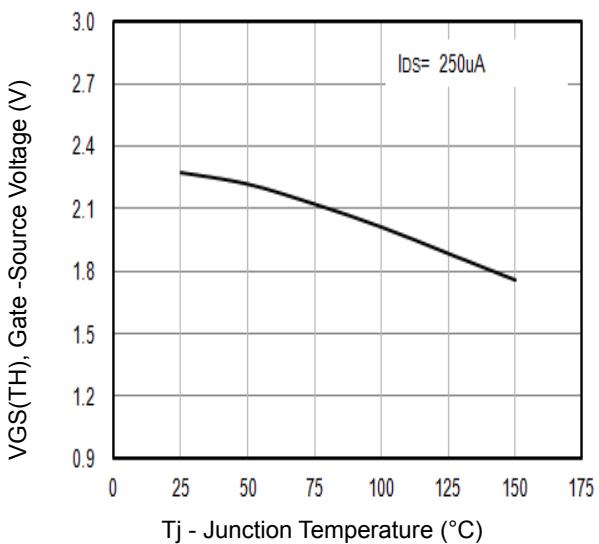


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

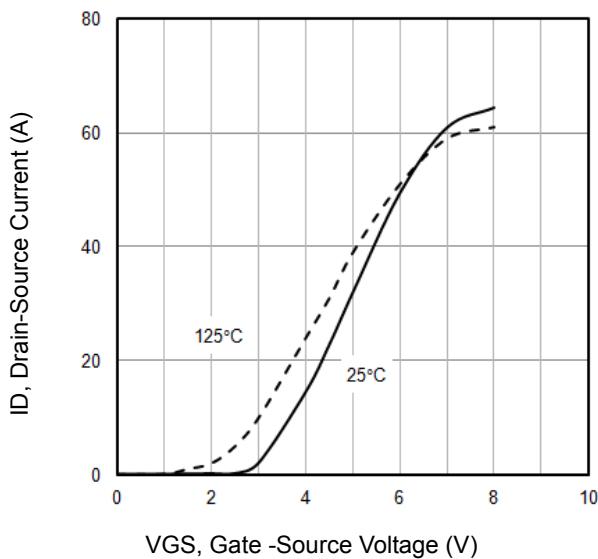


Fig3. Typical Transfer Characteristics

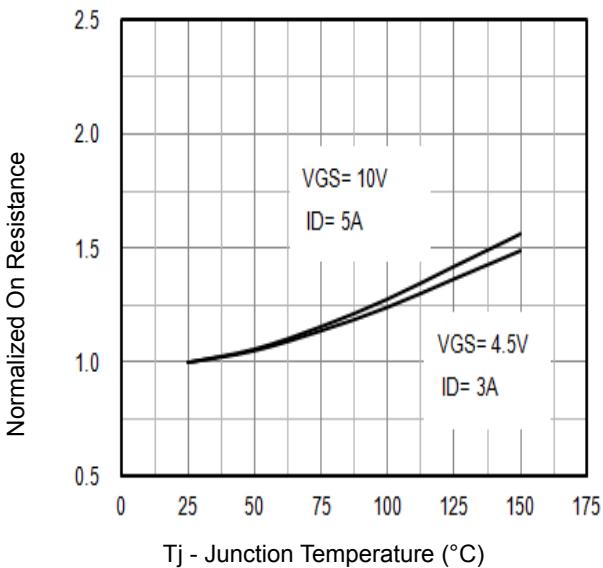


Fig4. Normalized On-Resistance Vs. T_j

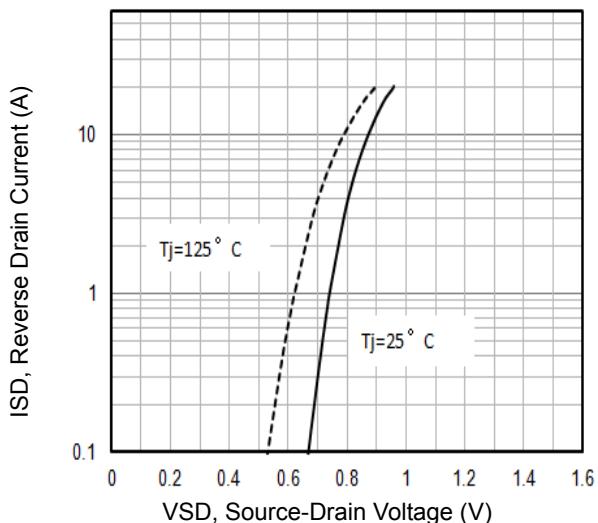


Fig5. Typical Source-Drain Diode Forward Voltage

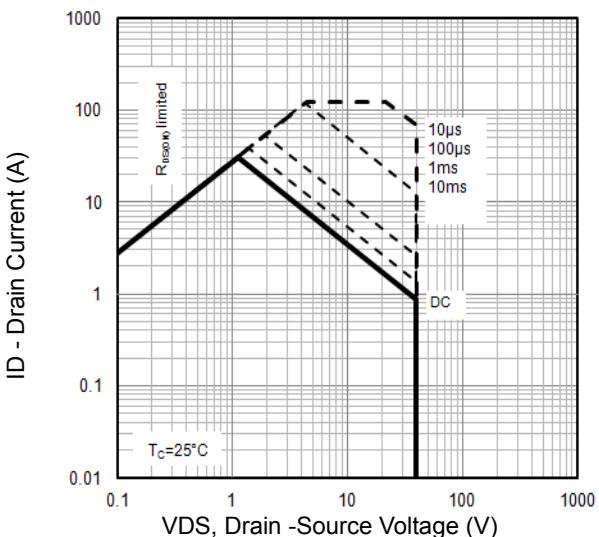


Fig6. Maximum Safe Operating Area

N-Channel Typical Characteristics

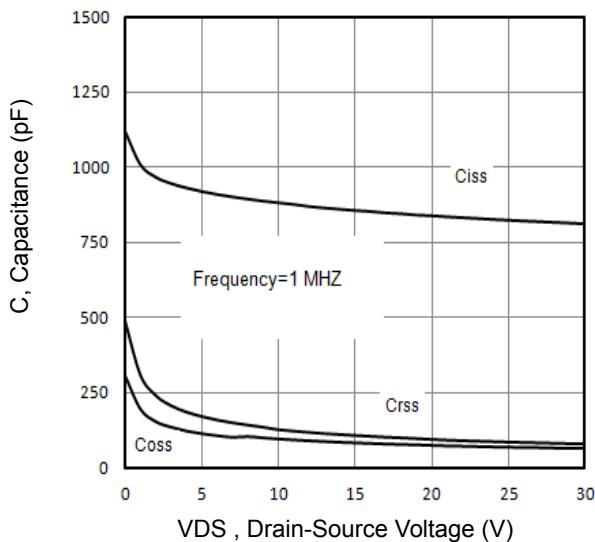


Fig7. Typical Capacitance Vs.Drain-Source Voltage

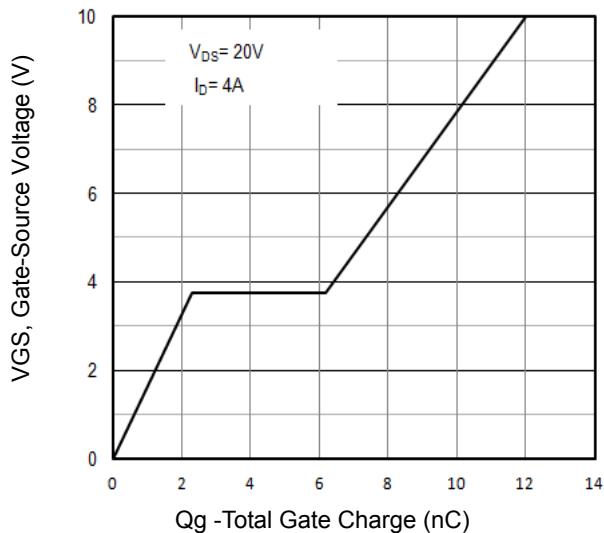


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

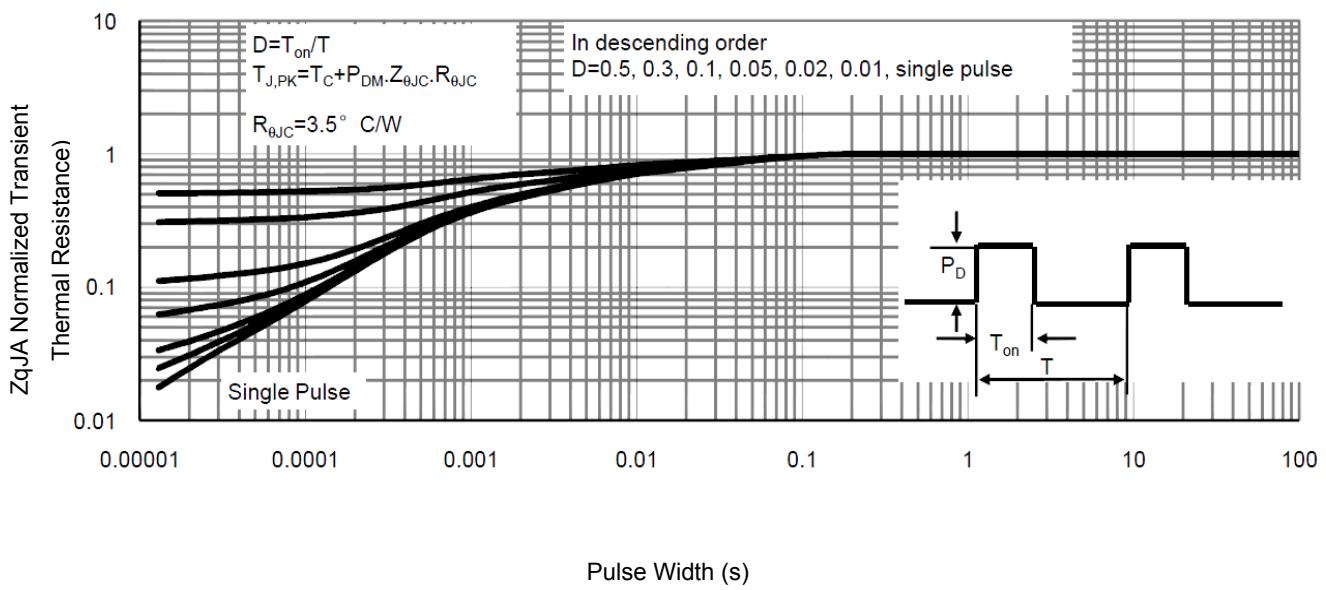


Fig 9 .Normalized Maximum Transient Thermal Impedance

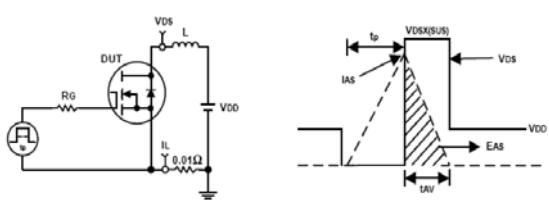


Fig10. Unclamped Inductive Test Circuit and waveforms

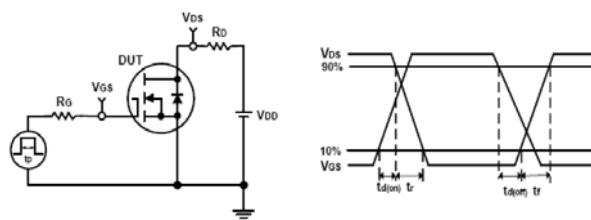


Fig11. Switching Time Test Circuit and waveforms

P-Channel Typical Characteristics

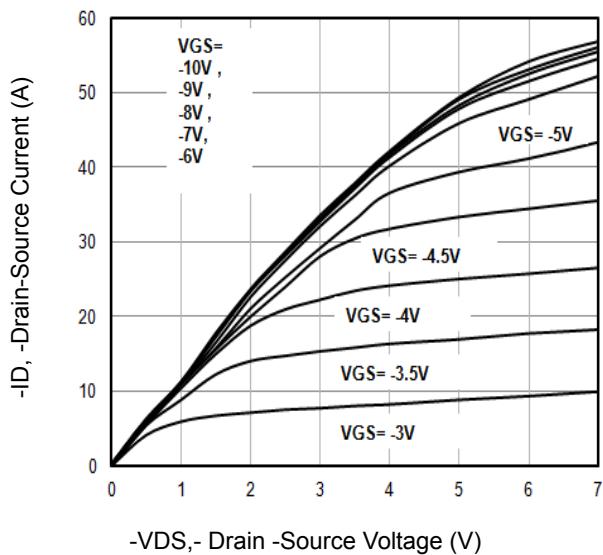


Fig1. Typical Output Characteristics

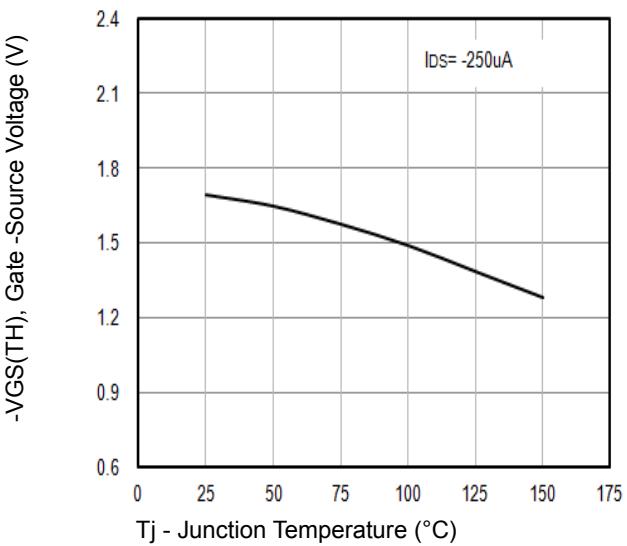


Fig2. $V_{GS(TH)}$ Gate-Source Voltage Vs. T_j

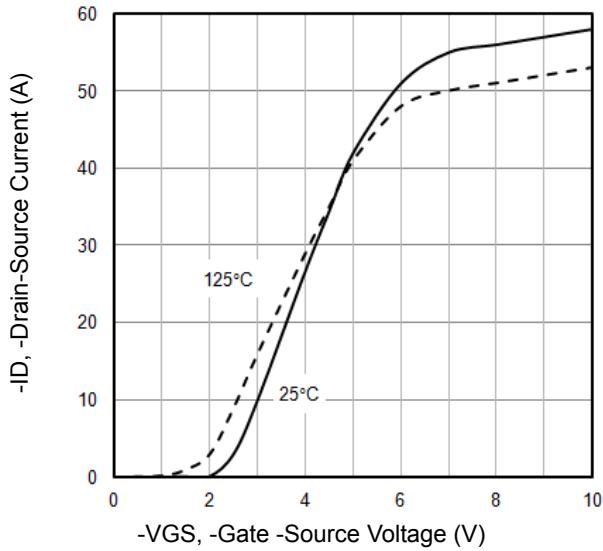


Fig3. Typical Transfer Characteristics

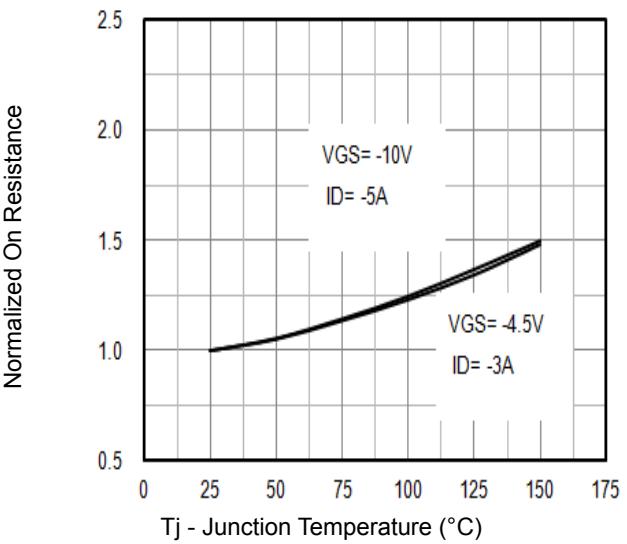


Fig4. Normalized On-Resistance Vs. T_j

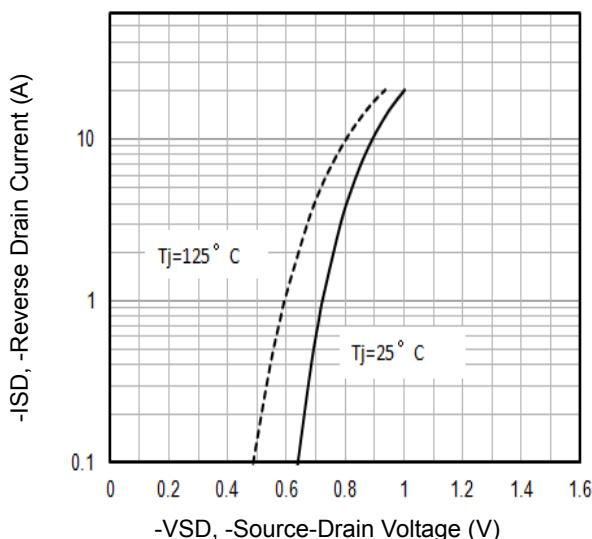


Fig5. Typical Source-Drain Diode Forward Voltage

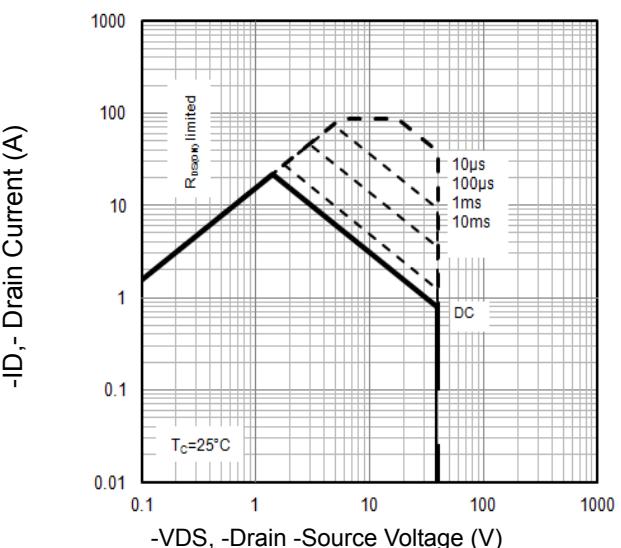


Fig6. Maximum Safe Operating Area

P-Channel Typical Characteristics

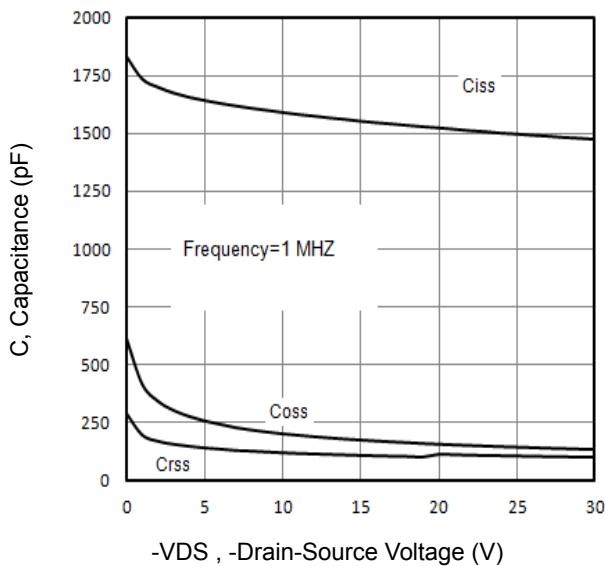


Fig7. Typical Capacitance Vs.Drain-Source Voltage

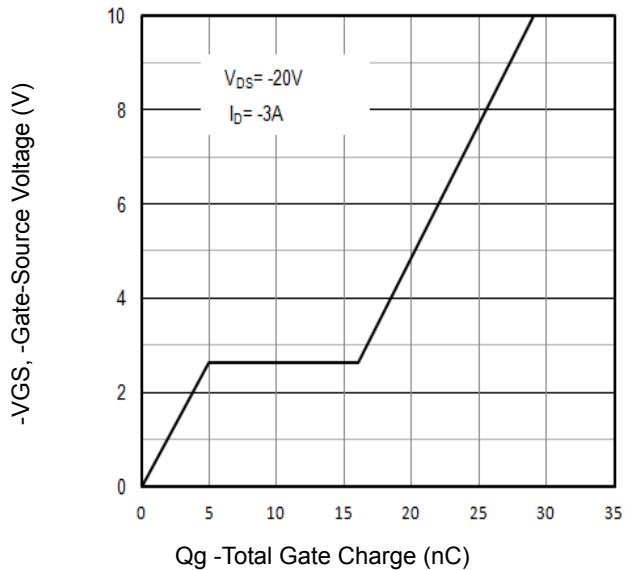


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

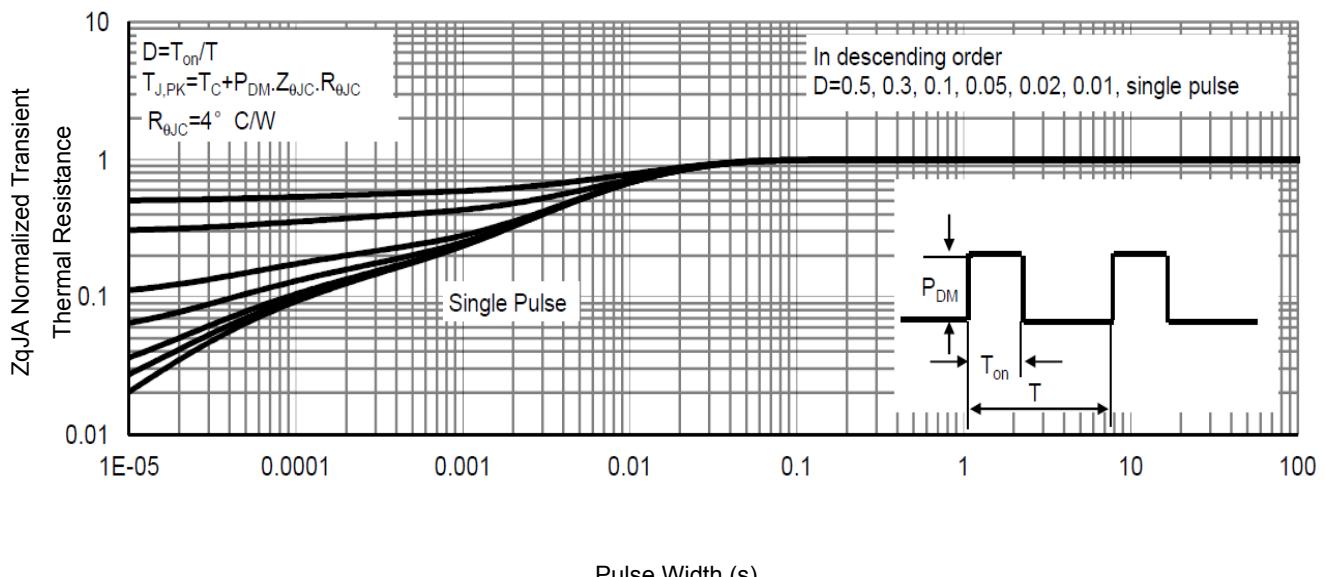


Fig9. Normalized Maximum Transient Thermal Impedance

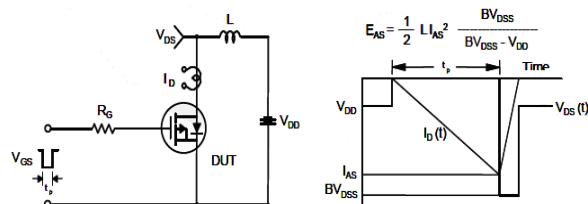


Fig10. Unclamped Inductive Test Circuit and Waveforms

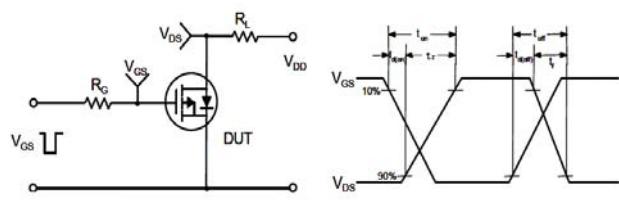
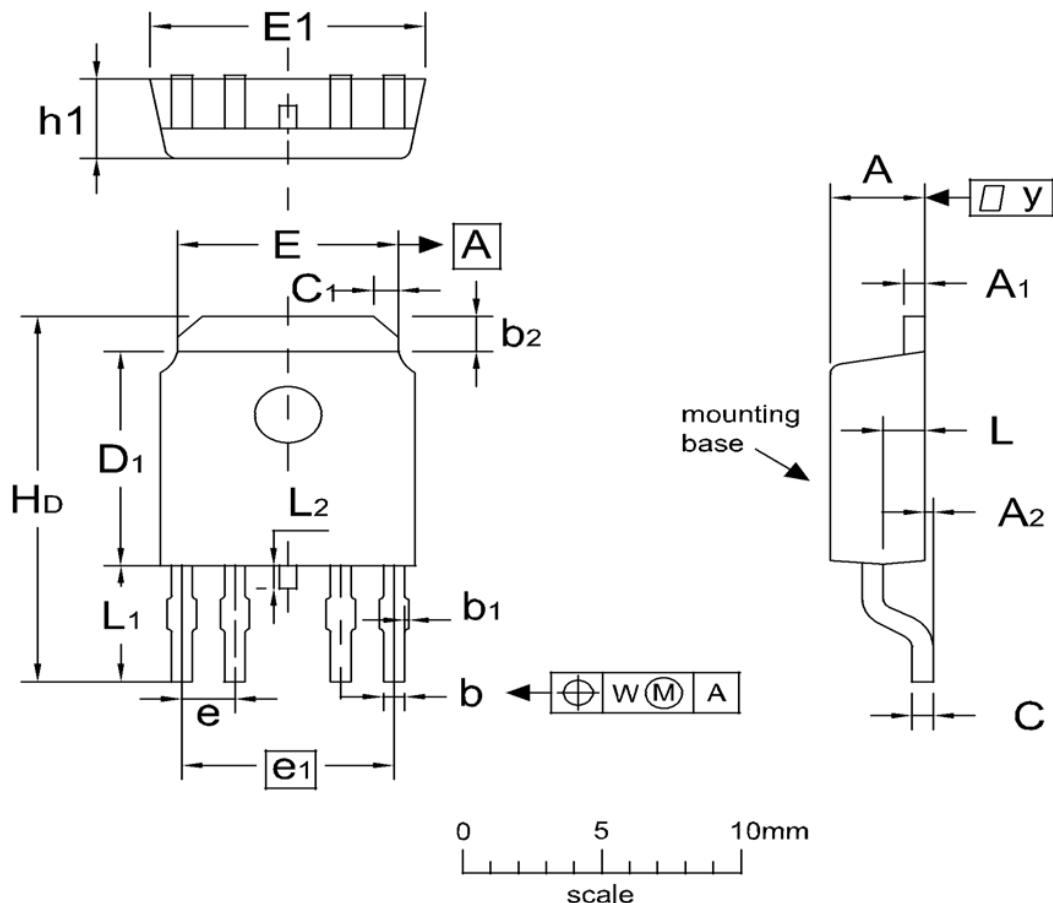


Fig11. Switching Time Test Circuit and waveforms



TO-252-4L Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.1	2.3	2.5	A ₁	0.4	0.5	0.6
A ₂	--	--	0.3	b	0.4	0.5	0.6
b ₁	--	--	0.1	b ₂	0.8	1.0	1.2
C	0.4	0.5	0.6	C ₁	0.4	0.6	0.8
D ₁	5.7	6.1	6.5	E	5.0	5.3	5.6
E ₁	6.3	6.6	6.9	e	--	1.27	--
e ₁	--	5.08	--	H _D	9.6	10.0	10.4
h ₁	2.1	2.3	2.5	L	0.80	1.0	1.2
L ₁	2.6	2.9	3.2	L ₂	0.35	0.65	0.95

Customer Service

Sales and Service:

sales@vgsemi.com

Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: www.vgsemi.com