

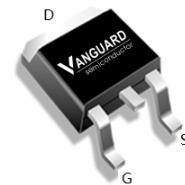
## Features

- N-Channel
- Enhancement mode
- low on-resistance @  $V_{GS}=4.5\text{ V}$
- Fast Switching
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant

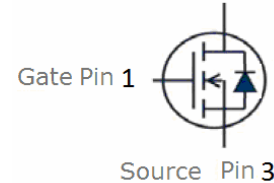


Part ID	Package Type	Marking	Tape and reel information
VSD280N15MS	TO-252	280N15M	2500pcs/reel

$V_{DS}$	150	V
$R_{DS(on),typ@VGS=10V}$	245	m $\Omega$
$R_{DS(on),typ@VGS=4.5V}$	240	m $\Omega$
$I_D$	5	A

**TO-252**


Drain Pin 2



## Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V_{(BR)DSS}$	Drain-Source breakdown voltage	150	V	
$V_{GS}$	Gate-Source voltage	$\pm 20$	V	
$I_D$	Continuous drain current@ $V_{GS}=10V$	$T_C=25^\circ\text{C}$	5	A
		$T_A=100^\circ\text{C}$	3.2	A
$I_{DM}$	Pulse drain current tested ①	$T_C=25^\circ\text{C}$	15	A
$P_D$	Maximum power dissipation	$T_C=25^\circ\text{C}$	41	W
$I_S$	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	5	A
EAS	Avalanche energy, single pulsed ③	$I_D=2.5A$	1.5	mJ
$T_J$	Maximum Junction Temperature		175	$^\circ\text{C}$
$T_{STG}$	Storage temperature range		-55 to 175	$^\circ\text{C}$
<b>Thermal characteristics</b>				
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	50	$^\circ\text{C/W}$	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	3.6	$^\circ\text{C/W}$	

**Typical Electrical Characteristics**

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	150	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(Tc=25°C)	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)	V <sub>DS</sub> =150V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	2.0	3.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance②	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	--	245	270	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance②	V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A	--	240	280	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =75V, V <sub>GS</sub> =0V, f=1MHz	--	855	--	pF
C <sub>oss</sub>	Output Capacitance		--	105	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	30	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =75V, I <sub>D</sub> =3A, V <sub>GS</sub> =10V	--	19	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	5	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	5.2	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =75V, I <sub>D</sub> =1A, R <sub>G</sub> =6.8Ω, V <sub>GS</sub> =4.5V	--	12	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	6	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	15.5	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	4.5	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =5A, V <sub>GS</sub> =0V	--	0.84	1.20	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>sd</sub> =4A, V <sub>GS</sub> =0V	--	35	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs		245		nC

**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature
- ② Pulse width ≤ 300μs; duty cycles ≤ 2%.
- ③ Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.5mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 2.5A, V<sub>GS</sub> = 10V. Part not recommended for use above this value

**Typical Characteristics**

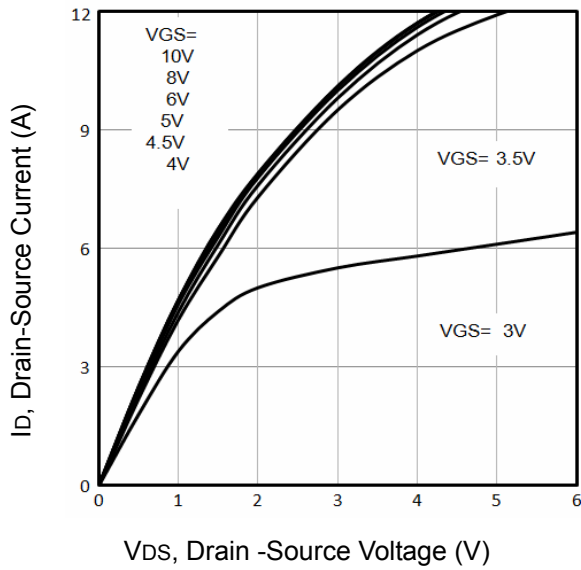


Fig1. Typical Output Characteristics

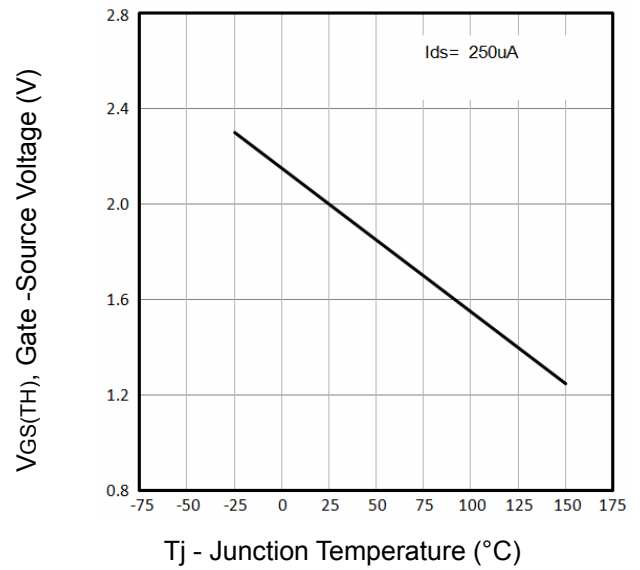


Fig2. Threshold Voltage Vs. Temperature

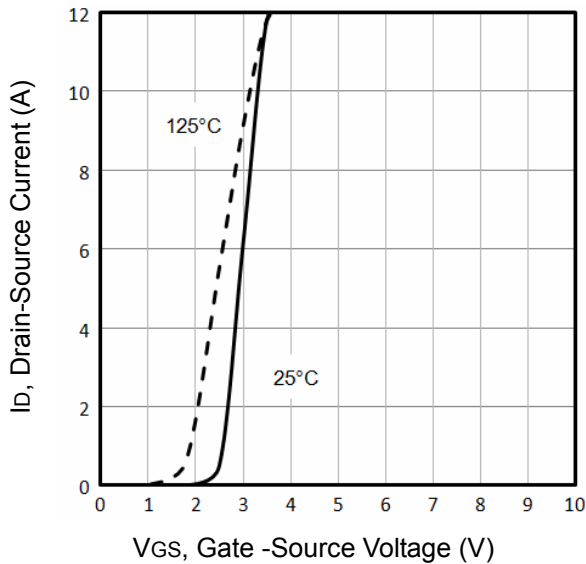


Fig3. Typical Transfer Characteristics

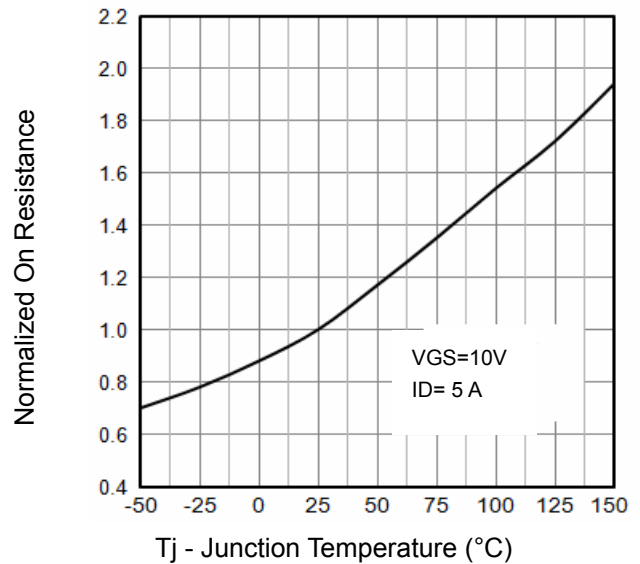


Fig4. Normalized On-Resistance Vs. Temperature

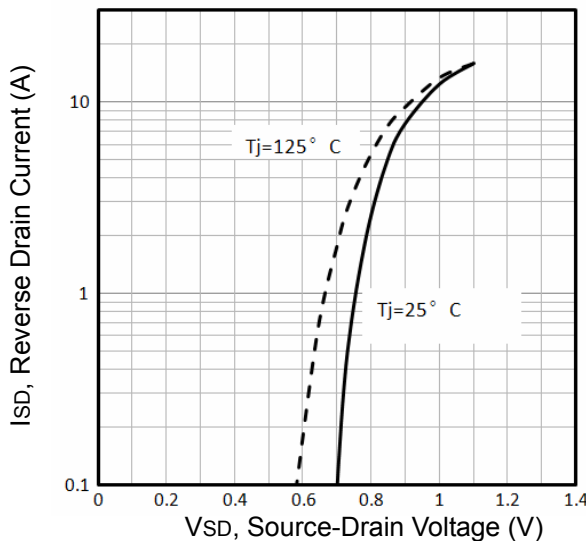


Fig5. Typical Source-Drain Diode Forward Voltage

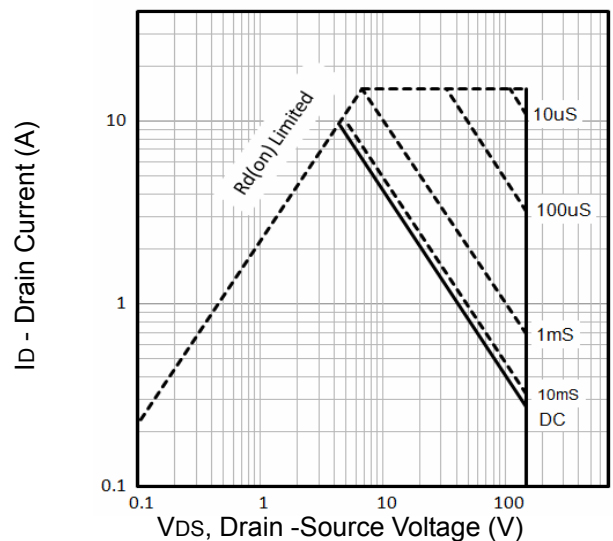


Fig6. Maximum Safe Operating Area

**Typical Characteristics**

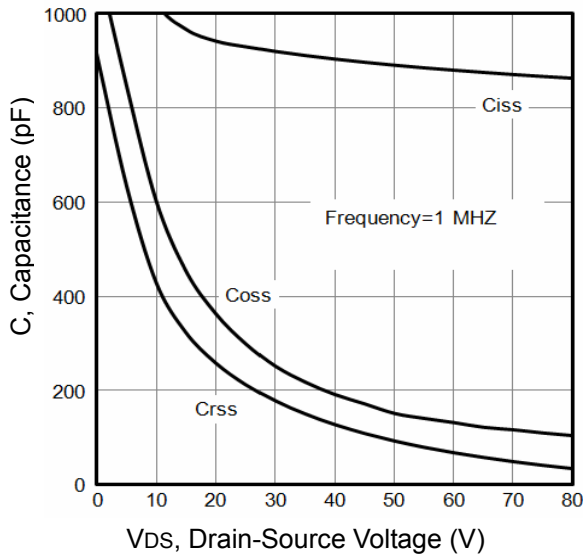


Fig7. Typical Capacitance Vs. Drain-Source Voltage

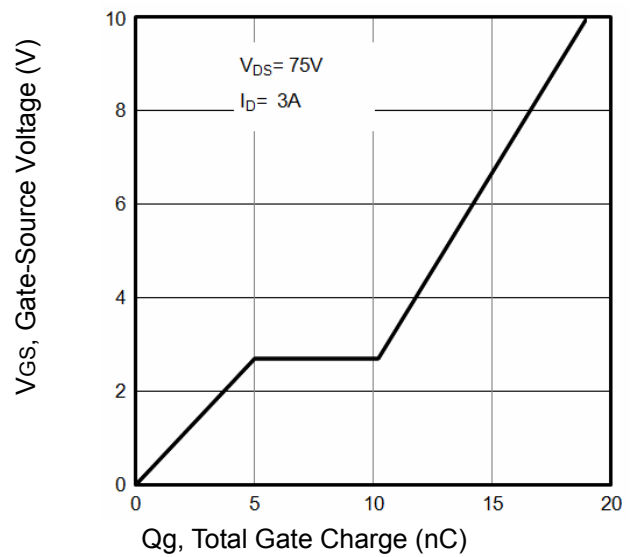


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

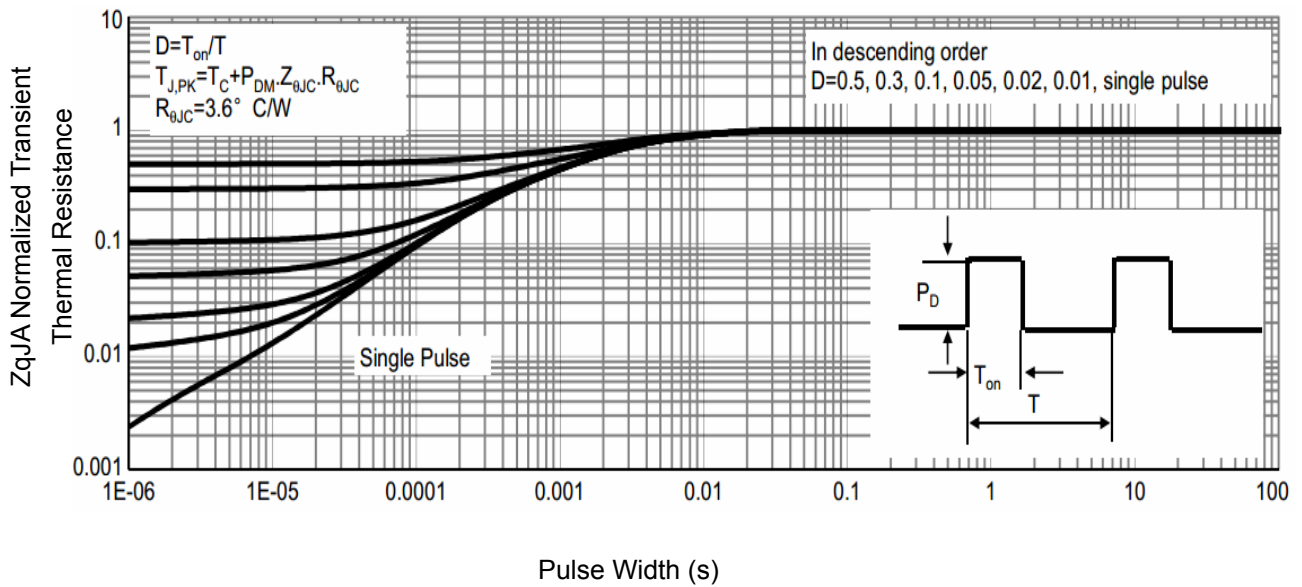


Figure 9: Normalized Maximum Transient Thermal

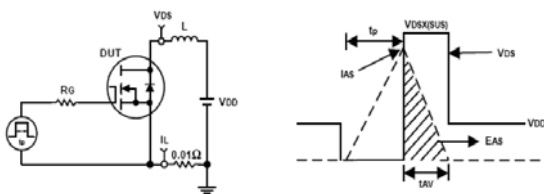


Fig10. Unclamped Inductive Test Circuit and waveforms

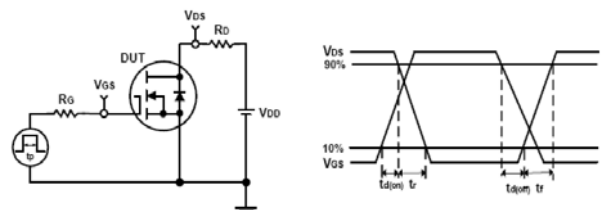
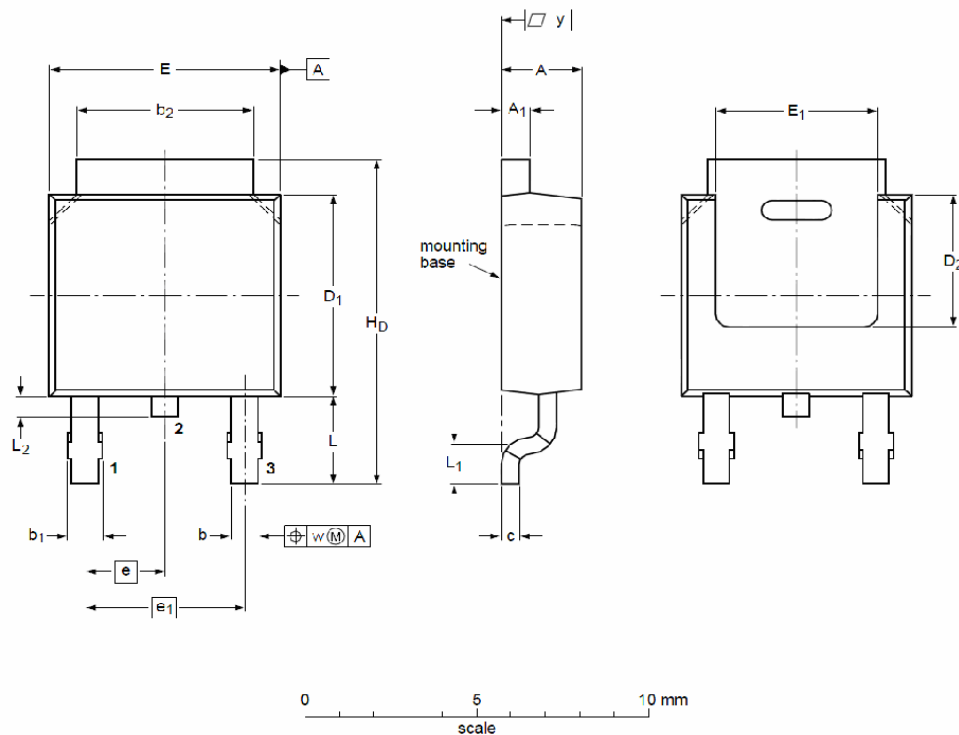


Fig11. Switching Time Test Circuit and waveforms

## TO-252 Package Outline



**DIMENSIONS** ( unit : mm )

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.22	2.30	2.38	A <sub>1</sub>	0.46	0.58	0.93
b	0.71	0.79	0.89	b <sub>1</sub>	0.90	0.98	1.10
b <sub>2</sub>	5.00	5.30	5.46	c	0.20	0.40	0.56
D <sub>1</sub>	5.98	6.05	6.22	D <sub>2</sub>	--	4.00	--
E	6.47	6.60	6.73	E <sub>1</sub>	5.10	5.28	5.45
e	--	2.28	--	e <sub>1</sub>	--	4.57	--
H <sub>D</sub>	9.60	10.08	10.40	L	2.75	2.95	3.05
L <sub>1</sub>	--	0.50	--	L <sub>2</sub>	0.80	0.90	1.10
w	--	0.20	--	y	0.20	--	--

## Customer Service

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