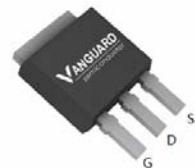


Features

- N-Channel
- Enhancement mode
- Low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V
- Fast Switching
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant

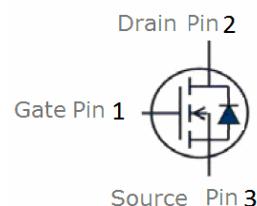
V_{DS}	30	V
$R_{DS(on),Typ}$ @ $V_{GS}=10$ V	16	mΩ
$R_{DS(on),Typ}$ @ $V_{GS}=4.5$ V	22	mΩ
I_D	30	A

TO-251-S



Halogen-Free

Part ID	Package Type	Marking	Tape and reel information
VSI018N03MS	TO-251-S	018N03M	80pcs/Tube



Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V_{(BR)DSS}$	Drain-Source breakdown voltage	30	V	
I_D	Continuous drain current@ $V_{GS}=10$ V	$T_c=25$ °C	30	A
		$T_c=100$ °C	19	A
I_{DM}	Pulse drain current tested ①	$T_c=25$ °C	100	A
EAS	Avalanche energy, single pulsed ②	$L=0.1$ mH	11	mJ
P_D	Maximum power dissipation	$T_c=25$ °C	42	W
V_{GS}	Gate-Source voltage	±20	V	
T_{STG}	Storage and operating temperature range	-55 to 175	°C	

Thermal characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	55	°C/W
$R_{\theta JC}$	Thermal Resistance-Junction to Case	3.5	°C/W

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current ($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current ($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.5	2.2	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=15\text{A}$	--	16	18.5	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=10\text{A}$	--	22	25	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ^③	$V_{\text{GS}}=4.2\text{V}, I_{\text{D}}=2\text{A}$	--	23	28	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	445	--	pF
C_{oss}	Output Capacitance		--	75	--	pF
C_{rss}	Reverse Transfer Capacitance		--	40	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=10\text{V}$	--	15	--	nC
Q_{gs}	GateSource Charge		--	4.5	--	nC
Q_{gd}	GateDrain Charge		--	5.0	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turnon Delay Time	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=6\text{A}, R_{\text{G}}=3.3\Omega, V_{\text{GS}}=10\text{V}$	--	6	--	nS
t_r	Turnon Rise Time		--	14	--	nS
$t_{\text{d(off)}}$	TurnOff Delay Time		-	21	--	nS
t_f	TurnOff Fall Time		--	10	--	nS
Source Drain Diode Characteristics						
I_{SD}	Sourcedrain current(Body Diode)	$T_c=25^\circ\text{C}$	30	--	--	A
V_{SD}	Forward on voltage	$T_j=25^\circ\text{C}, I_{\text{SD}}=15\text{A}, V_{\text{GS}}=0\text{V}$	--	0.91	1.3	V

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by $T_{J\text{max}}$, starting $T_j = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 15\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value
- ③ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics

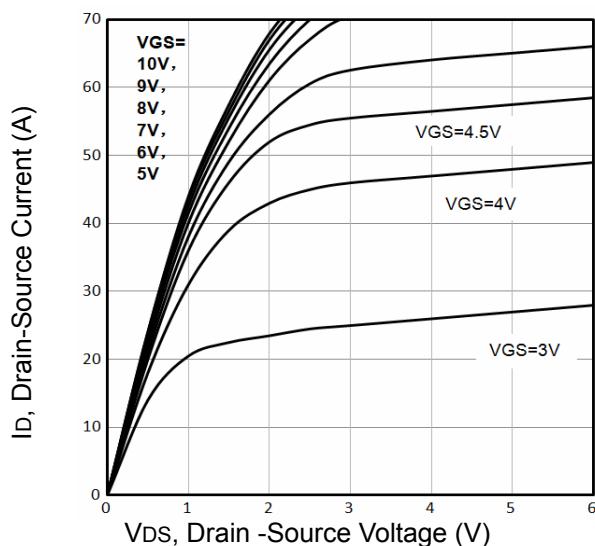


Fig1. Typical Output Characteristics

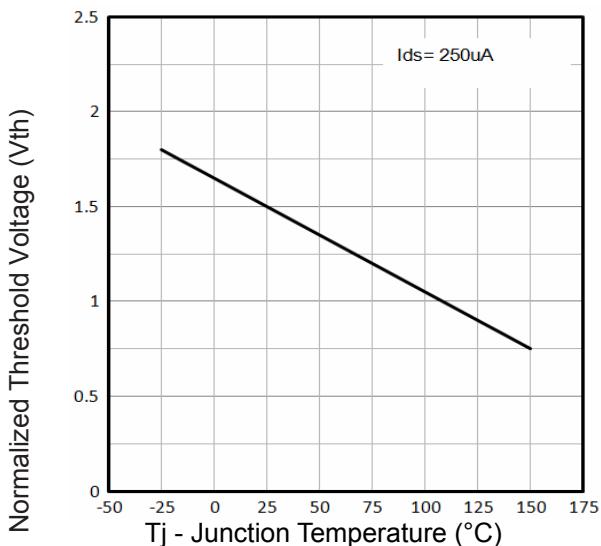


Fig2. Normalized Threshold Voltage Vs. Temperature

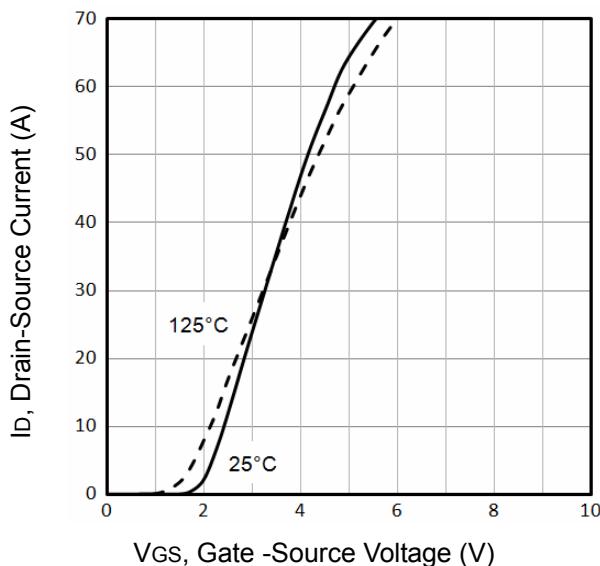


Fig3. Typical Transfer Characteristics

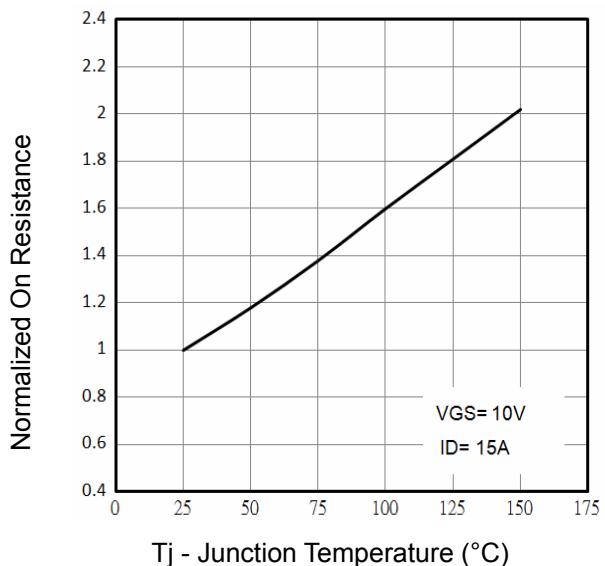


Fig4. Normalized On-Resistance Vs. Temperature

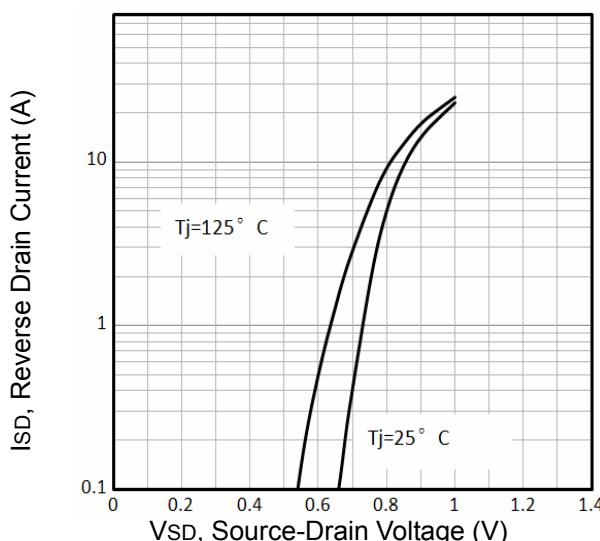


Fig5. Typical Source-Drain Diode Forward Voltage

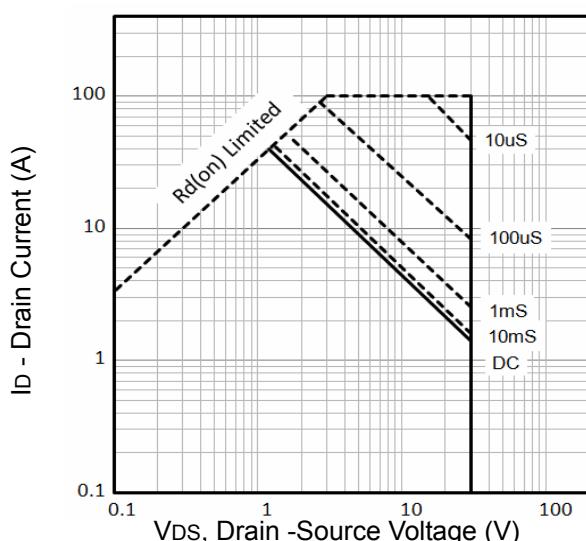


Fig6. Maximum Safe Operating Area

Typical Characteristics

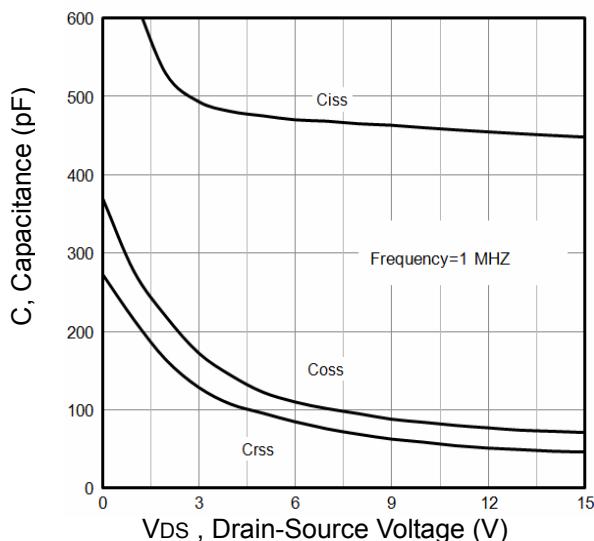


Fig7. Typical Capacitance Vs.Drain-Source Voltage

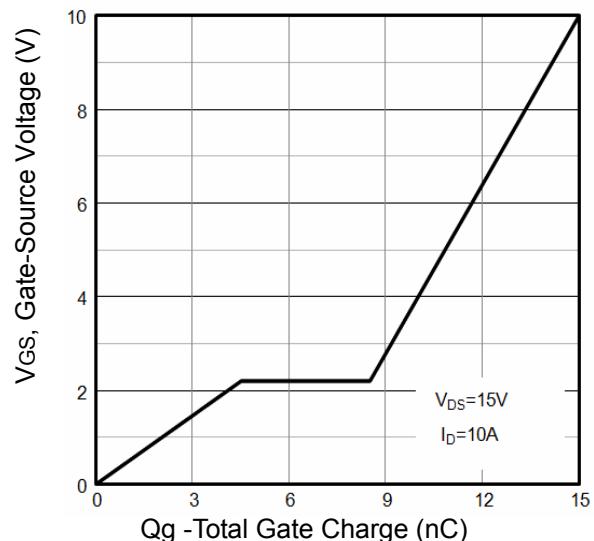


Fig8. Typical Gate Charge Vs.Gate-Source

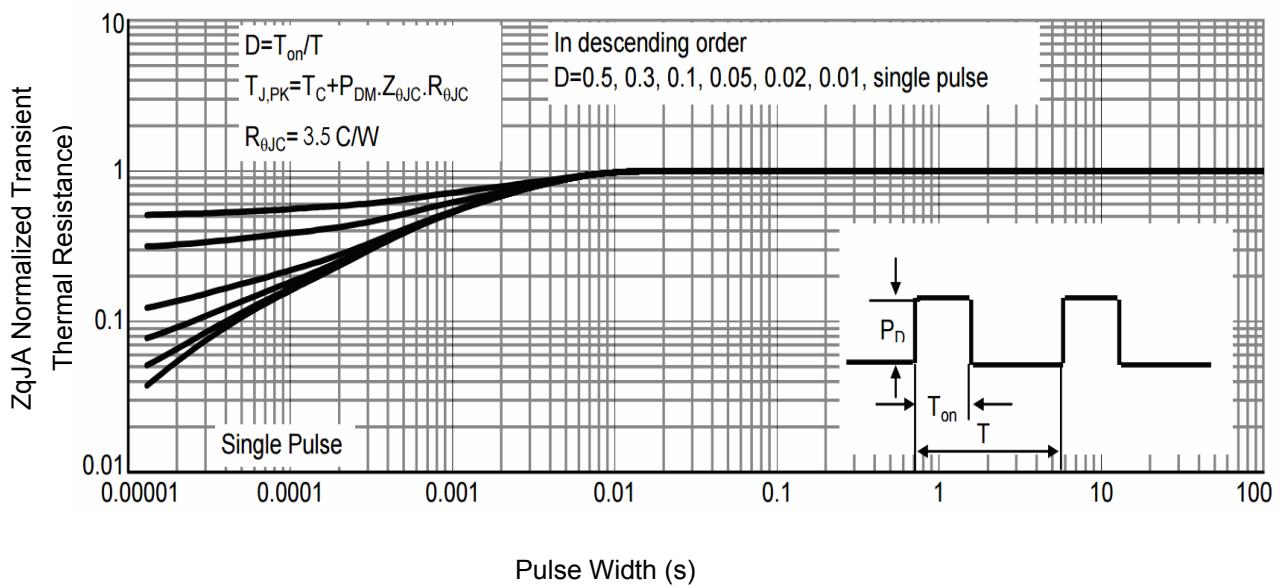


Fig 9 .Normalized Maximum Transient Thermal Impedance

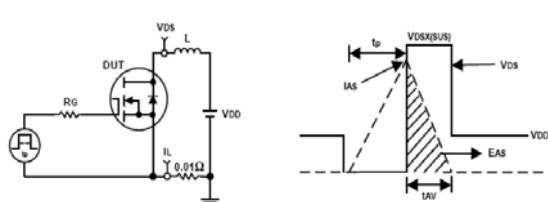


Fig10. Unclamped Inductive Test Circuit and waveforms

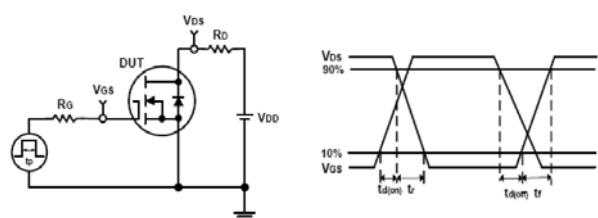
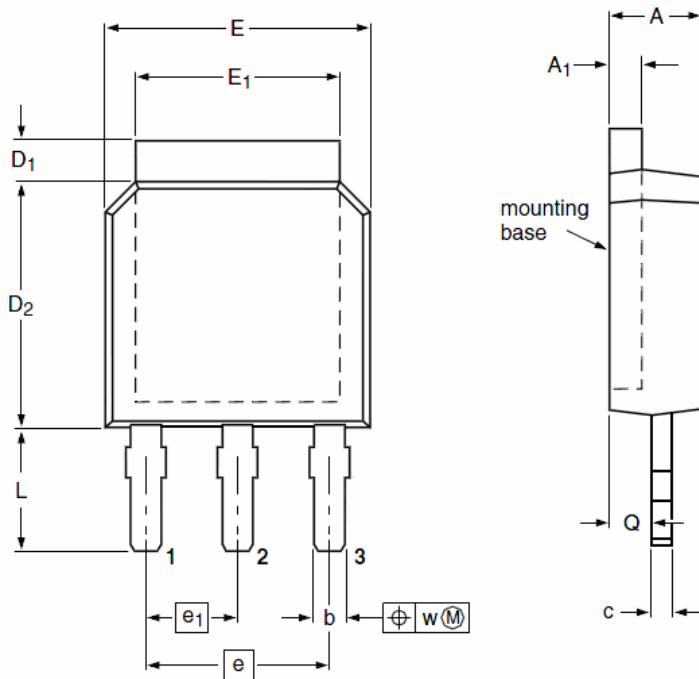


Fig11. Switching Time Test Circuit and waveforms

TO-251-S Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.10	2.30	2.50	A ₁	0.40	0.48	0.60
b	0.65	0.75	0.85	c	0.40	0.50	0.60
D ₁	0.65	0.90	1.20	D ₂	5.90	6.08	6.25
E	6.35	6.58	6.80	E ₁	5.10	5.28	5.50
e	--	2.28	--	e ₁	--	4.57	--
L	4.75	5.15	5.85	Q	0.80	0.90	1.08
w	--	0.20	--				

Customer Service

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