

## Features

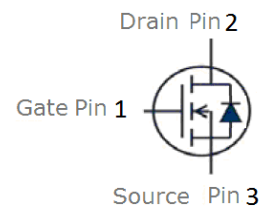
- N-Channel, 10V Logic Level Control
- Enhancement mode
- Very low on-resistance
- Fast Switching
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant



Part ID	Package Type	Marking	Tape and reel information
VSI018N08HS	TO-251-S	018N08H	80pcs/Tube

$V_{DS}$	80	V
$R_{DS(on),TYP@ V_{GS}=10V}$	14	m $\Omega$
$I_D$	53	A

TO-251-S



## Maximum ratings, at $T_j=25^{\circ}\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	80	V
$I_S$	Diode continuous forward current	$T_C=25^{\circ}\text{C}$	53 A
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C=25^{\circ}\text{C}$	53 A
		$T_C=100^{\circ}\text{C}$	33 A
$I_{DM}$	Pulse drain current tested ①	$T_C=25^{\circ}\text{C}$	150 A
EAS	Avalanche energy, single pulsed ②	20	mJ
$P_D$	Maximum power dissipation	$T_C=25^{\circ}\text{C}$	75 W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 175	$^{\circ}\text{C}$

## Thermal Characteristics

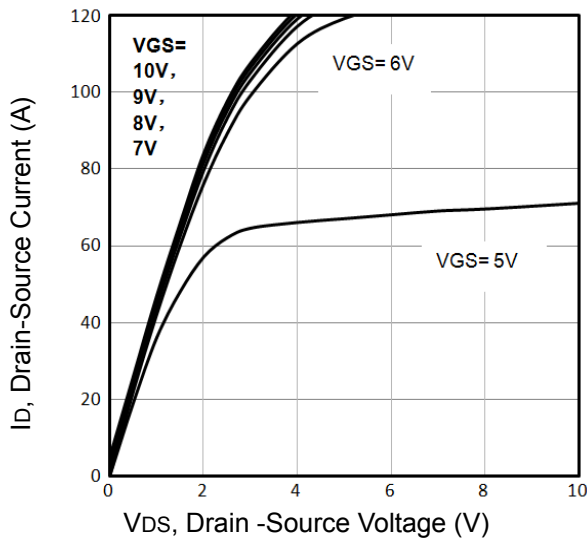
Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	2.0	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	42	$^{\circ}\text{C/W}$

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	80	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(T <sub>c</sub> =25°C)	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T <sub>c</sub> =125°C)	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0	3.0	4.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>③</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A	--	14.0	18	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz	--	2.5	--	Ω
C <sub>iss</sub>	Input Capacitance		--	2640	--	pF
C <sub>oss</sub>	Output Capacitance		--	160	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	110	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =40V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V	--	39	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	8	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	16	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =40V, I <sub>D</sub> =10A, R <sub>G</sub> =6.8Ω, V <sub>GS</sub> =10V	--	18	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	14	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	28	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	12	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>c</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =30A, V <sub>GS</sub> =0V	--	0.87	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>j</sub> =25°C, I <sub>sd</sub> =20A, V <sub>GS</sub> =0V	--	26	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=500A/μs		82		nC

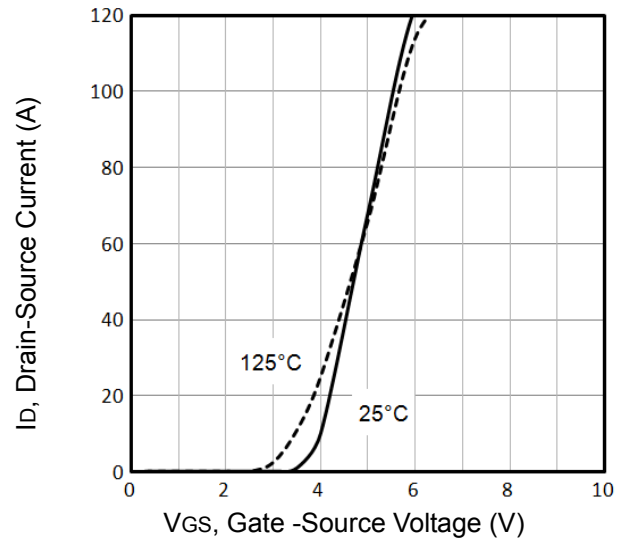
**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by T<sub>jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.1mH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 20A, V<sub>GS</sub> = 10V. Part not recommended for use above this value
- ③ Pulse width ≤ 300μs; duty cycle ≤ 2%.

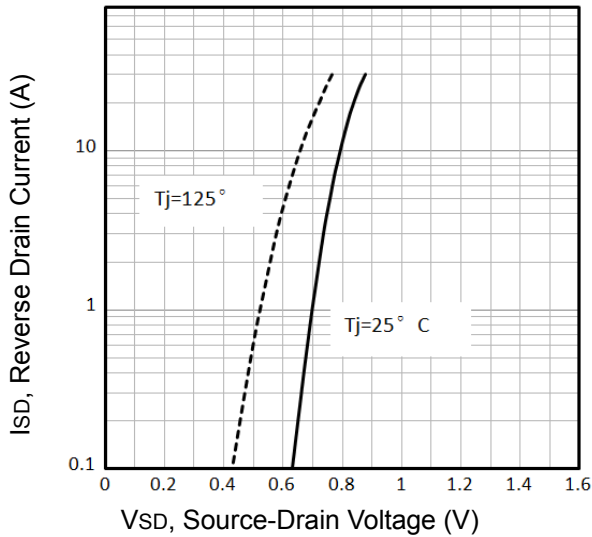
**Typical Characteristics**



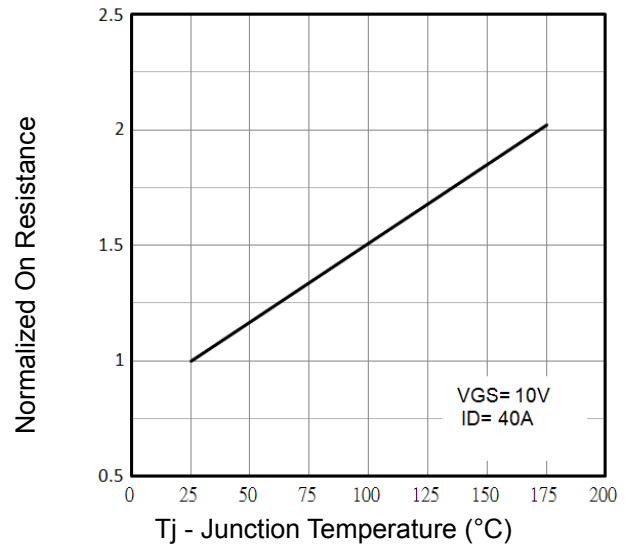
**Fig1.** Typical Output Characteristics



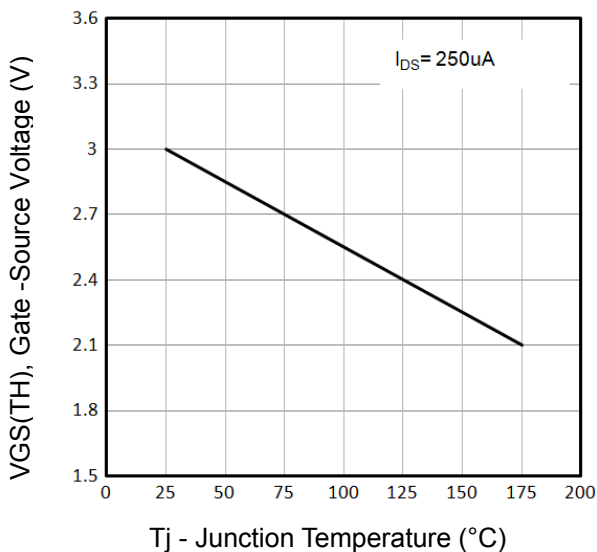
**Fig2.** Typical Transfer Characteristics



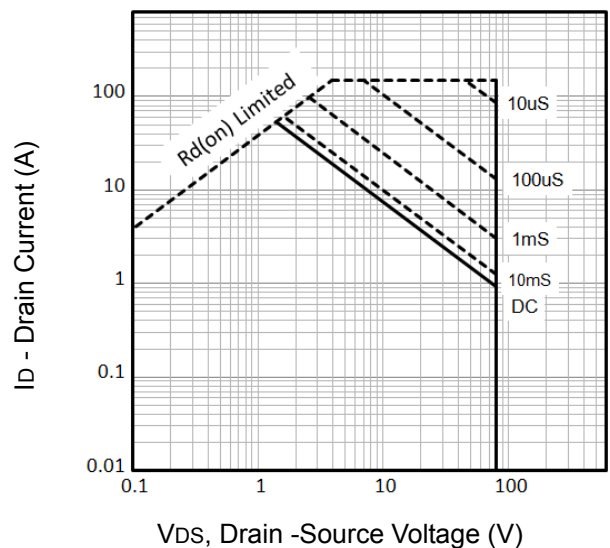
**Fig3.** Typical Source-Drain Diode Forward



**Fig4.** Normalized On-Resistance Vs. Tj

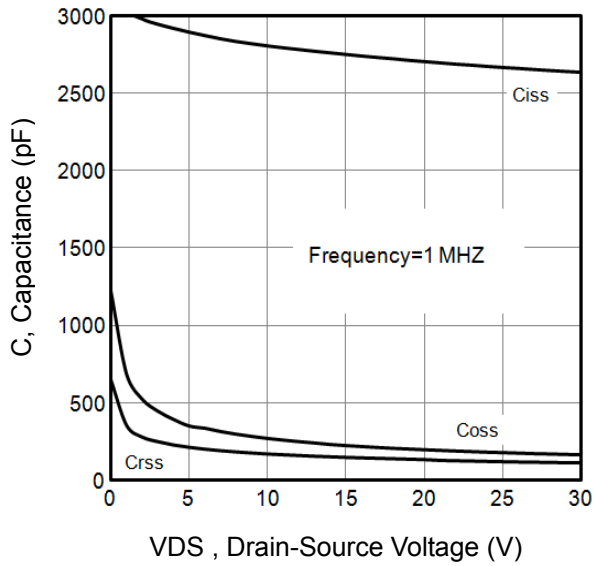


**Fig5.**  $V_{GS(TH)}$  Gate-Source Voltage Vs. Tj

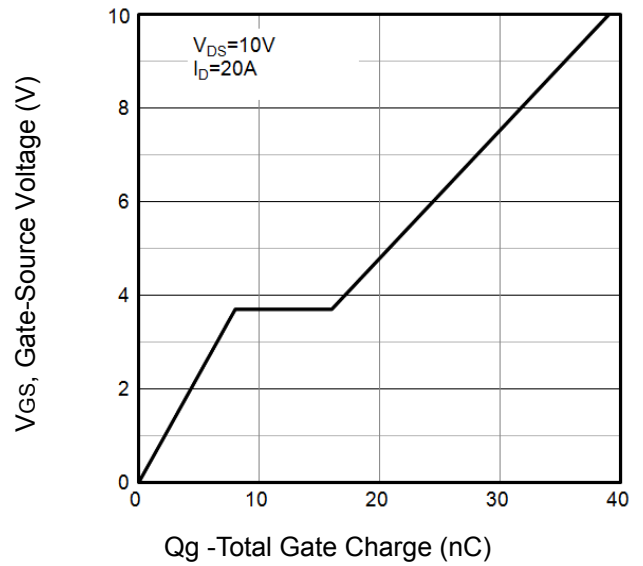


**Fig6.** Maximum Safe Operating Area

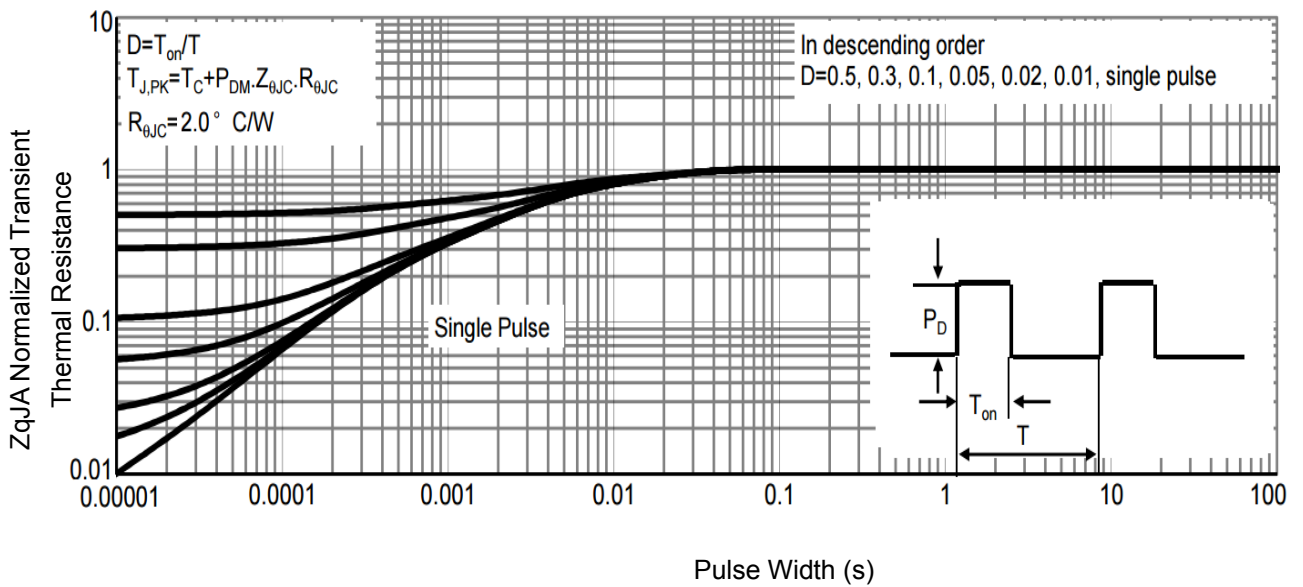
**Typical Characteristics**



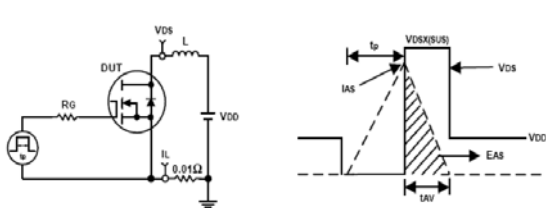
**Fig7.** Typical Capacitance Vs. Drain-Source



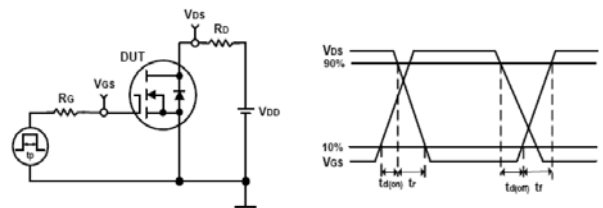
**Fig8.** Typical Gate Charge Vs. Gate-Source



**Fig9.** Normalized Maximum Transient Thermal

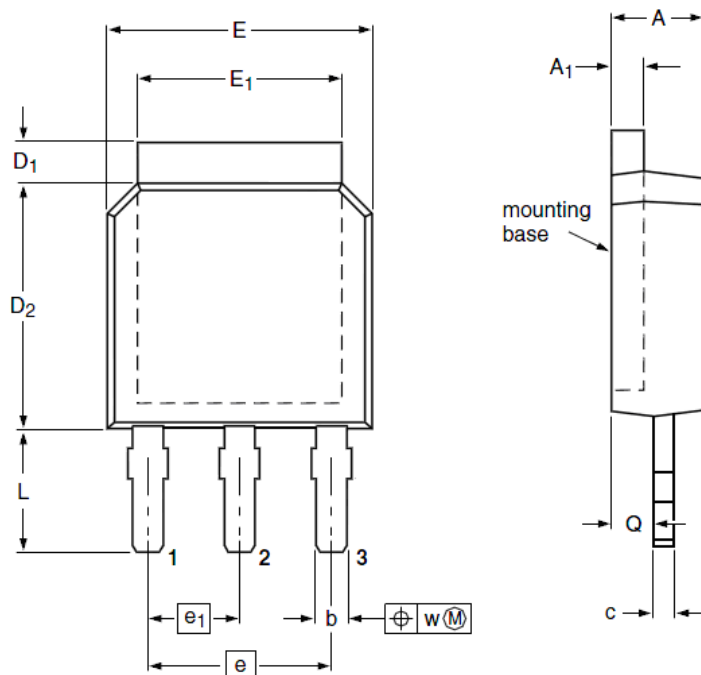


**Fig10.** Unclamped Inductive Test Circuit and waveforms



**Fig11.** Switching Time Test Circuit and waveforms

**TO-251-S Package Outline Data**



**DIMENSIONS** ( unit : mm )

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	2.10	2.30	2.50	A <sub>1</sub>	0.40	0.48	0.60
b	0.65	0.75	0.85	c	0.40	0.50	0.60
D <sub>1</sub>	0.65	0.90	1.20	D <sub>2</sub>	5.90	6.08	6.25
E	6.35	6.58	6.80	E <sub>1</sub>	5.10	5.28	5.50
e	--	2.28	--	e <sub>1</sub>	--	4.57	--
L	4.75	5.15	5.85	Q	0.80	0.90	1.08
w	--	0.20	--				

**Customer Service**

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