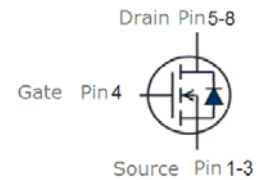


## Features

- N-Channel
- Enhancement mode
- Low on-resistance  $R_{DS(on)}$  @  $V_{GS}=4.5\text{ V}$
- Fast Switching
- Pb-free lead plating; RoHS compliant

$V_{DS}$	30	V
$R_{DS(on),Typ}$ @ $V_{GS}=10\text{ V}$	17	$m\Omega$
$R_{DS(on),Typ}$ @ $V_{GS}=4.5\text{ V}$	22	$m\Omega$
$I_D$	8	A



Part ID	Package Type	Marking	Tape and reel information
VSO018N03MS	SOP8	018N03M	3000pcs/reel

## Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V_{(BR)DSS}$	Drain-Source breakdown voltage	30	V	
$I_D$	Continuous drain current @ $V_{GS}=10\text{ V}$	$T_C=25\text{ }^\circ\text{C}$	8	A
		$T_C=100\text{ }^\circ\text{C}$	5.2	A
$I_{DM}$	Pulse drain current tested ①	$T_C=25\text{ }^\circ\text{C}$	32	A
$P_D$	Maximum power dissipation	$T_C=25\text{ }^\circ\text{C}$	2	W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V	
MSL		Level 3		
$T_{STG}$	Storage and operating temperature range	-55 to 150	$^\circ\text{C}$	

### Thermal characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	62.5	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	1.55	$^\circ\text{C/W}$

Symbol	Parameter	Condition	Min	Typ	Max	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (T <sub>c</sub> =25°C)	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current (T <sub>c</sub> =125°C)	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.8	2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	--	17	20	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance	V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A	--	22	25	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	--	445	--	pF
C <sub>oss</sub>	Output Capacitance		--	75	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	40	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =20V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V	--	15	--	nC
Q <sub>gs</sub>	GateSource Charge		--	4.5	--	nC
Q <sub>gd</sub>	GateDrain Charge		--	5.0	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turnon Delay Time	V <sub>DD</sub> =20V, I <sub>D</sub> =6A, R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =10V	--	15	--	nS
t <sub>r</sub>	Turnon Rise Time		--	19	--	nS
t <sub>d(off)</sub>	TurnOff Delay Time		-	30	--	nS
t <sub>f</sub>	TurnOff Fall Time		--	13	--	nS
<b>Source Drain Diode Characteristics</b>						
I <sub>SD</sub>	Sourcedrain current(Body Diode)	T <sub>c</sub> =25°C	7.5 <sup>①</sup>	--	--	A
V <sub>SD</sub>	Forward on voltage	T <sub>J</sub> =25°C, I <sub>SD</sub> =5A, V <sub>GS</sub> =0V	--	0.82	1.3	V

Notes:

- ① Pulse test ; Pulse width ≤ 300μs, duty cycle ≤ 2%.
- ② Pulse width limited by maximum allowable junction temperature

**Typical Characteristics**

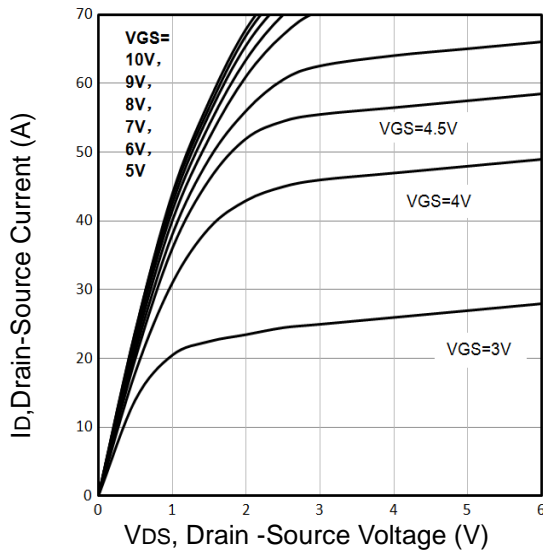


Fig1. Typical Output Characteristics

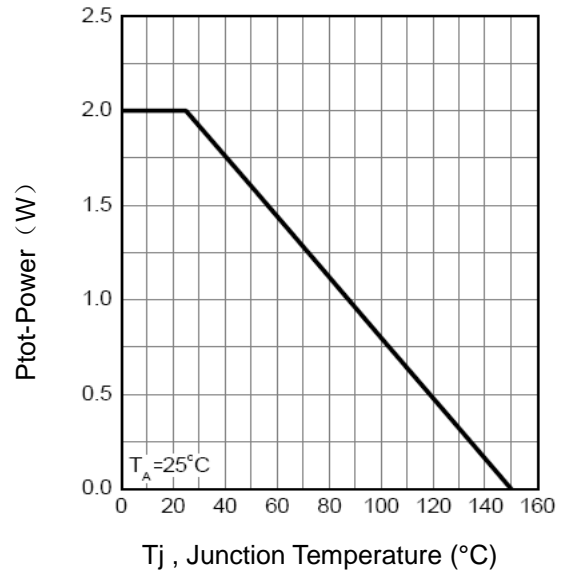


Fig2. Power Dissipation

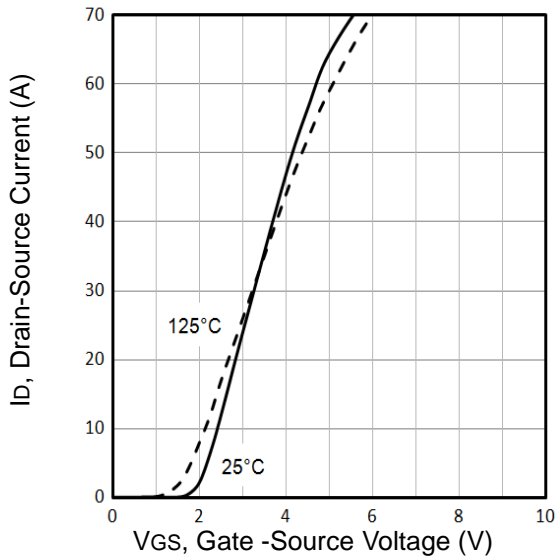


Fig3. Typical Transfer Characteristics

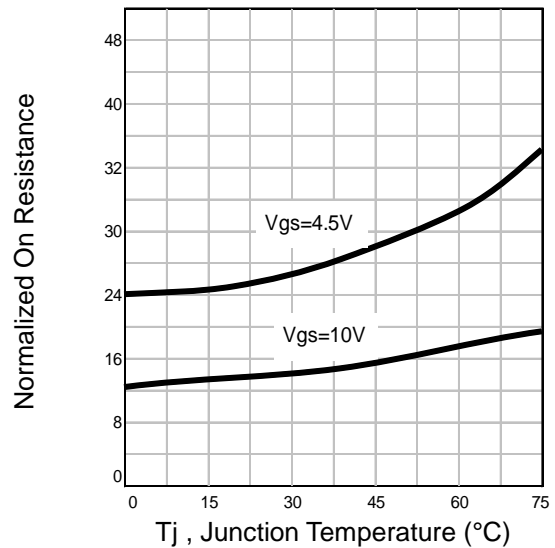


Fig4. Normalized On-Resistance Vs. Temperature

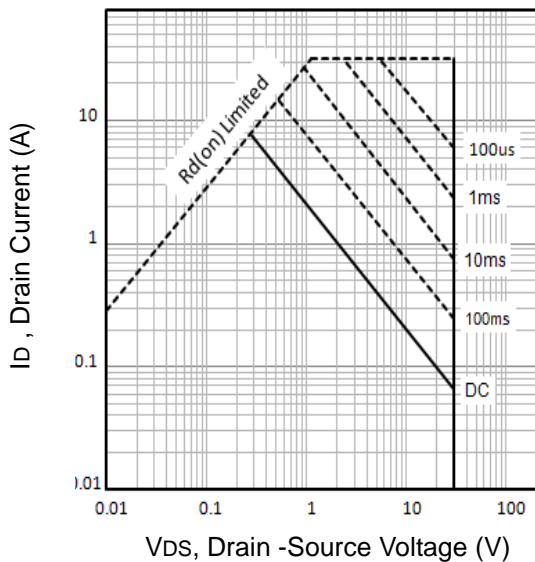


Fig5. Maximum Safe Operating Area

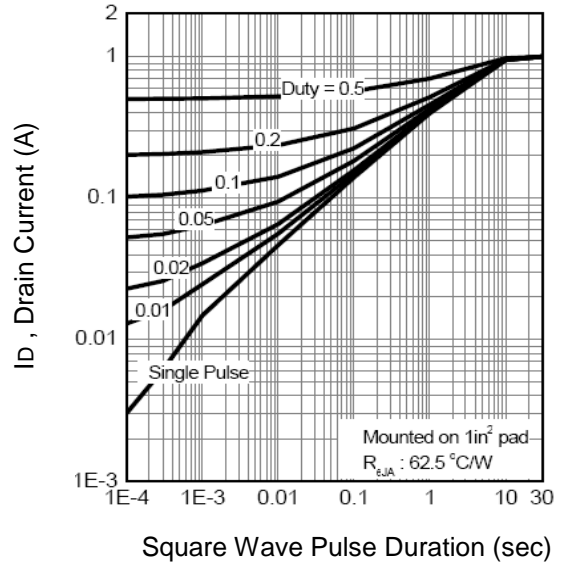


Fig6. Thermal Transient Impedance

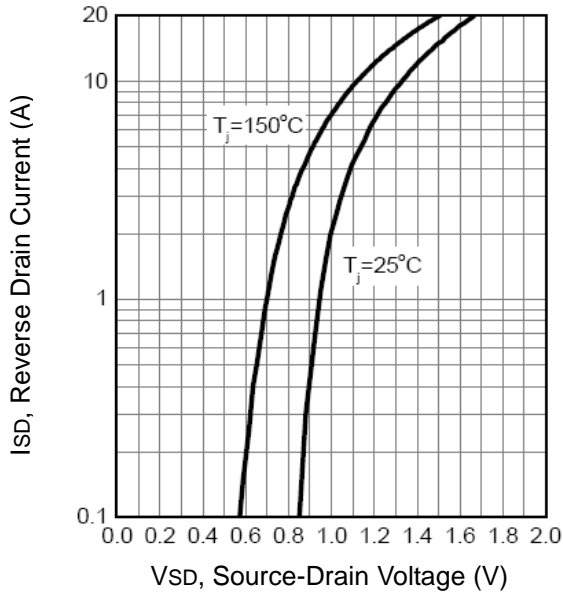


Fig7. Typical Source-Drain Diode Forward Voltage

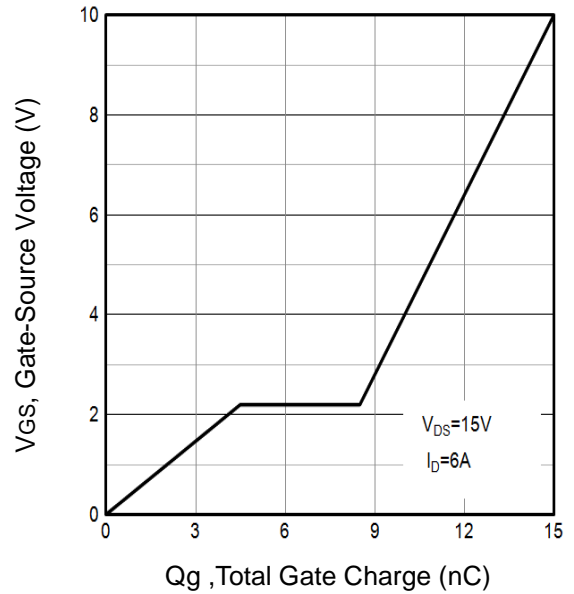


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

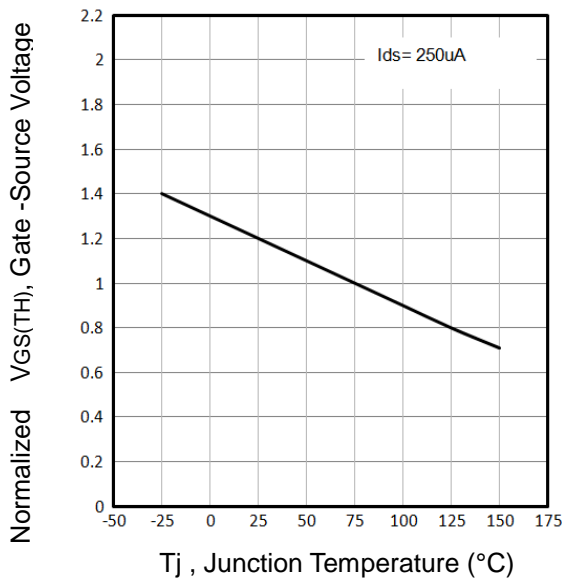


Fig9. Threshold Voltage Vs. Temperature

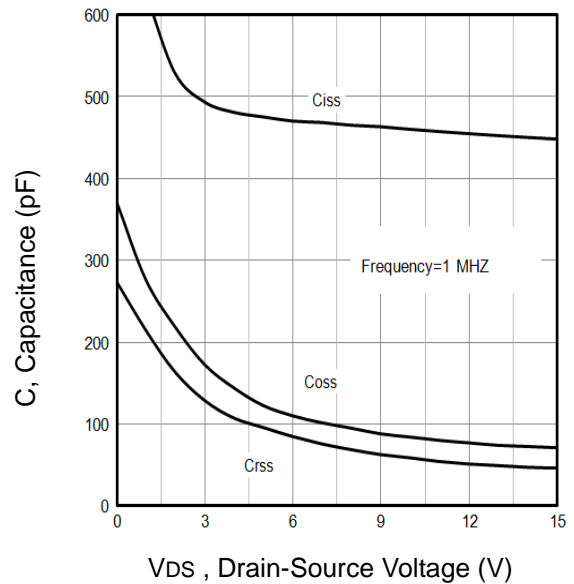


Fig10. Typical Capacitance Vs. Drain-Source Voltage

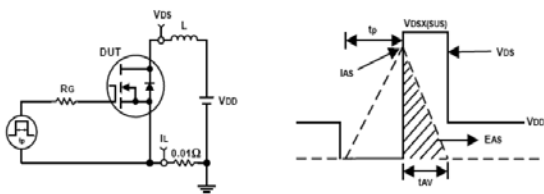


Fig11. Unclamped Inductive Test Circuit and waveforms

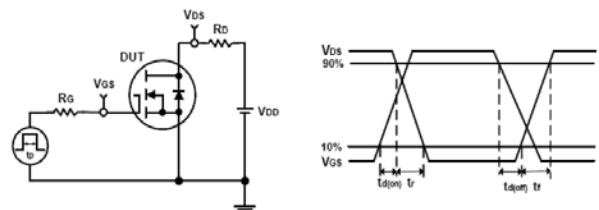
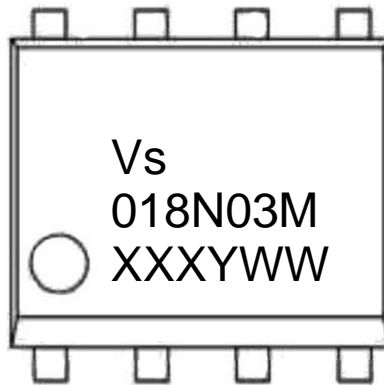


Fig12. Switching Time Test Circuit and waveforms



## Marking Information



1<sup>st</sup> line: Company Code (Vs), Company Logo

2<sup>nd</sup> line: Part Number (018N03M)

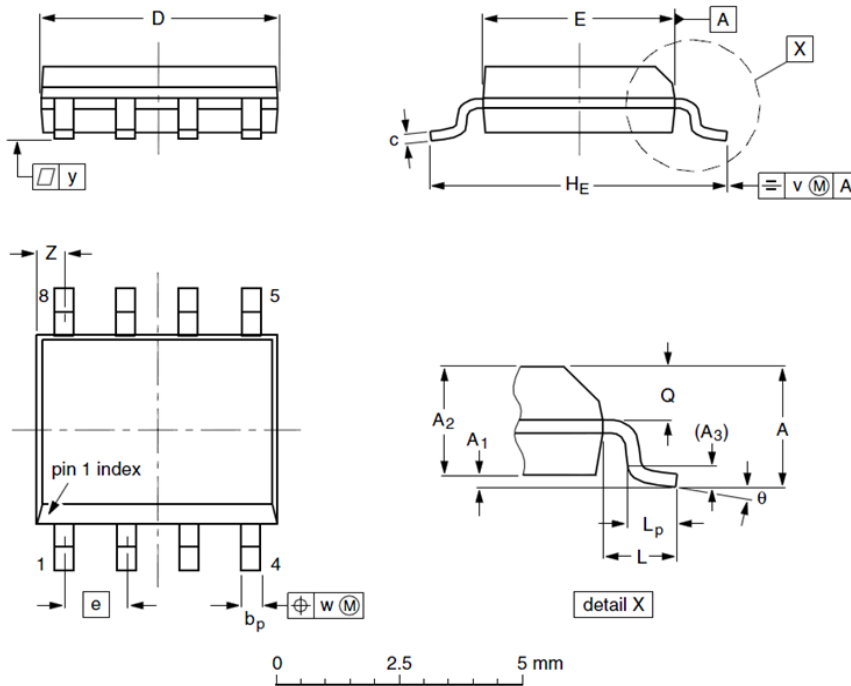
3<sup>rd</sup> line: Date code (XXXYWW)

XXX: Wafer Lot Number

Y: Year Code, e.g. E means 2017

WW: Week Code

SOP8 Package Outline Data



Label	Dimensions (unit: mm)		
	Min	Typ	Max
A	--	--	1.75
A <sub>1</sub>	0.10	0.18	0.25
A <sub>2</sub>	1.25	1.35	1.50
A <sub>3</sub>	--	0.25	--
bp	0.36	0.42	0.51
c	0.19	0.22	0.25
D	4.80	4.92	5.00
E	3.80	3.90	4.00
e	--	1.27	--
HE	5.80	6.00	6.20
L	--	1.05	--
L <sub>p</sub>	0.40	0.68	1.00
Q	0.60	0.65	0.725
v	--	0.25	--
w	--	0.25	--
y	--	0.10	--
Z	0.30	0.50	0.70
θ	0°		8°

Notes:

1. Follow JEDEC MS-012.
2. Dimension "D" does NOT include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15mm per side.
3. Dimension "E" does NOT include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.25mm per side.
4. Dimension "bp" does NOT include dambar protrusion. Allowable dambar protrusion shall be 0.1mm total in excess of "bp" dimension at maximum material condition. The dambar cannot be located on the lower radius of the foot.

Customer Service

Sales and Service:

[sales@vgsemi.com](mailto:sales@vgsemi.com)

Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: [www.vgsemi.com](http://www.vgsemi.com)