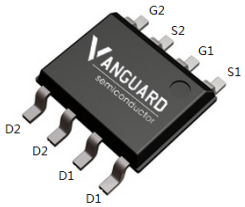
Features

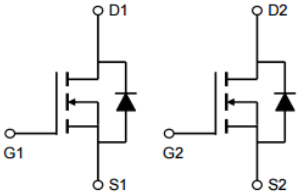
- Dual N-Channel
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ V_{GS}=4.5 V
- Fast Switching
- High Effective
- Pb-free lead plating; RoHS compliant; Hg-Free

V_{DS}	40	V
$R_{DS(on),TYP}$@ V_{GS}=10 V	32	mΩ
$R_{DS(on),TYP}$@ V_{GS}=4.5 V	38	mΩ
I_D	6	A

SOP8



Pb-Free RoHS HF Halogen-Free



Part ID	Package Type	Marking	Tape and reel information
VSO040N04MD	SOP8	040N04MD	3000pcs/reel

Maximum ratings, at T_j=25 °C, unless otherwise specified

Symbol	Parameter		Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage		40	V
I_S	Diode continuous forward current	T_A=25°C	1.4	A
I_D	Continuous drain current@V_{GS}=10V	T_A=25°C	6	A
		T_A=100°C	3.8	A
I_{DM}	Pulse drain current tested ①	T_A=25°C	24	A
P_D	Maximum power dissipation	T_A=25°C	1.8	W
V_{GS}	Gate-Source voltage		±20	V
T_{STG}	Storage temperature range		-55 to 175	°C
T_J	Maximum Junction Temperature		150	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	30	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	70	°C/W

Typical Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=250μA	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current(Tc=25℃)	V_{DS}=32V,V_{GS}=0V	--	0.01	1	μA
	Zero Gate Voltage Drain Current(Tc=125℃)	V_{DS}=32V,V_{GS}=0V	--	5	100	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V,V_{DS}=0V	--	--	±100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=250μA	1.0	1.5	2.5	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=10V, I_D=5A	--	32	40	mΩ
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=4.5V, I_D=2A	--	38	50	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C_{iss}	Input Capacitance	V_{DS}=20V,V_{GS}=0V, f=1MHz	--	345	--	pF
C_{oss}	Output Capacitance		--	43	--	pF
C_{rss}	Reverse Transfer Capacitance		--	18	--	pF
Q_g	Total Gate Charge	V_{DS}=20V,I_D=5A, V_{GS}=10V	--	8.6	--	nC
Q_{gs}	Gate-Source Charge		--	1.7	--	nC
Q_{gd}	Gate-Drain Charge		--	2.2	--	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=20V, I_D=1A, R_G=6.8Ω, V_{GS}=10V	--	5.8	--	nS
t_r	Turn-on Rise Time		--	2.8	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	16	--	nS
t_f	Turn-Off Fall Time		--	2.2	--	nS
Source- Drain Diode Characteristics@ T_J = 25°C (unless otherwise stated)						
I_{SD}	Source-drain current(Body Diode)	T_C=25℃	--	--	6	A
V_{SD}	Forward on voltage	I_{SD}=5A,V_{GS}=0V	--	0.85	1.3	V
t_{rr}	Reverse Recovery Time	T_j=25℃,I_{sd}=5A, V_{GS}=0V di/dt=100A/μs	--	25	--	nS
Q_{rr}	Reverse Recovery Charge			28		nC

NOTE:

①Pulse width ≤ 300μs; duty cycle≤ 2%.

Typical Characteristics

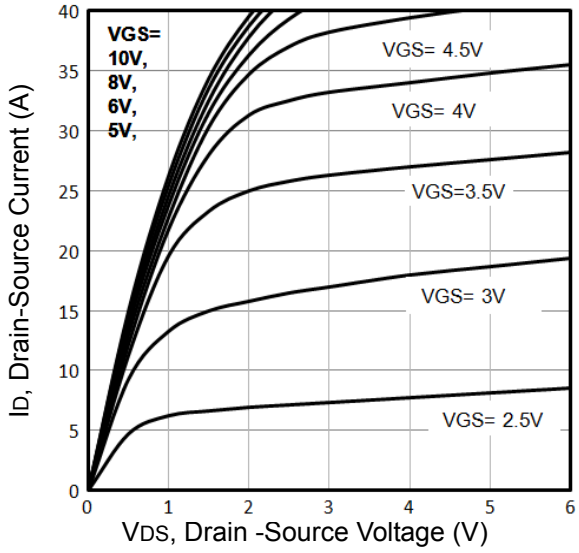


Fig1. Typical Output Characteristics

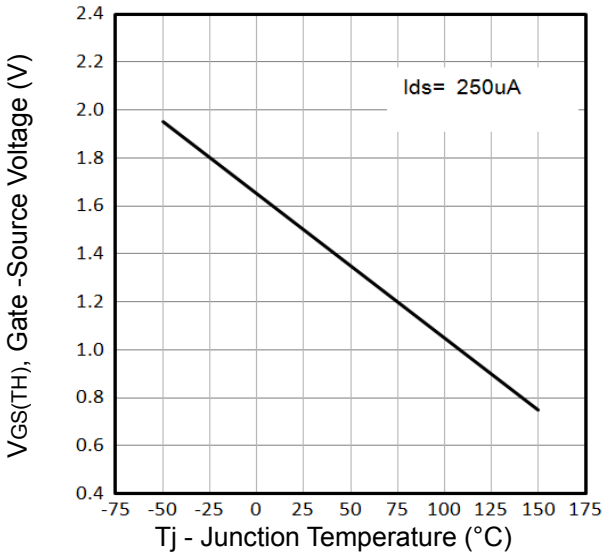


Fig2. Threshold Voltage Vs. Temperature

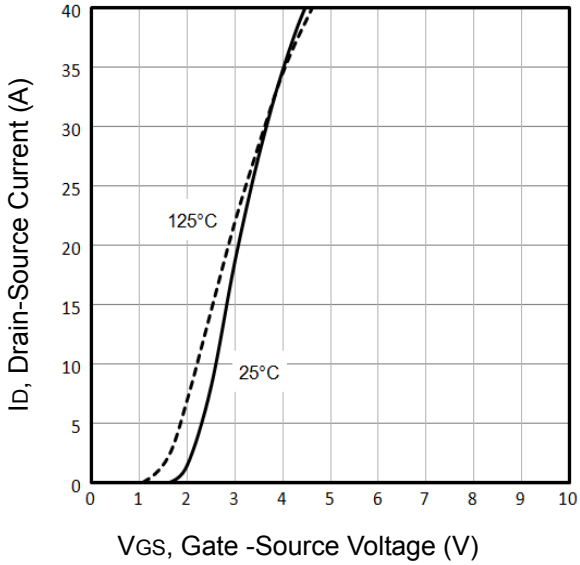


Fig3. Typical Transfer Characteristics

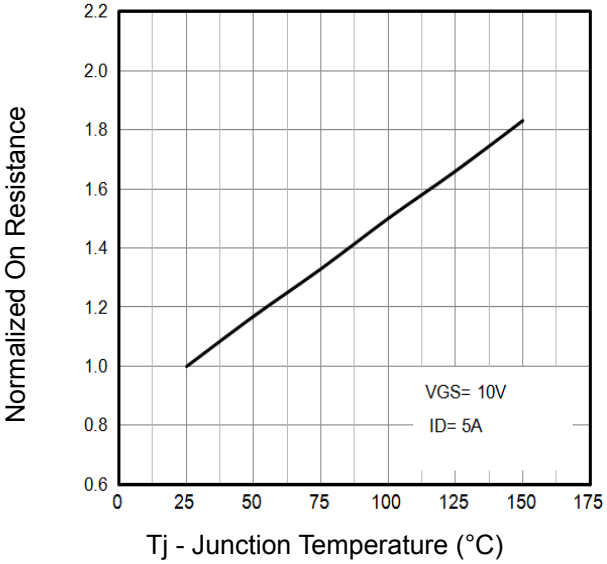


Fig4. Normalized On-Resistance Vs. Temperature

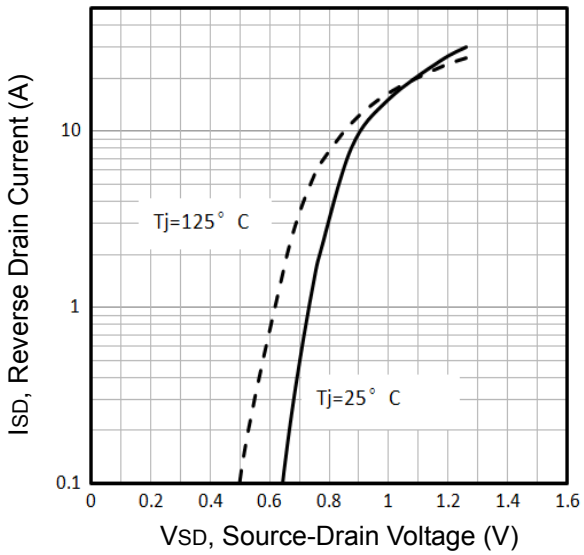


Fig5. Typical Source-Drain Diode Forward Voltage

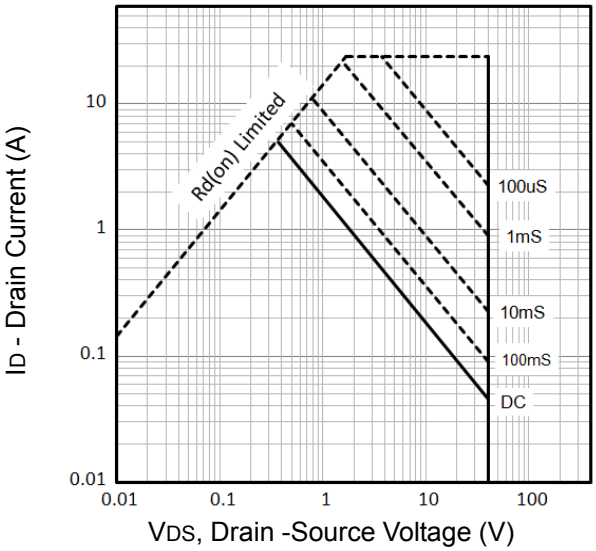


Fig6. Maximum Safe Operating Area

Typical Characteristics

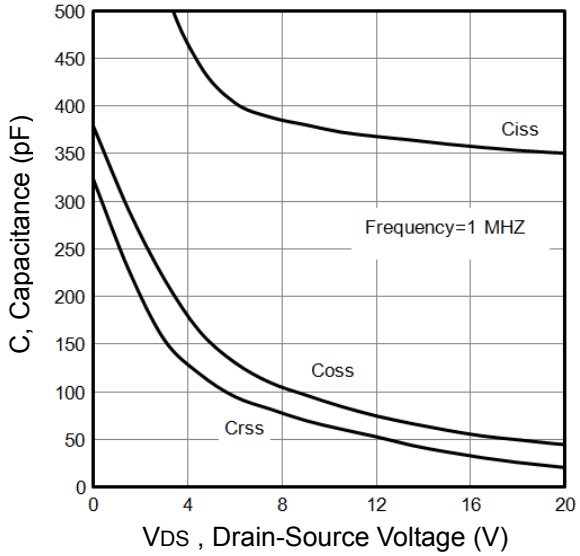


Fig7. Typical Capacitance Vs.Drain-Source Voltage

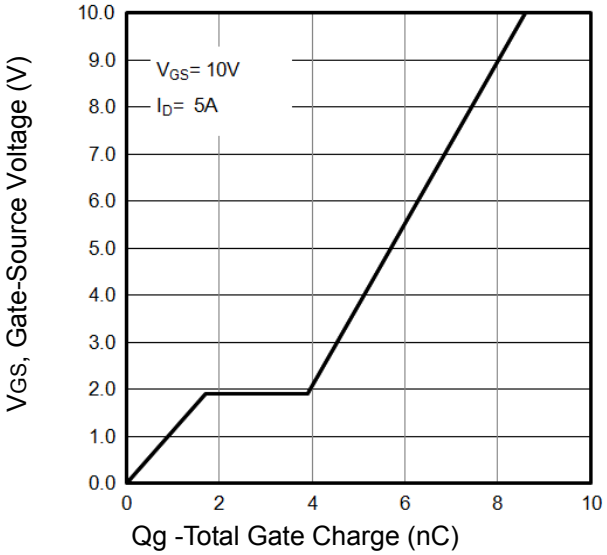


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

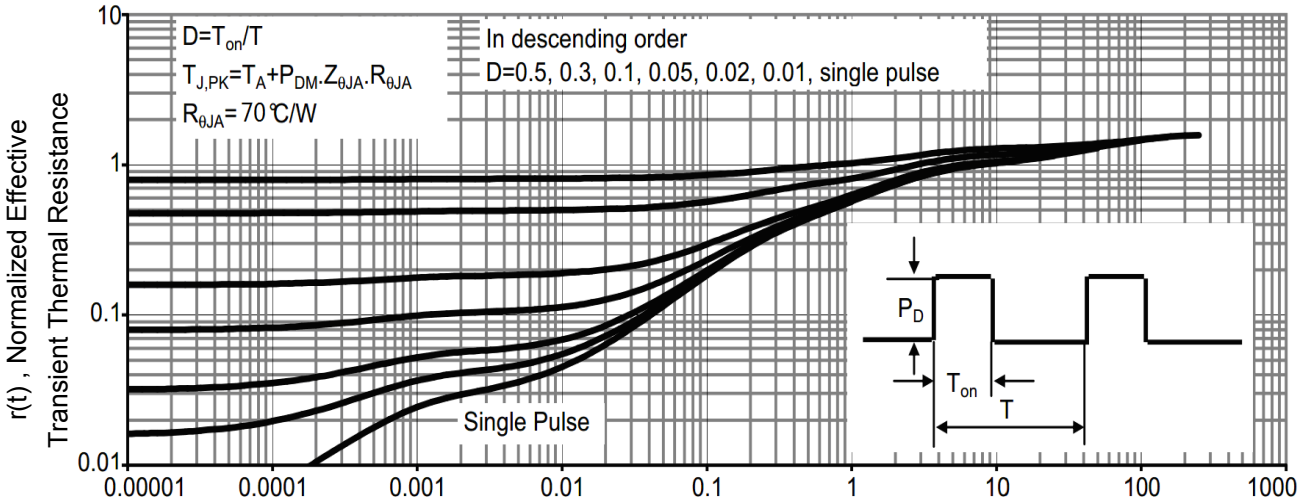


Fig9. T1 ,Transient Thermal Response Curve

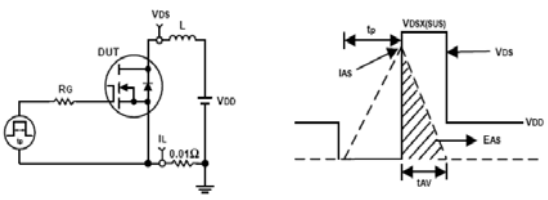


Fig10. Unclamped Inductive Test Circuit and waveforms

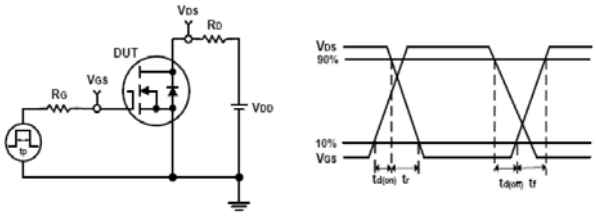
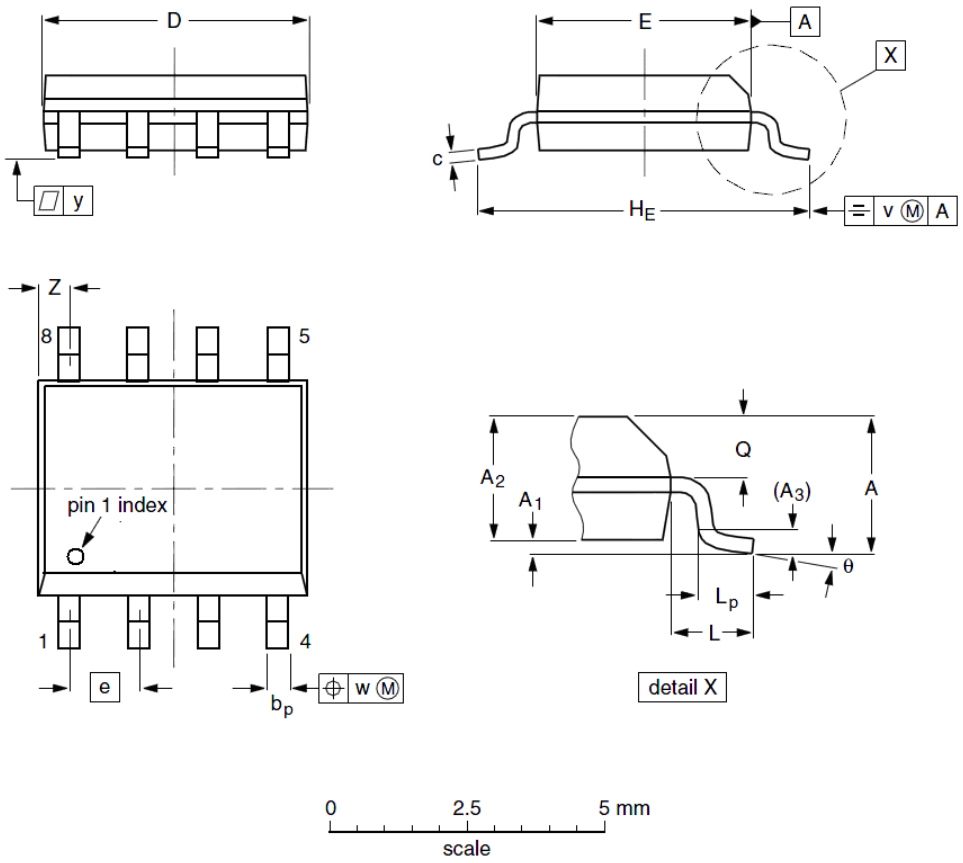


Fig11. Switching Time Test Circuit and waveforms

SOP8 Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	--	1.75	--	**A_1**	0.10	0.18	0.25
A_2	1.25	1.35	1.45	**A_3**	--	0.25	--
b_p	0.36	0.42	0.49	**c**	0.19	0.22	0.25
D	4.80	4.92	5.00	**E**	3.80	3.90	4.00
e	--	1.27	--	**H_E**	5.80	5.98	6.20
L	--	1.05	--	**L_p**	0.40	0.68	1.00
Q	0.60	0.65	0.70	**v**	--	0.25	--
w	--	0.25	--	**y**	--	0.10	--
Z	0.30	0.50	0.70	**θ**	0°		8°

Customer Service

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