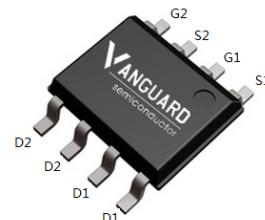


Features

- Dual P-Channel, -5V Logic Level Control
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=-4.5$ V
- Fast Switching
- High Effective
- Pb-free lead plating; RoHS compliant; Hg-Free

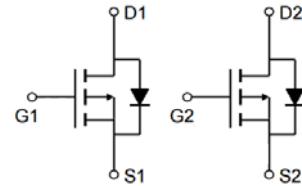
V_{DS}	-60	V
$R_{DS(on),TYP}$ @ $V_{GS}=-10$ V	70	$\text{m}\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=-4.5$ V	85	$\text{m}\Omega$
I_D	-4.6	A

SOP8



HF Halogen-Free

Part ID	Package Type	Marking	Tape and reel information
VSO100P06MD	SOP8	100P06MD	3000pcs/reel



Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit	
$V_{(BR)DSS}$	Drain-Source breakdown voltage	-60	V	
I_s	Diode continuous forward current	$T_A = 25^\circ\text{C}$	-2.3	A
I_D	Continuous drain current@ $V_{GS}=10$ V	$T_A = 25^\circ\text{C}$	-4.6	A
		$T_A = 100^\circ\text{C}$	-2.9	A
I_{DM}	Pulse drain current tested ②	$T_A = 25^\circ\text{C}$	-18.4	A
P_D	Power dissipation for Dual Operation	$T_A = 25^\circ\text{C}$	3	W
V_{GS}	Gate-Source voltage	± 20	V	
T_{STG}	Storage temperature range	-55 to 150	°C	
T_j	Maximum Junction Temperature①	150	°C	

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	24	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	40	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=-250\mu\text{A}$	-60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A = 25^\circ\text{C}$)	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-1	μA
	Zero Gate Voltage Drain Current($T_A = 125^\circ\text{C}$)	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	-100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=-250\mu\text{A}$	-1.0	-2.0	-3.0	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^②	$V_{\text{GS}}=-10\text{V}, I_D=-4\text{A}$	--	70	90	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ^②	$V_{\text{GS}}=-4.5\text{V}, I_D=-2\text{A}$	--	85	100	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	940	--	pF
C_{oss}	Output Capacitance		--	55	--	pF
C_{rss}	Reverse Transfer Capacitance		--	40	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=-30\text{V}, I_D=-4\text{A}, V_{\text{GS}}=-10\text{V}$	--	17.5	--	nC
Q_{gs}	Gate-Source Charge		--	2.8	--	nC
Q_{gd}	Gate-Drain Charge		--	3.6	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=-30\text{V}, I_D=-4\text{A}, R_G=6.8\Omega, V_{\text{GS}}=-10\text{V}$	--	9	--	nS
t_r	Turn-on Rise Time		--	4	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	33	--	nS
t_f	Turn-Off Fall Time		--	8	--	nS
Source-Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=-4\text{A}, V_{\text{GS}}=0\text{V}$	--	-0.85	-1.3	V
t_{rr}	Reverse Recovery Time	$T_J=25^\circ\text{C}, I_{\text{SD}}=-4\text{A}, V_{\text{GS}}=0\text{V}, \frac{di}{dt}=-100\text{A}/\mu\text{s}$	--	32	--	nS
Q_{rr}	Reverse Recovery Charge		--	39	--	nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

②Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics

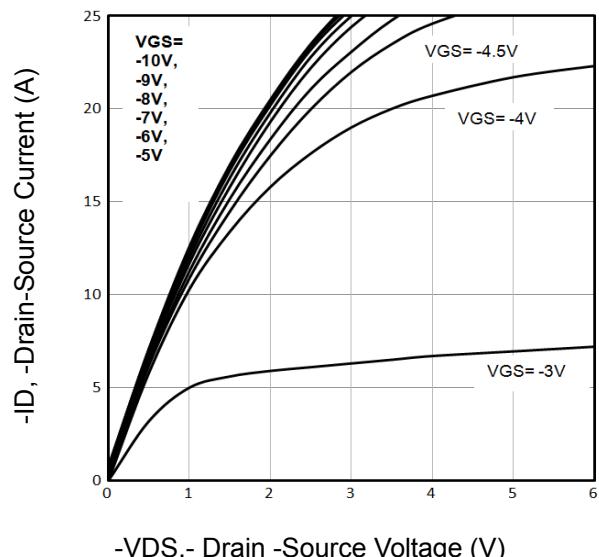


Fig1. Typical Output Characteristics

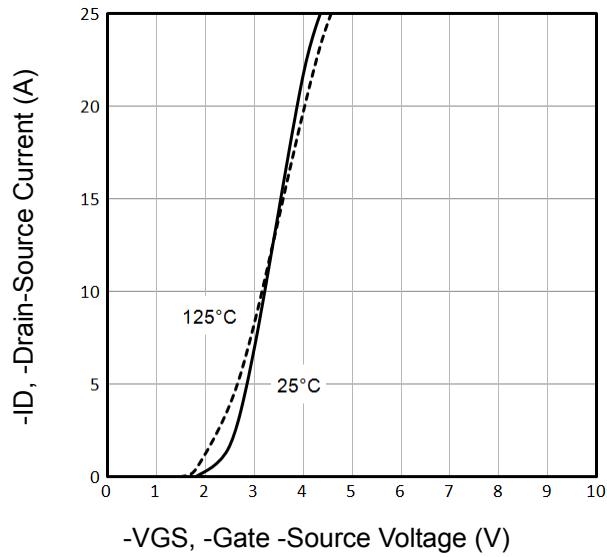


Fig3. Typical Transfer Characteristics

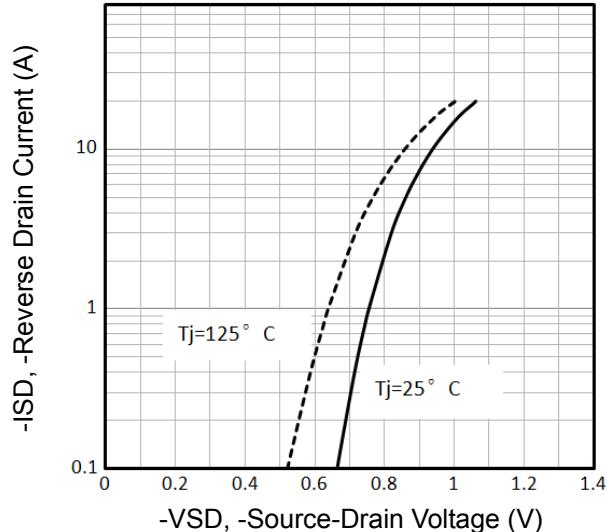


Fig5. Typical Source-Drain Diode Forward Voltage

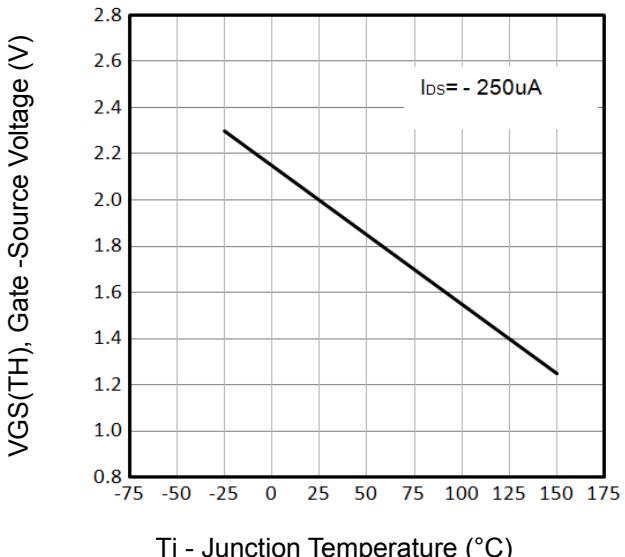


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

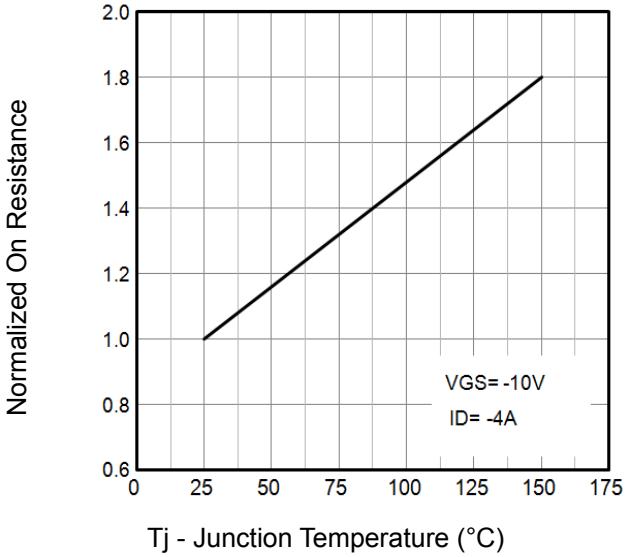


Fig4. Normalized On-Resistance Vs. T_j

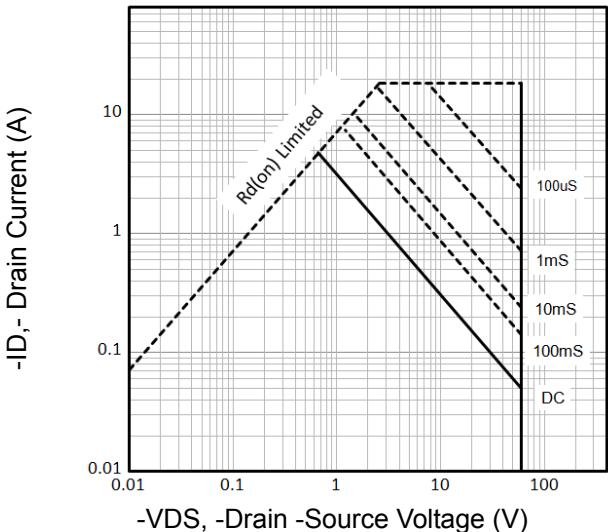


Fig6. Maximum Safe Operating Area

Typical Characteristics

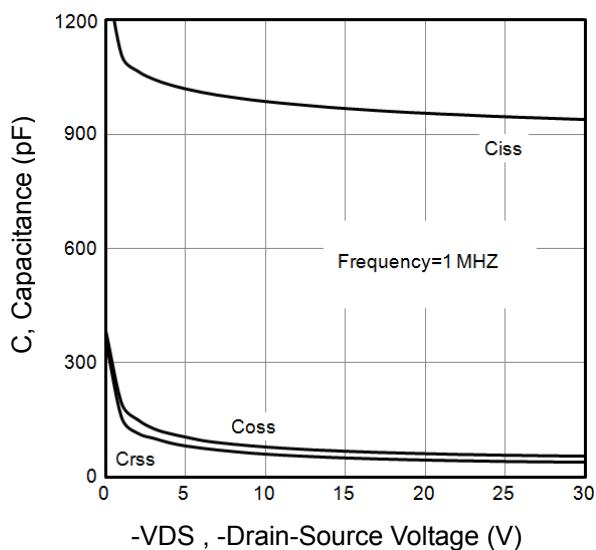


Fig7. Typical Capacitance Vs.Drain-Source

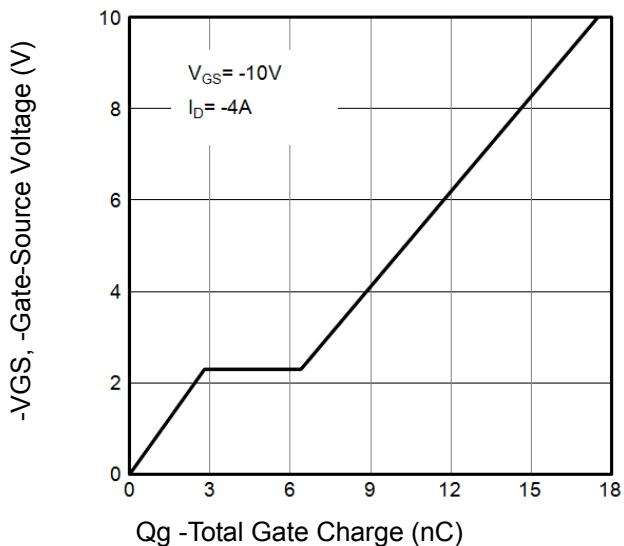


Fig8. Typical Gate Charge Vs.Gate-Source

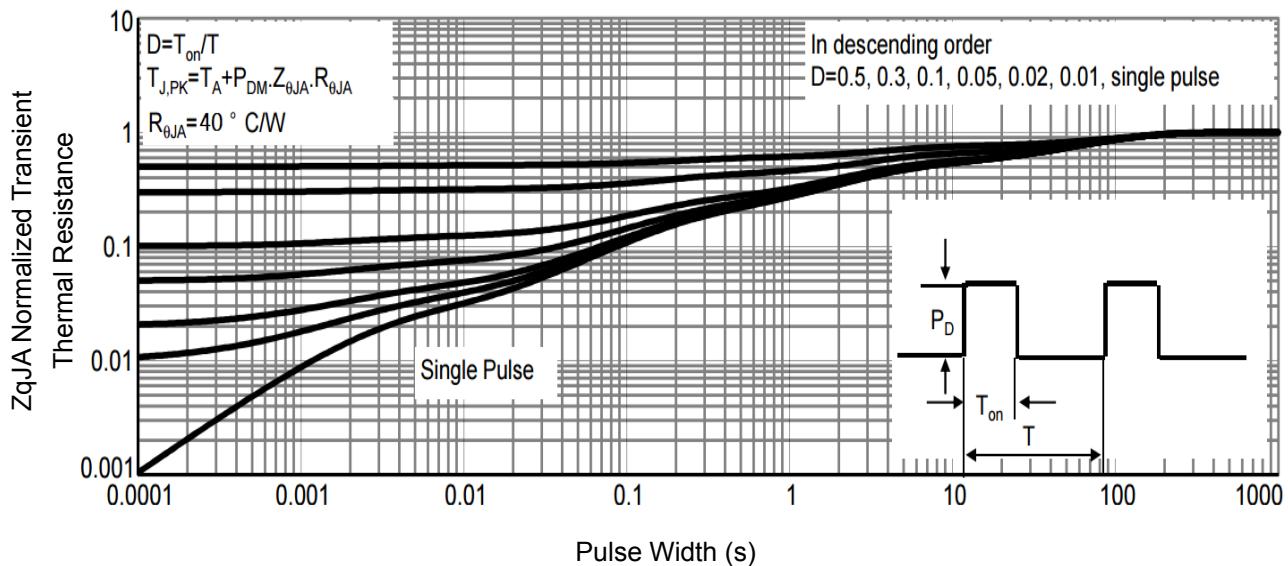


Fig9. Normalized Maximum Transient Thermal Impedance

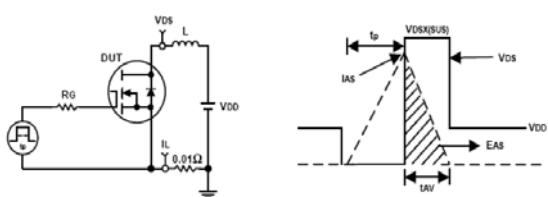


Fig10. Unclamped Inductive Test Circuit and Waveforms

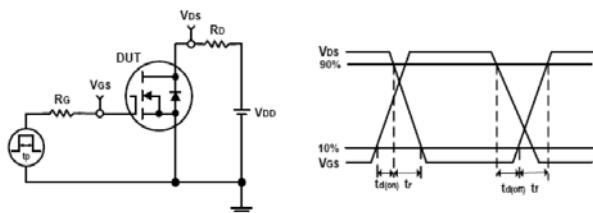
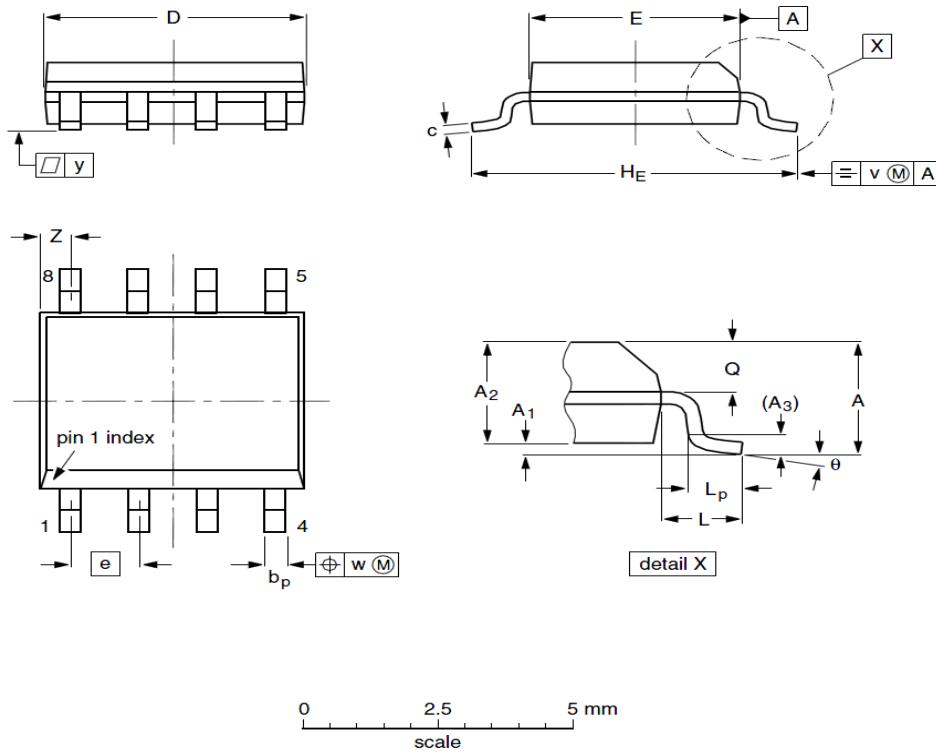


Fig11. Switching Time Test Circuit and waveforms

SOP8 Package Outline



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	--	1.75	--	A₁	0.10	0.18	0.25
A₂	1.25	1.35	1.45	A₃	--	0.25	--
b_p	0.36	0.42	0.49	c	0.19	0.22	0.25
D	4.80	4.92	5.00	E	3.80	3.90	4.00
e	--	1.27	--	H_E	5.80	5.98	6.20
L	--	1.05	--	L_p	0.40	0.68	1.00
Q	0.60	0.65	0.70	v	--	0.25	--
w	--	0.25	--	y	--	0.10	--
Z	0.30	0.50	0.70	θ	0°		8°

Customer Service

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