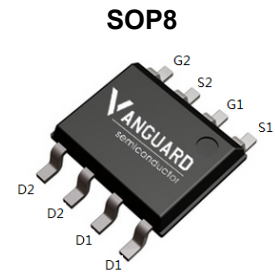


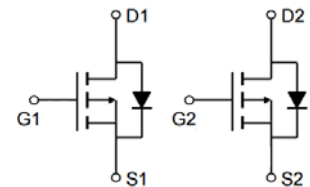
## Features

- Dual P-Channel, -5V Logic Level Control
- Enhancement mode
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=-4.5$  V
- Fast Switching
- High Effective
- Pb-free lead plating; RoHS compliant; Hg-Free

$V_{DS}$	-60	V
$R_{DS(on),TYP}@ V_{GS}=-10$ V	130	m $\Omega$
$R_{DS(on),TYP}@ V_{GS}=-4.5$ V	160	m $\Omega$
$I_D$	-3.4	A



Part ID	Package Type	Marking	Tape and reel information
VSO200P06MD	SOP8	200P06MD	3000pcs/reel



## Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	-60	V
$I_S$	Diode continuous forward current	$T_A=25^\circ\text{C}$ -2.3	A
$I_D$	Continuous drain current@ $V_{GS}=10$ V	$T_A=25^\circ\text{C}$ -3.4	A
		$T_A=100^\circ\text{C}$ -2.1	A
$I_{DM}$	Pulse drain current tested ②	$T_A=25^\circ\text{C}$ -13.6	A
$P_D$	Power dissipation for Dual Operation	$T_A=25^\circ\text{C}$ 3	W
$V_{GS}$	Gate-Source voltage	$\pm 20$	V
$T_{STG}$	Storage temperature range	-55 to 150	$^\circ\text{C}$
$T_J$	Maximum Junction Temperature①	150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	28	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	40	$^\circ\text{C/W}$

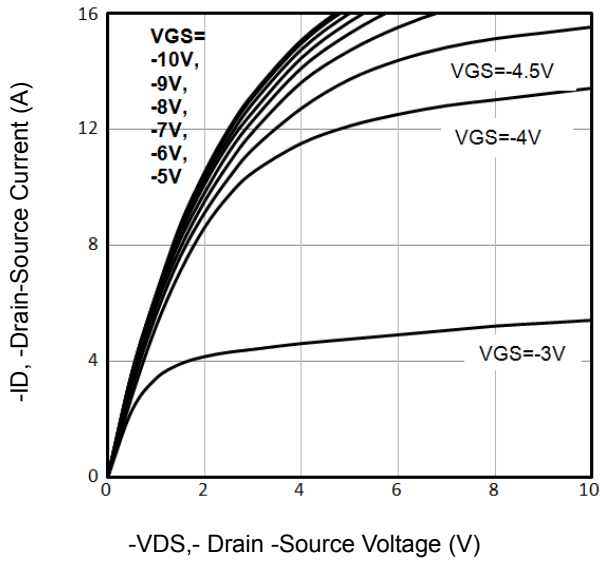
Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-60	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(Tc=25°C)	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	--	--	-1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V	--	--	-100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0	-2.0	-3.0	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>②</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.5A	--	130	180	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance <sup>②</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.5A	--	160	200	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V, f=1MHz	--	500	--	pF
C <sub>oss</sub>	Output Capacitance		--	30	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	20	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-30V, I <sub>D</sub> =-2A, V <sub>GS</sub> =-10V	--	12	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	1.8	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	4.2	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-30V, I <sub>D</sub> =-2A, R <sub>G</sub> =6.8Ω, V <sub>GS</sub> =-10V	--	8	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	10	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	22	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	11	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =-2.5A, V <sub>GS</sub> =0V	--	-0.86	-1.3	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> =25°C, I <sub>sd</sub> =-2A, V <sub>GS</sub> =0V	--	16.5	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=-100A/μs	--	8.8	--	nC

**NOTE:**

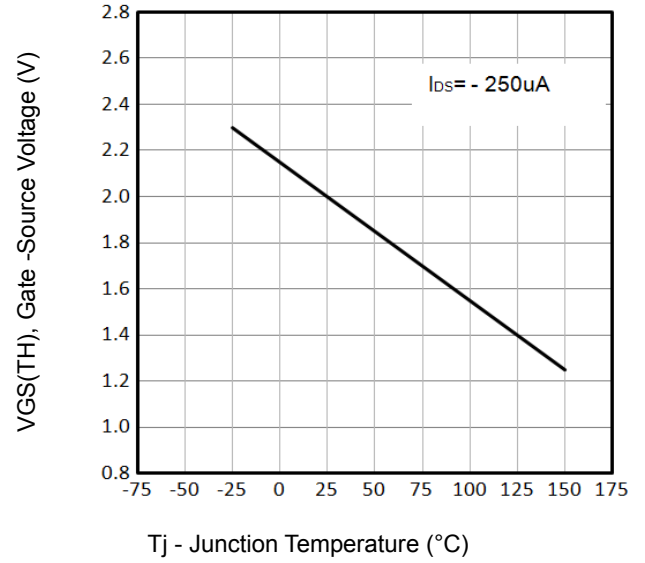
① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width ≤ 300μs; duty cycle ≤ 2%.

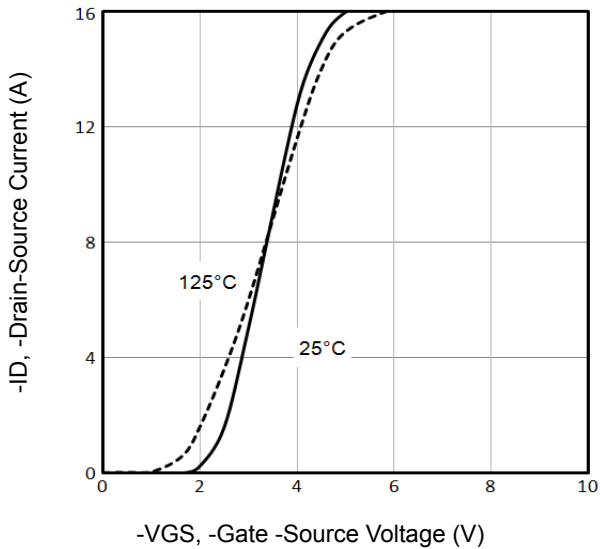
**Typical Characteristics**



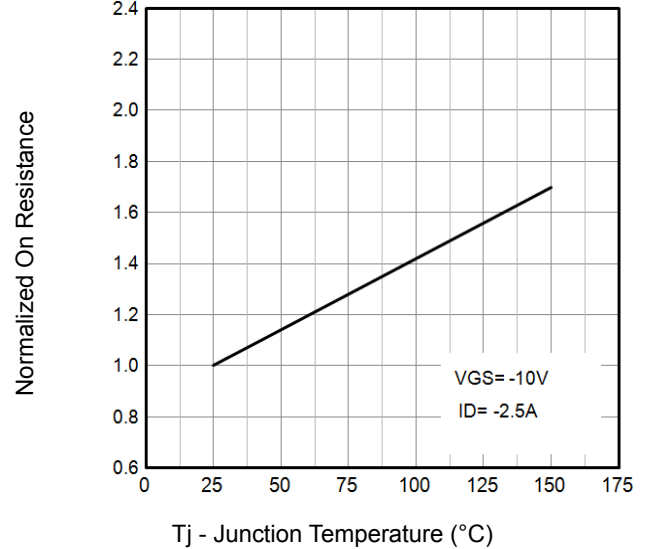
**Fig1.** Typical Output Characteristics



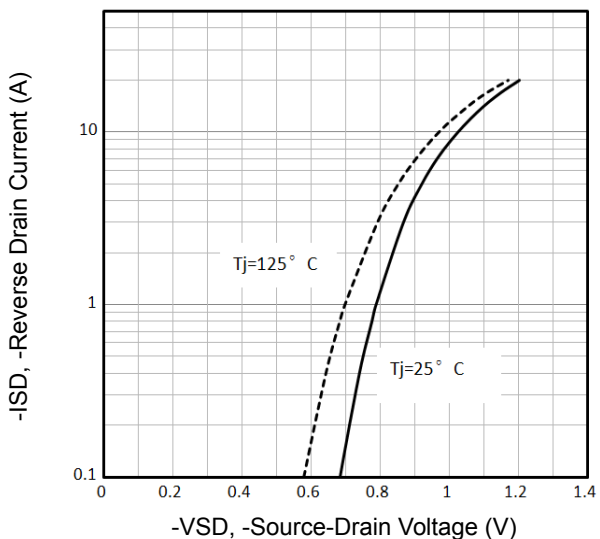
**Fig2.**  $V_{GS(TH)}$  Gate -Source Voltage Vs.  $T_j$



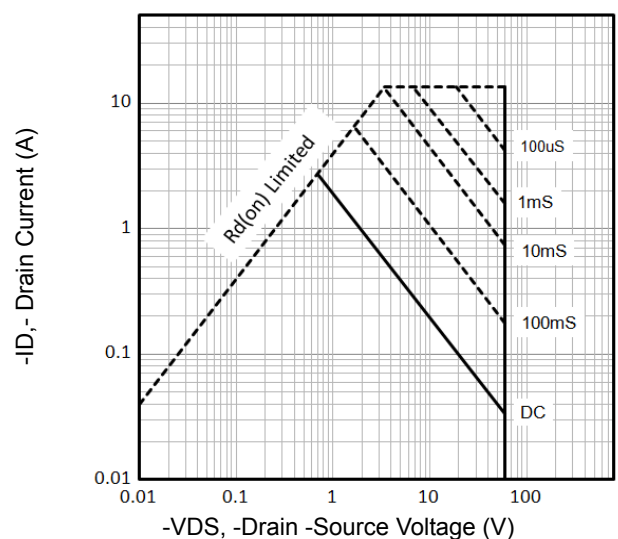
**Fig3.** Typical Transfer Characteristics



**Fig4.** Normalized On-Resistance Vs.  $T_j$

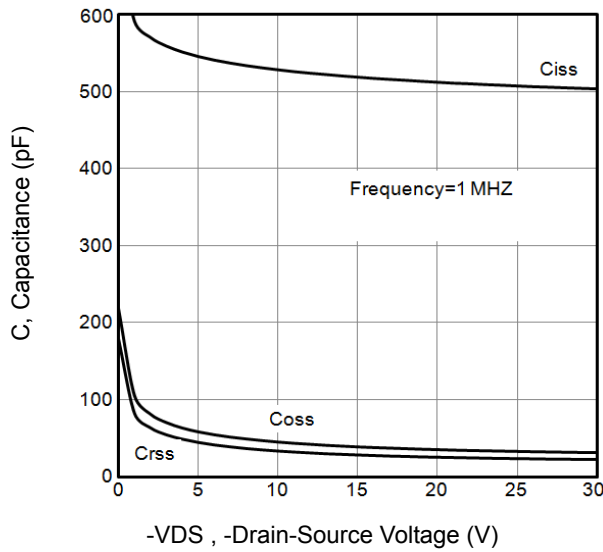


**Fig5.** Typical Source-Drain Diode Forward Voltage

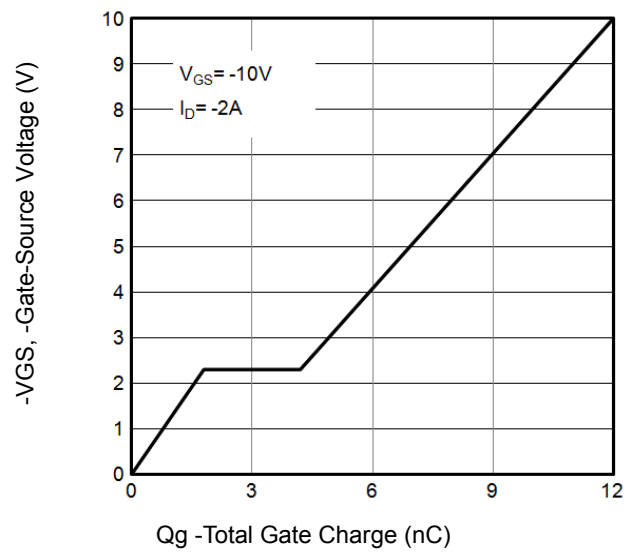


**Fig6.** Maximum Safe Operating Area

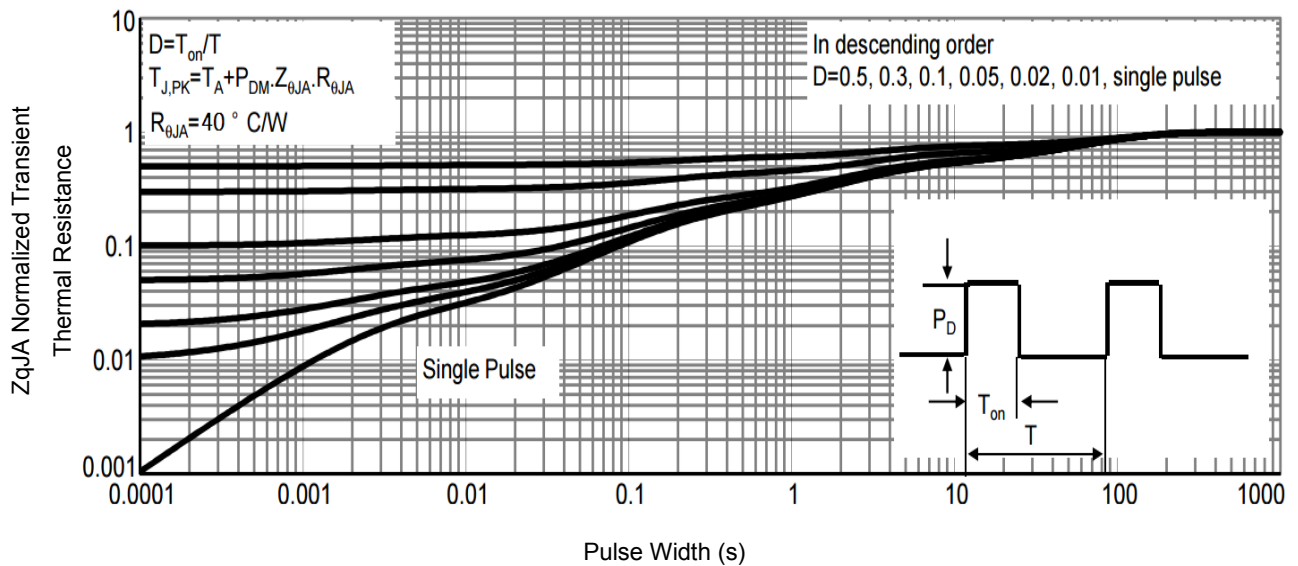
**Typical Characteristics**



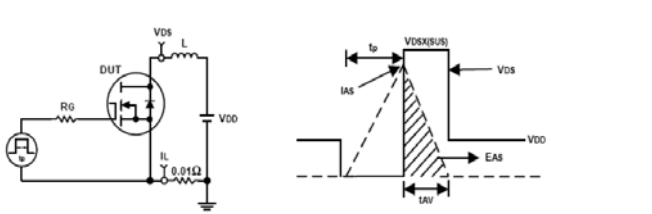
**Fig7.** Typical Capacitance Vs.Drain-Source Voltage



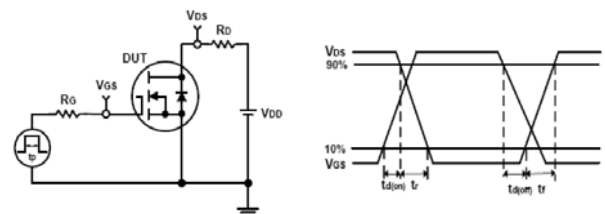
**Fig8.** Typical Gate Charge Vs.Gate-Source Voltage



**Fig9.** Normalized Maximum Transient Thermal Impedance

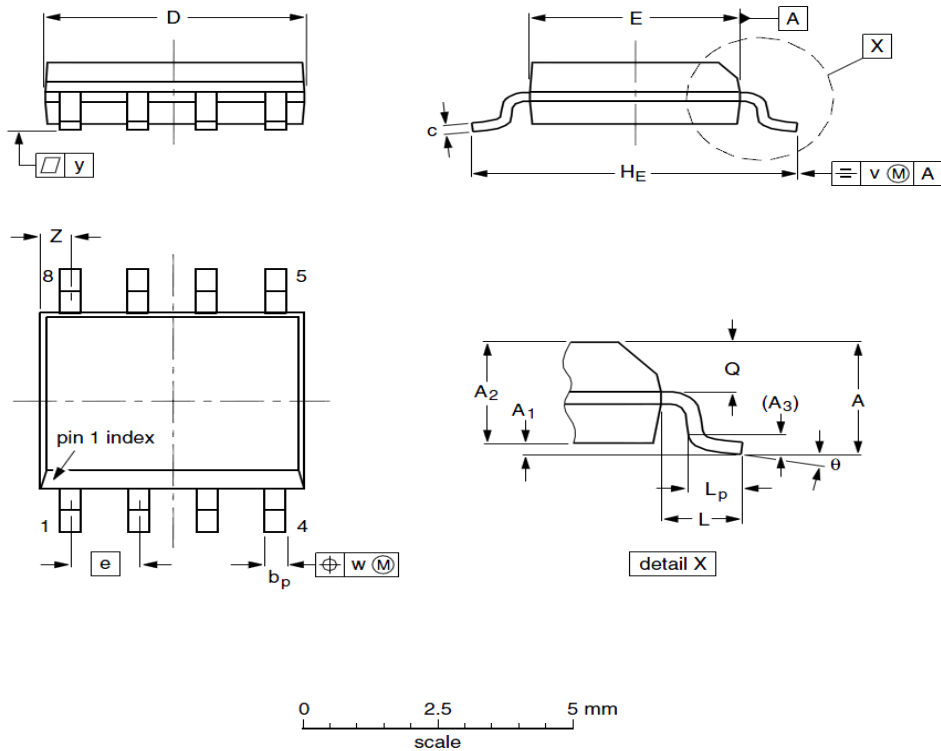


**Fig10.** Unclamped Inductive Test Circuit and Waveforms



**Fig11.** Switching Time Test Circuit and waveforms

**SOP8 Package Outline**



**DIMENSIONS ( unit : mm )**

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	--	1.75	--	A <sub>1</sub>	0.10	0.18	0.25
A <sub>2</sub>	1.25	1.35	1.45	A <sub>3</sub>	--	0.25	--
b <sub>p</sub>	0.36	0.42	0.49	c	0.19	0.22	0.25
D	4.80	4.92	5.00	E	3.80	3.90	4.00
e	--	1.27	--	H <sub>E</sub>	5.80	5.98	6.20
L	--	1.05	--	L <sub>p</sub>	0.40	0.68	1.00
Q	0.60	0.65	0.70	v	--	0.25	--
w	--	0.25	--	y	--	0.10	--
Z	0.30	0.50	0.70	$\theta$	0°		8°

**Customer Service**

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