

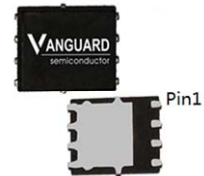
Features

- N-Channel
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5\text{ V}$
- Fast Switching
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant

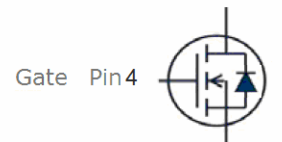
V_{DS}	50	V
$R_{DS(on),TYP}@ V_{GS}=10\text{ V}$	5.5	m Ω
$R_{DS(on),TYP}@ V_{GS}=4.5\text{ V}$	7.5	m Ω
I_D	70	A



PDFN5x6



Drain Pin5-8



Source Pin 1-3

Part ID	Package Type	Marking	Tape and reel information
VSP006N05MS	PDFN5x6	006N05M	3000pcs/reel

Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25\text{ }^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	50	V	
T_j	Maximum Junction Temperature	175	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$	
I_s	Diode Continuous Forward Current	$T_c = 25\text{ }^\circ\text{C}$ 70	A	
Mounted on Large Heat Sink				
I_D	Continuous Drain current@ $V_{GS}=10\text{ V}$	$T_c = 25\text{ }^\circ\text{C}$	70	A
		$T_c = 100\text{ }^\circ\text{C}$	55	A
I_{DM}	Pulse Drain Current Tested ①	$T_c = 25\text{ }^\circ\text{C}$	250	A
P_D	Maximum Power Dissipation	$T_c = 25\text{ }^\circ\text{C}$	83	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.8	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		52.5	$^\circ\text{C/W}$
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed ②		60	mJ

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_c = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	50	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(T _c =25°C)	V _{DS} =40V, V _{GS} =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T _c =125°C)	V _{DS} =40V, V _{GS} =0V	--	--	100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.0	2.0	3.0	V
R _{DS(ON)}	Drain-Source On-State Resistance ^③	V _{GS} =10V, I _D =25A	--	5.5	6.5	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance ^③	V _{GS} =4.5V, I _D =10A	--	7.5	10.0	mΩ
Dynamic Electrical Characteristics @ T_c = 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	--	1825	--	pF
C _{oss}	Output Capacitance		--	230	--	pF
C _{rss}	Reverse Transfer Capacitance		--	135	--	pF
Q _g	Total Gate Charge	V _{DS} =20V, I _D =10A, V _{GS} =10V	--	58	--	nC
Q _{gs}	Gate-Source Charge		--	12	--	nC
Q _{gd}	Gate-Drain Charge		--	10	--	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =30V, I _D =5A, R _G =6.8Ω, V _{GS} =10V	--	24	--	nS
t _r	Turn-on Rise Time		--	113	--	nS
t _{d(off)}	Turn-Off Delay Time		--	48	--	nS
t _f	Turn-Off Fall Time		--	105	--	nS
Source- Drain Diode Characteristics @ T_c = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	I _{SD} =25A, V _{GS} =0V	--	0.84	1.2	V
t _{rr}	Reverse Recovery Time	T _j =25°C, I _{sd} =10A, V _{GS} =0V di/dt=100A/μs	--	36	--	nS
Q _{rr}	Reverse Recovery Charge		--	41	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Limited by T_{jmax}, starting T_J = 25°C, L = 0.3mH, R_G = 25Ω, I_{AS} = 20A, V_{GS} = 10V. Part not recommended for use above this value
- ③ Pulse width ≤ 300μs; duty cycle ≤ 2%.

Typical Characteristics

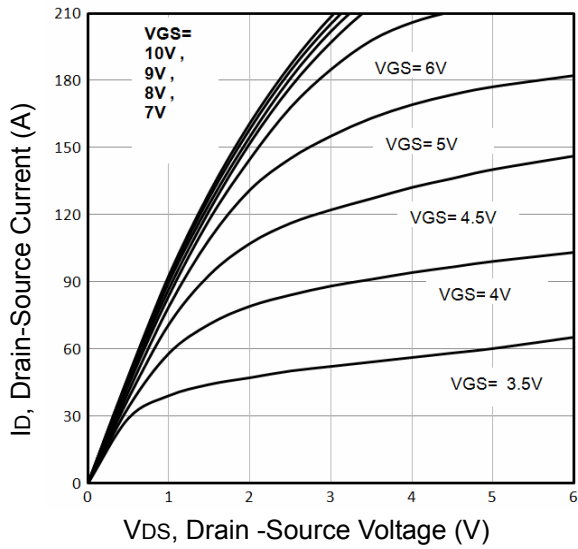


Fig1. Typical Output Characteristics

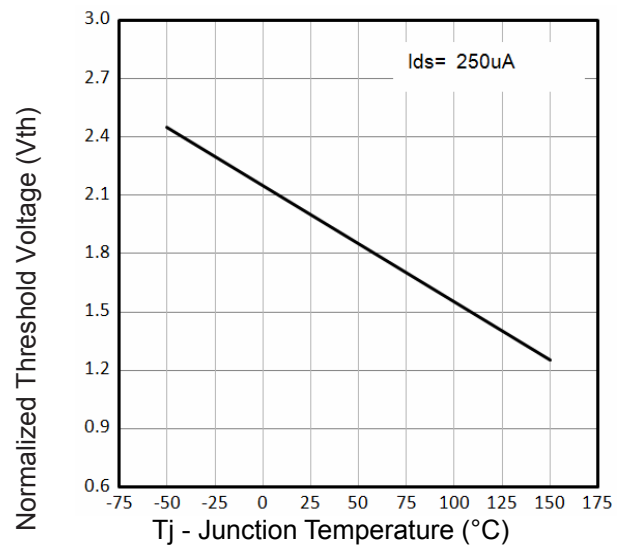


Fig2. Normalized Threshold Voltage Vs. Temperature

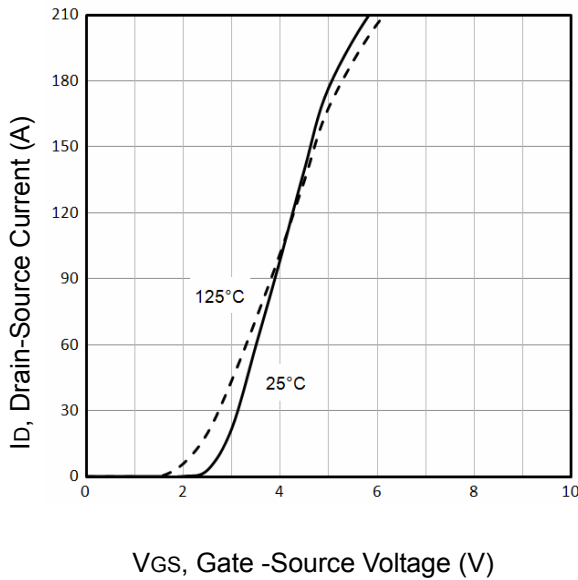


Fig3. Typical Transfer Characteristics

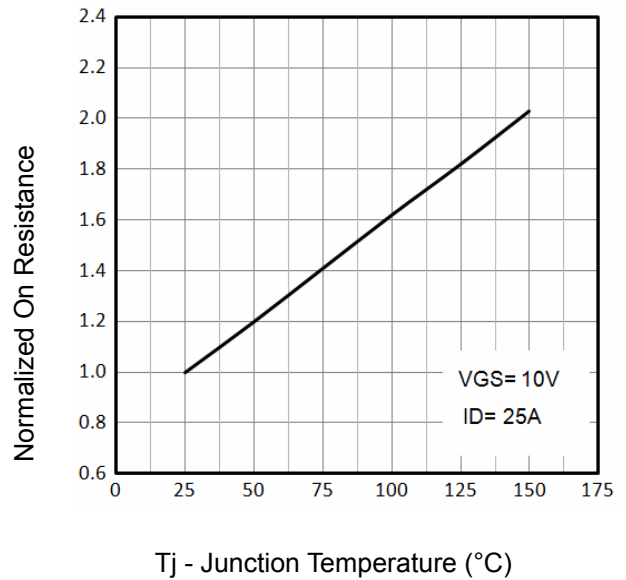


Fig4. Normalized On-Resistance Vs. Temperature

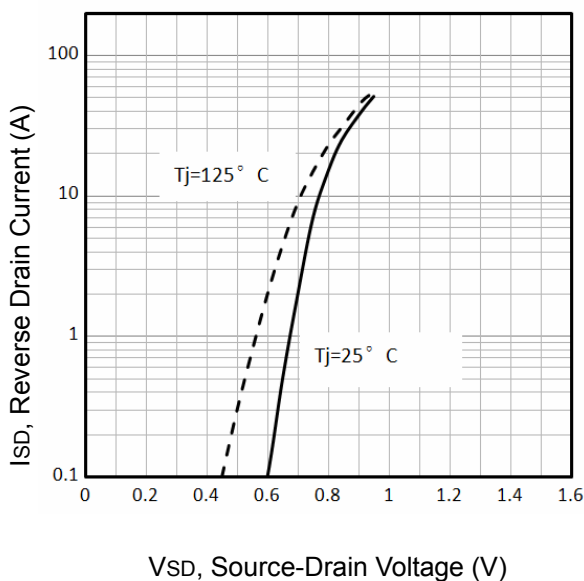


Fig5. Typical Source-Drain Diode Forward Voltage

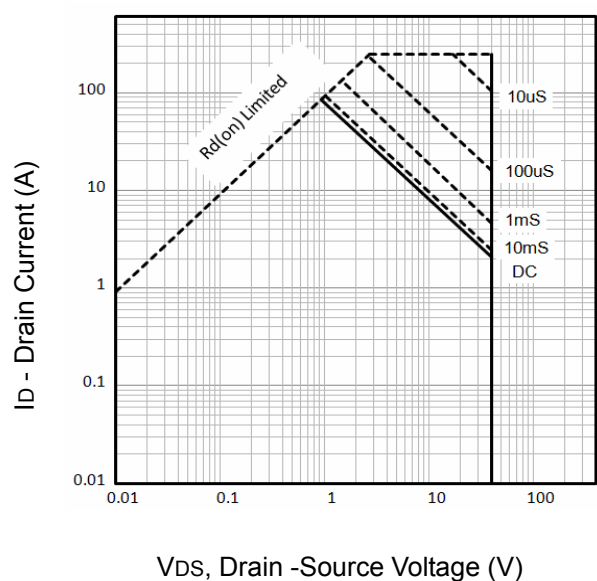


Fig6. Maximum Safe Operating Area

Typical Characteristics

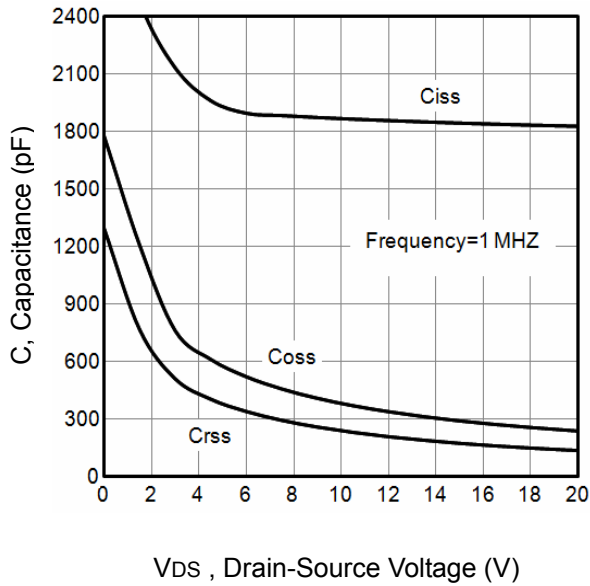


Fig7. Typical Capacitance Vs.Drain-Source Voltage

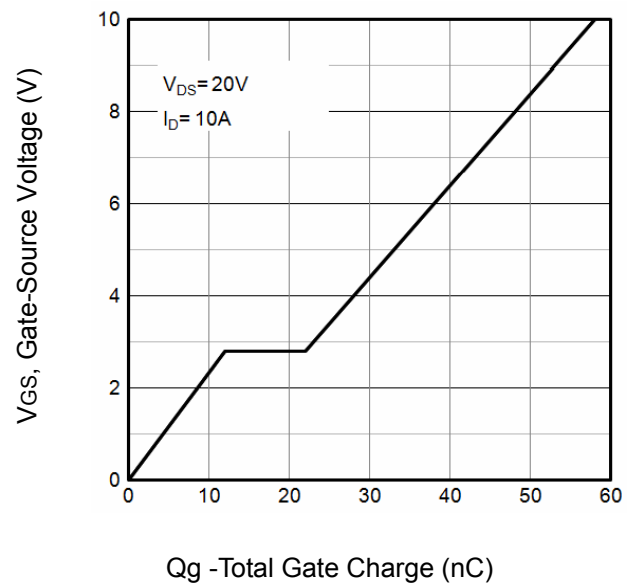


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

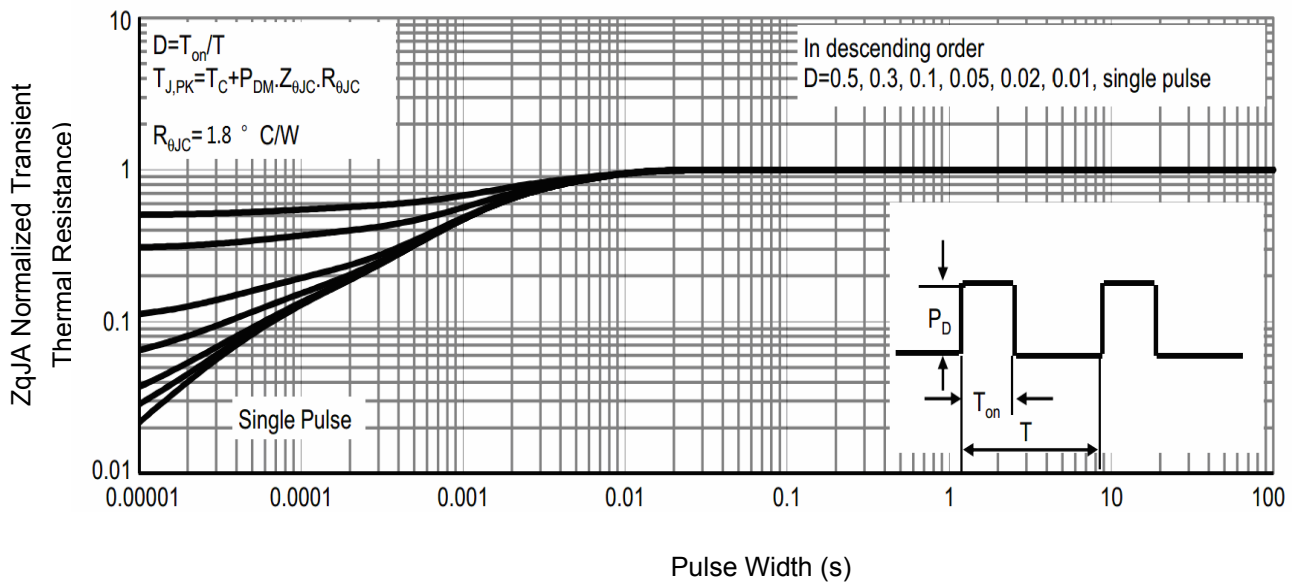


Fig9. Normalized Maximum Transient Thermal Impedance (Note H)

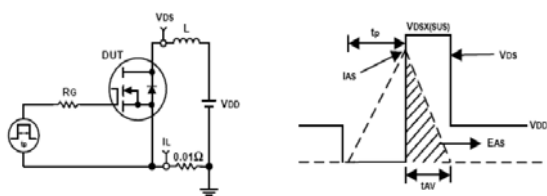


Fig10. Unclamped Inductive Test Circuit and waveforms

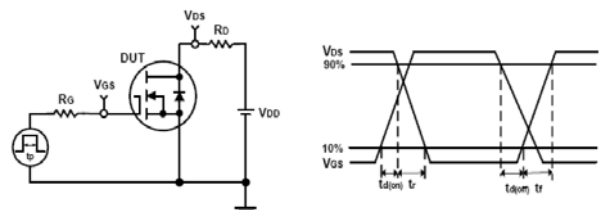
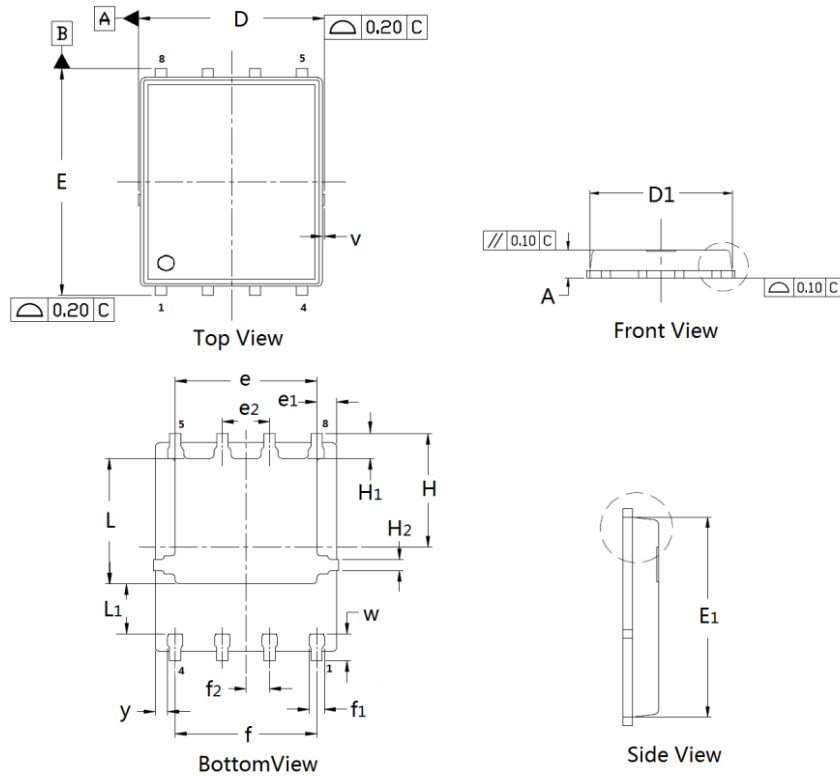


Fig11. Switching Time Test Circuit and waveforms

PDFN5×6 Package Outline Data

DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	0.90	1.02	1.10	D	4.90	4.98	5.10
D ₁	4.80	4.89	5.00	E	6.00	6.11	6.20
E ₁	5.65	5.74	5.85	e	3.72	3.80	3.92
e ₁	--	0.54	--	e ₂	--	1.27	--
f	--	3.82	--	f ₁	0.31	0.37	0.51
f ₂	--	0.64	--	H	--	3.15	--
H ₁	0.59	0.63	0.79	H ₂	0.26	0.28	0.32
L	3.38	3.45	3.58	L ₁	--	1.39	--
v	--	0.13	--	w	0.64	0.68	0.84
y	--	0.34	--		--		--

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