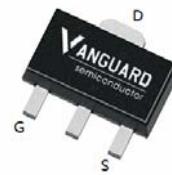


Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5\text{ V}$
- Fast Switching
- High conversion efficiency
- Pb-free lead plating; RoHS compliant

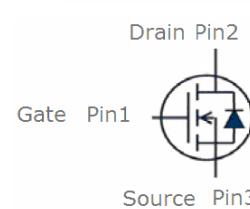
V_{DS}	60	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	30	$\text{m}\Omega$
$R_{DS(on),TYP} @ V_{GS}=4.5\text{ V}$	38	$\text{m}\Omega$
I_D	6	A

SOT89



Halogen-Free

Part ID	Package Type	Marking	Tape and reel information
VSR050N06MS	SOT89	050N06	3000pcs/reel



Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	60	V
I_s	Diode continuous forward current	$T_c=25\text{ }^\circ\text{C}$	A
I_D	Continuous drain current@ $V_{GS}=10\text{ V}$	$T_c=25\text{ }^\circ\text{C}$	A
		$T_c=100\text{ }^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_c=25\text{ }^\circ\text{C}$	A
P_d	Maximum power dissipation	$T_c=25\text{ }^\circ\text{C}$	W
V_{GS}	Gate-Source voltage	± 20	V
T_{STG}	Storage temperature range	-55 to 175	$^\circ\text{C}$
T_j	Maximum Junction Temperature	150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	20	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	100	$^\circ\text{C/W}$

Typical Electrical Characteristics

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.0	2.0	3.0	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=6\text{A}$	--	30	50	$\text{m}\Omega$
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=3\text{A}$	--	38	55	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	--	485	--	pF
C_{oss}	Output Capacitance		--	85	--	pF
C_{rss}	Reverse Transfer Capacitance		--	35	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}, I_{\text{D}}=3\text{A}, V_{\text{GS}}=10\text{V}$	--	30	--	nC
Q_{gs}	Gate-Source Charge		--	7	--	nC
Q_{gd}	Gate-Drain Charge		--	5	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}, I_{\text{D}}=1\text{A}, R_{\text{G}}=6.8\Omega, V_{\text{GS}}=10\text{V}$	--	8	--	nS
t_r	Turn-on Rise Time		--	3	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	20	--	nS
t_f	Turn-Off Fall Time		--	11	--	nS
Source- Drain Diode Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=6\text{A}, V_{\text{GS}}=0\text{V}$	--	0.83	1.3	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{sd}}=5\text{A}, V_{\text{GS}}=0\text{V}, \frac{di}{dt}=100\text{A}/\mu\text{s}$	--	40	--	nS
Q_{rr}	Reverse Recovery Charge		--	36	--	nC

NOTE:

①Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

Typical Characteristics

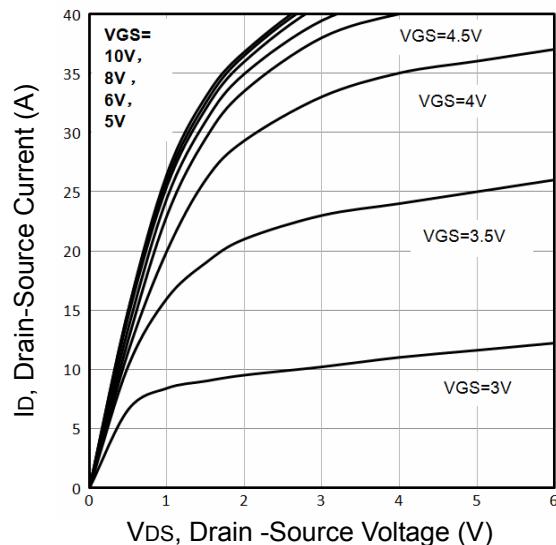


Fig1. Typical Output Characteristics

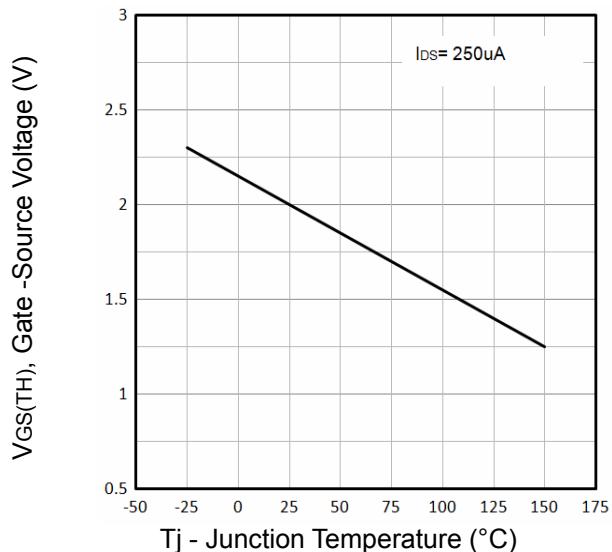


Fig2. Threshold Voltage Vs. Temperature

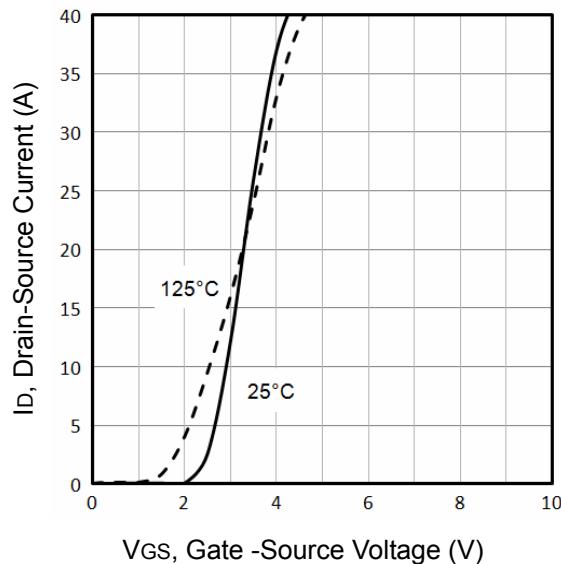


Fig3. Typical Transfer Characteristics

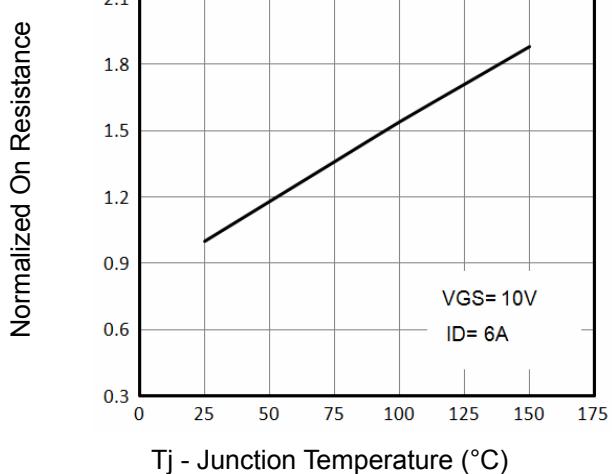


Fig4. Normalized On-Resistance Vs. Temperature

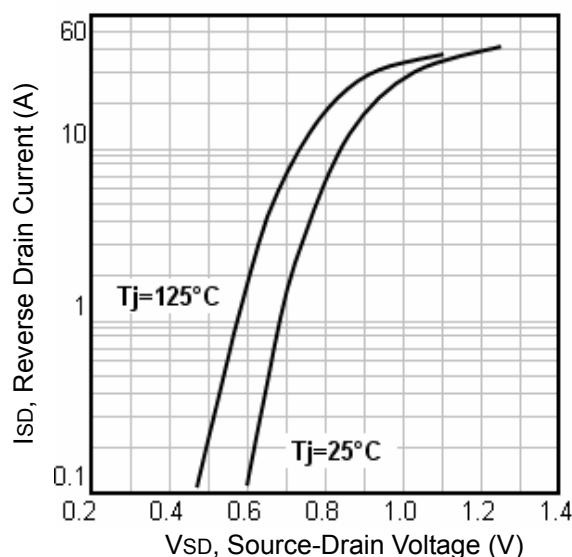


Fig5. Typical Source-Drain Diode Forward Voltage

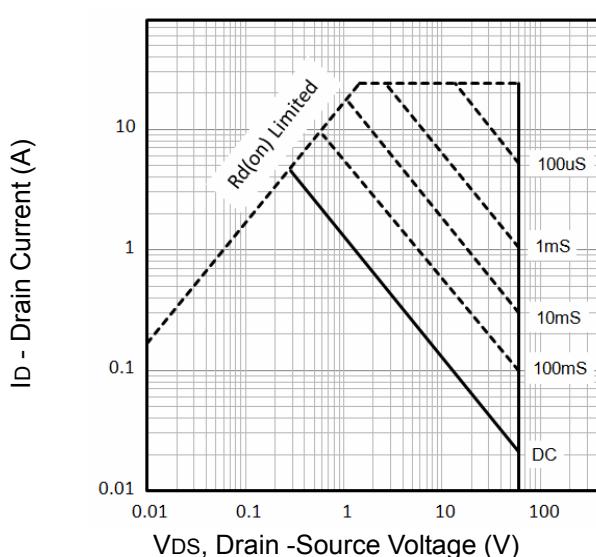


Fig6. Maximum Safe Operating Area

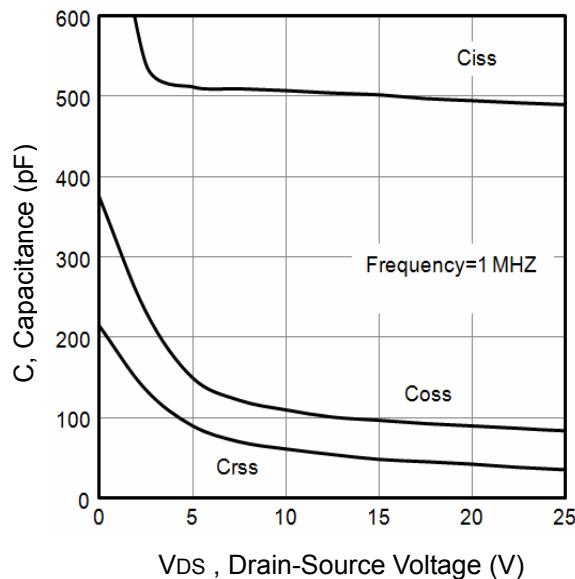


Fig7. Typical Capacitance Vs.Drain-Source Voltage

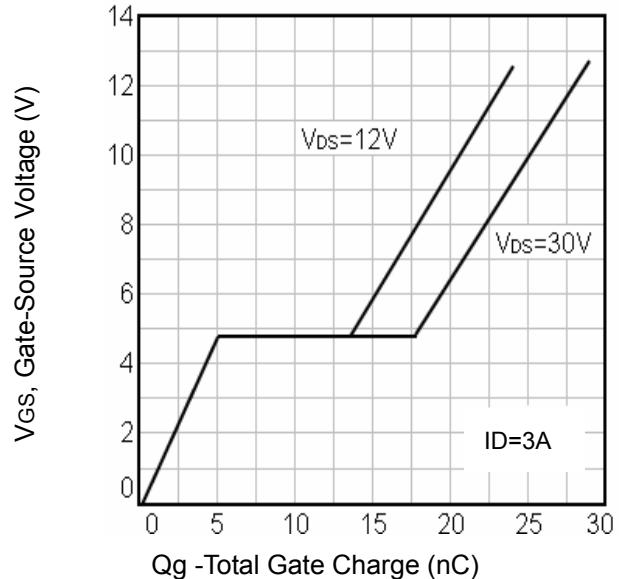


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

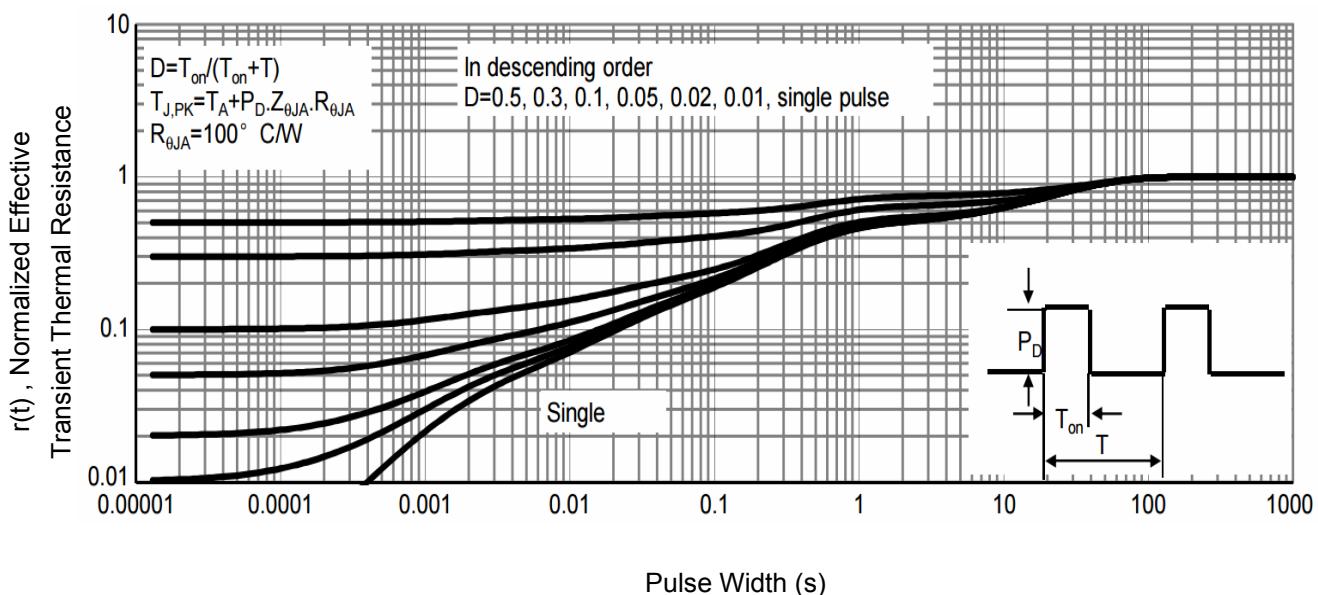


Fig9. Normalized Maximum Transient Thermal Impedance T1, Square

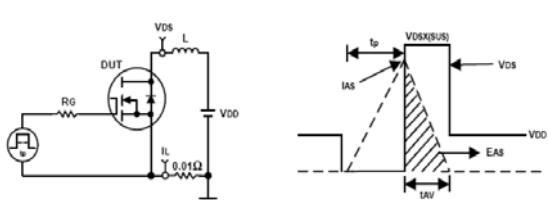


Fig10. Unclamped Inductive Test Circuit and waveforms

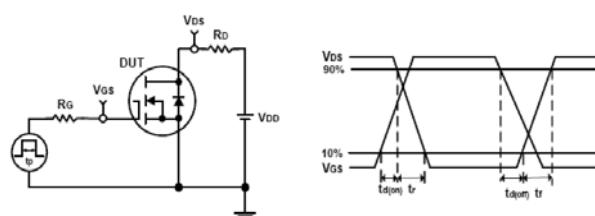
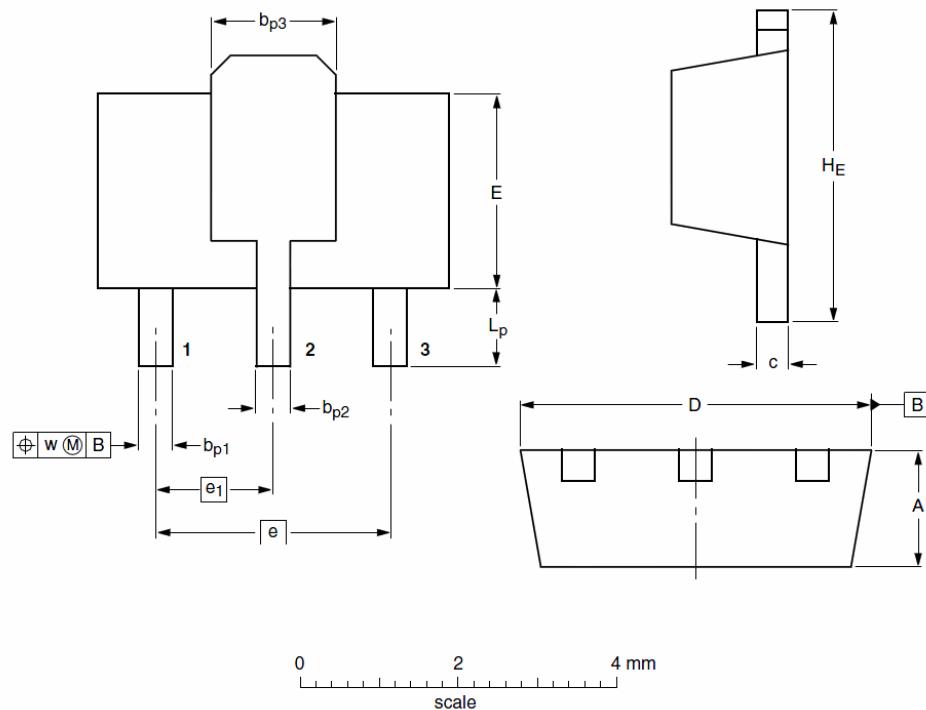


Fig11. Switching Time Test Circuit and waveforms

SOT89 Package Outline Data



DIMENSIONS (unit : mm)

Symbol	Min	Typ	Max	Symbol	Min	Typ	Max
A	1.40	1.50	1.60	b_{p1}	0.35	0.43	0.48
b_{p2}	0.40	0.47	0.53	b_{p3}	1.40	1.68	1.80
c	0.23	0.35	0.44	D	4.40	4.48	4.60
E	2.40	2.51	2.60	e	--	3.00	--
e_1	--	1.50	--	H_e	3.75	4.08	4.25
L_p	0.80	0.90	1.20	w	--	0.13	--

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