

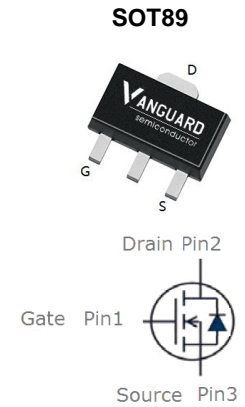
## Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Low on-resistance  $R_{DS(on)}$  @  $V_{GS}=4.5\text{ V}$
- Fast Switching
- High conversion efficiency
- Pb-free lead plating; RoHS compliant



Part ID	Package Type	Marking	Tape and reel information
VSR080N08LS	SOT-89	080N08	3000pcs/Reel

$V_{DS}$	80	V
$R_{DS(on),TYP} @ V_{GS}=10\text{ V}$	55	m $\Omega$
$R_{DS(on),TYP} @ V_{GS}=4.5\text{ V}$	57	m $\Omega$
$I_D$	15	A



## Maximum ratings, at $T_A = 25^\circ\text{C}$ , unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	80	V
$I_S$	Diode continuous forward current	$T_C = 25^\circ\text{C}$	15 A
$I_D$	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C = 25^\circ\text{C}$	15 A
		$T_C = 100^\circ\text{C}$	9 A
$I_{DM}$	Pulse drain current tested ①	$T_C = 25^\circ\text{C}$	60 A
$P_D$	Maximum power dissipation	$T_C = 25^\circ\text{C}$	20 W
$V_{GS}$	Gate-Source voltage	$\pm 16$	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	6.3	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	100	$^\circ\text{C/W}$



Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>j</sub>=25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	80	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	1	μA
	Zero Gate Voltage Drain Current(T <sub>j</sub> =125°C)	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V	--	--	100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	1.1	1.6	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance②	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	--	55	68	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance②	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	--	57	70	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1MHz	600	685	800	pF
C <sub>oss</sub>	Output Capacitance		50	140	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance		40	100	pF	
R <sub>g</sub>	Gate Resistance	f=1MHz	--	2	--	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =40V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V	--	17.3	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	3.2	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	4.8	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =40V, I <sub>D</sub> =5A, R <sub>G</sub> =3Ω, V <sub>GS</sub> =10V	--	7	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	2.9	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	23	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	2.8	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>j</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =5A, V <sub>GS</sub> =0V	--	0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>j</sub> =25°C, I <sub>sd</sub> =5A, V <sub>GS</sub> =0V di/dt=500A/μs	--	23	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge		96	--	nC	

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width ≤ 300μs; duty cycles ≤ 2%.

### Typical Characteristics

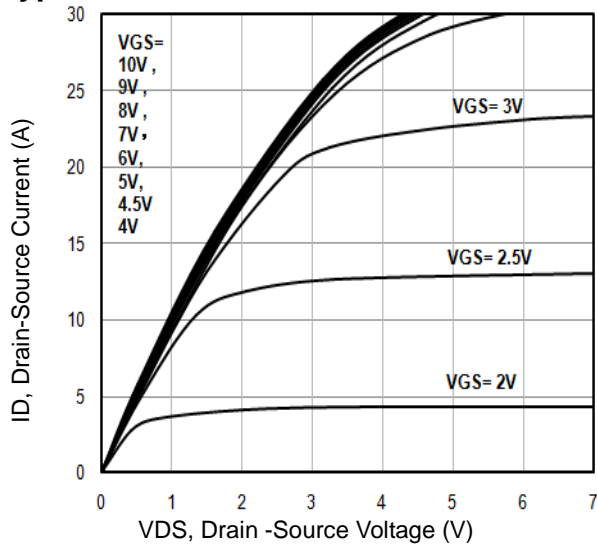


Fig1. Typical Output Characteristics

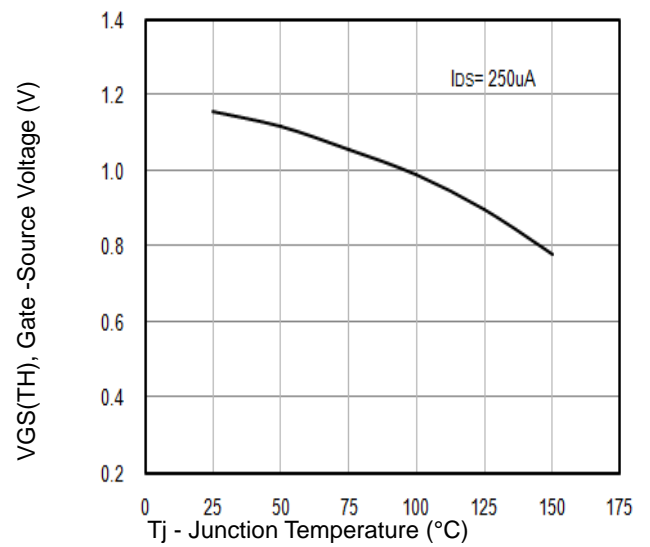


Fig2. VGS(TH) Gate-Source Voltage Vs. Tj

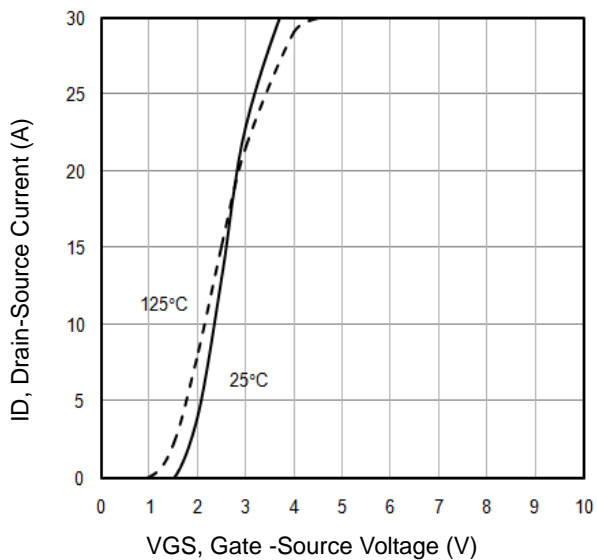


Fig3. Typical Transfer Characteristics

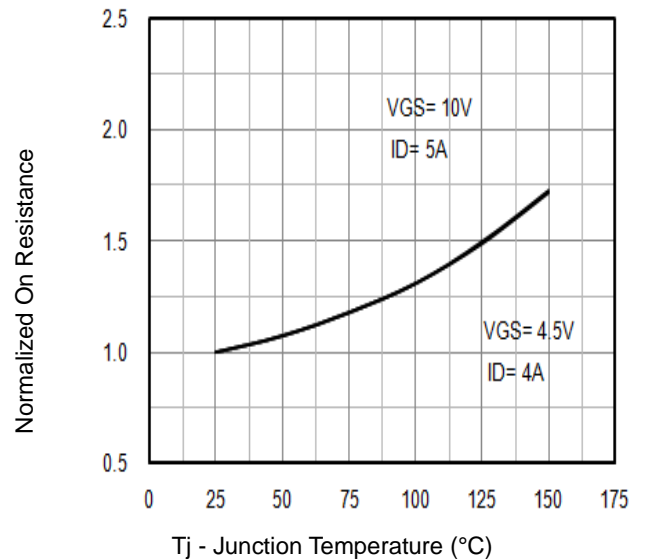


Fig4. Normalized On-Resistance Vs. Tj

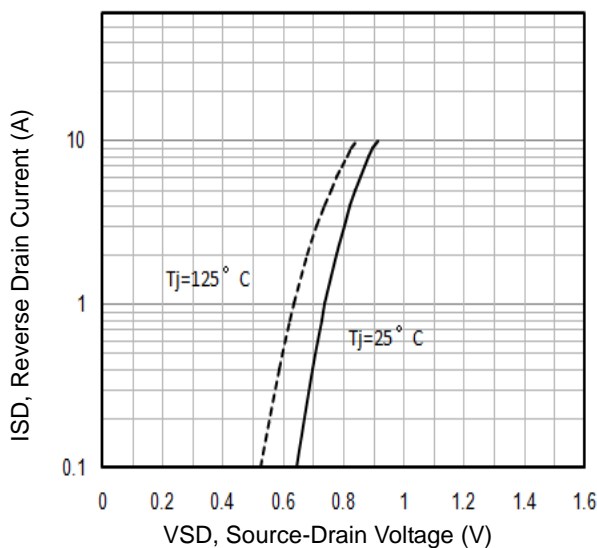


Fig5. Typical Source-Drain Diode Forward Voltage

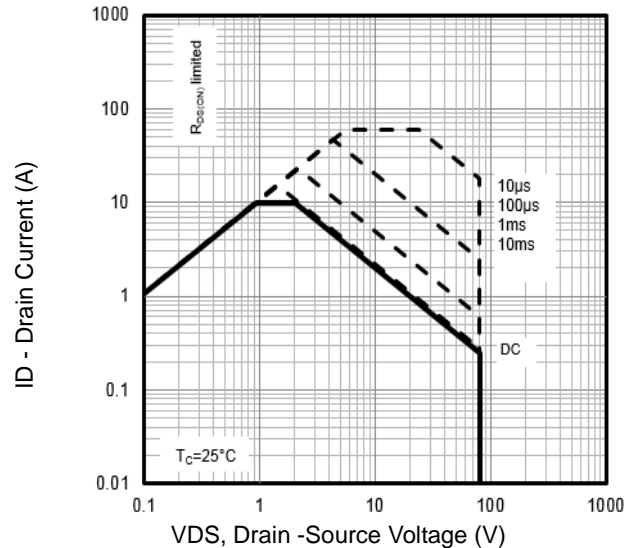


Fig6. Maximum Safe Operating Area

### Typical Characteristics

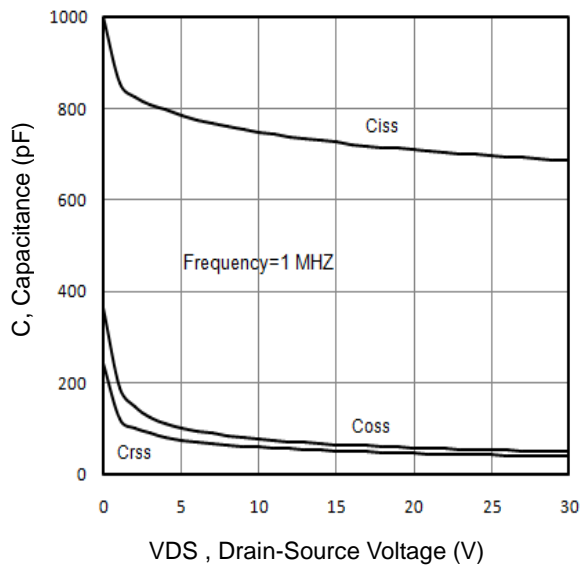


Fig7. Typical Capacitance Vs. Drain-Source Voltage

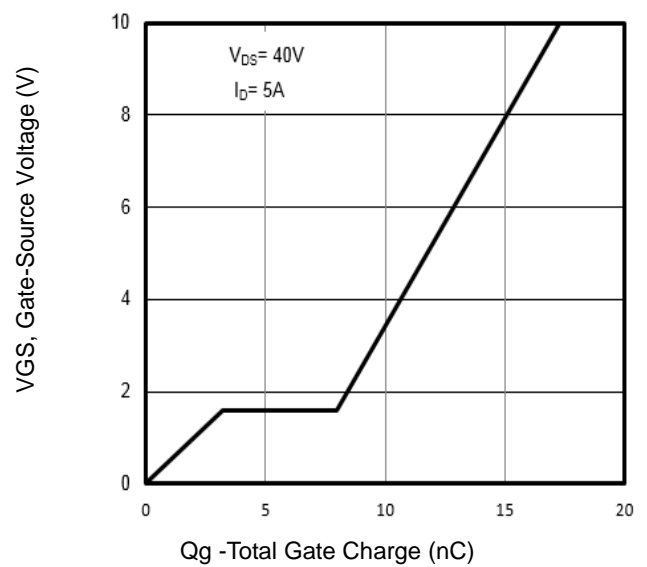


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

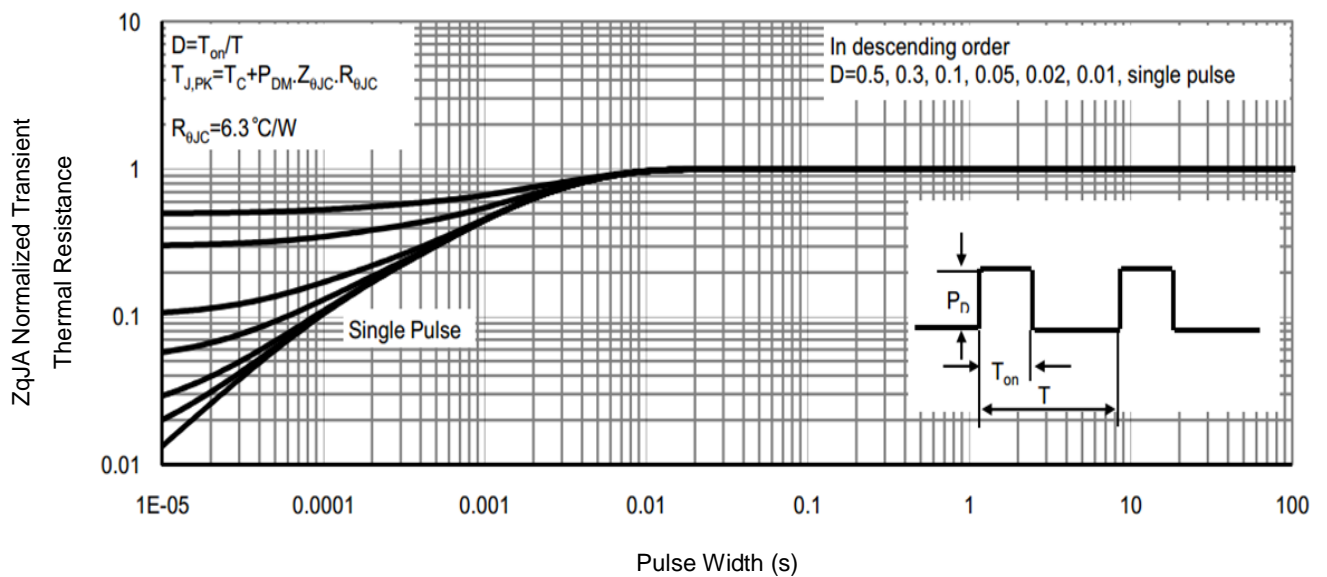


Fig9. Normalized Maximum Transient Thermal Impedance

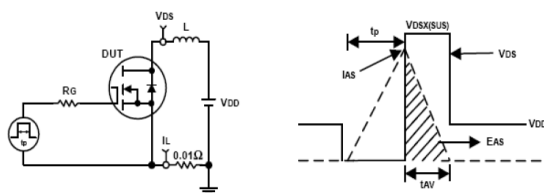


Fig10. Unclamped Inductive Test Circuit and waveforms

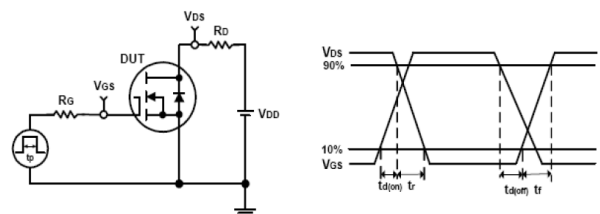
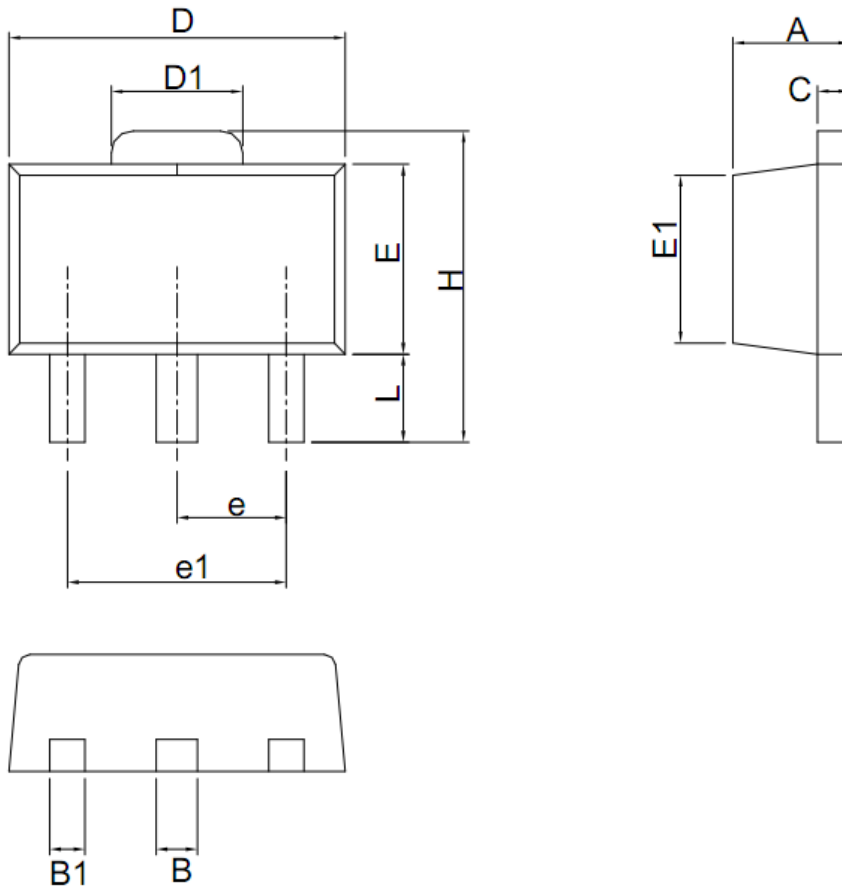


Fig11. Switching Time Test Circuit and waveforms

### SOT89 Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	1.40	1.50	1.60
B	0.44	0.51	0.56
B1	0.36	0.42	0.48
C	0.35	0.40	0.44
D	4.40	4.50	4.60
D1	1.62	1.70	1.83
E	2.40	2.50	2.60
E1	2.13		2.29
e		1.50 BSC	
e1		3.00 BSC	
H	3.94	4.20	4.25
L	0.89	1.00	1.20

Notes:

1. Refer to JEDEC TO-243 variation AA

### Customer Service

Sales and Service:

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