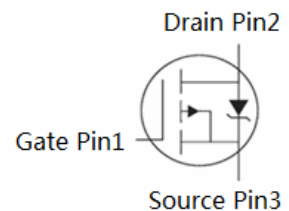
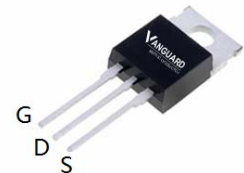


## Features

- P-Channel, -5V Logic Level Control
- Very low on-resistance  $R_{DS(on)}$  @  $V_{GS}=-4.5V$
- Fast Switching
- Enhancement mode
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant



$V_{DS}$	-30	V
$R_{DS(on),TYP} @ V_{GS}=-10V$	4.2	m $\Omega$
$R_{DS(on),TYP} @ V_{GS}=-4.5V$	6.0	m $\Omega$
$I_D$	-80	A

**TO-220AB**


Part ID	Package Type	Marking	Tape and reel information
VST004P03MS	TO-220AB	004P03M	50pcs/Tube

Maximum ratings, at  $T_j=25^\circ\text{C}$ , unless otherwise specified

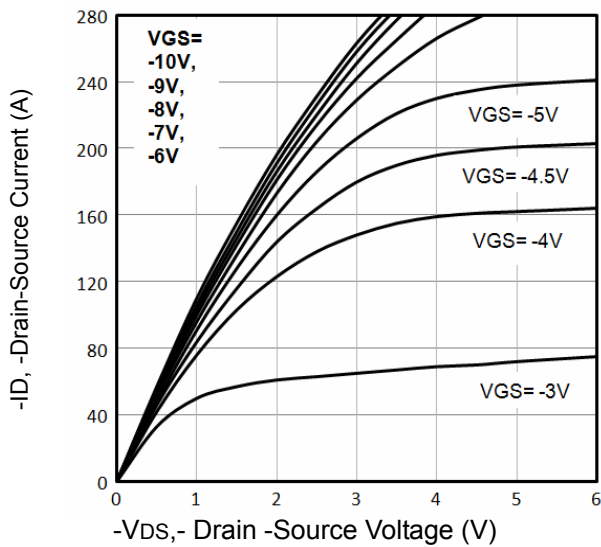
Symbol	Parameter	Rating	Unit	
<b>Common Ratings (<math>T_c=25^\circ\text{C}</math> Unless Otherwise Noted)</b>				
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	-30	V	
$T_j$	Maximum Junction Temperature	175	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range <sup>①</sup>	-55 to 175	$^\circ\text{C}$	
$I_s$	Diode Continuous Forward Current	$T_c=25^\circ\text{C}$ -80	A	
<b>Mounted on Large Heat Sink</b>				
$I_D$	Continuous Drain current @ $V_{GS}=-10V$	$T_c=25^\circ\text{C}$	-80	A
		$T_c=100^\circ\text{C}$	-51	A
$I_{DM}$	Pulse Drain Current Tested <sup>②</sup>	$T_c=25^\circ\text{C}$	-320	A
$P_D$	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	83	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.8	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		50	$^\circ\text{C/W}$
<b>Drain-Source Avalanche Ratings</b>				
EAS	Avalanche Energy, Single Pulsed <sup>③</sup>		93.7	mJ

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
<b>Static Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-30	--	--	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current(Tc=25°C)	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	--	--	-1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	--	--	-100	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	--	--	±100	nA
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1.0	-1.6	-2.5	V
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance ②	V <sub>GS</sub> =-10V, I <sub>D</sub> =-30A	--	4.2	6.0	mΩ
R <sub>DS(ON)</sub>	Drain-Source On-State Resistance ②	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	--	6.0	8.0	mΩ
<b>Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V,V <sub>GS</sub> =0V, f=1MHz	--	5960	--	pF
C <sub>oss</sub>	Output Capacitance		--	585	--	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	415	--	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A, V <sub>GS</sub> =-10V	--	85	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	18	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	28	--	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =-15V, I <sub>D</sub> =-5A, R <sub>G</sub> =6.8Ω, V <sub>GS</sub> =-10V	--	18	--	nS
t <sub>r</sub>	Turn-on Rise Time		--	22	--	nS
t <sub>d(off)</sub>	Turn-Off Delay Time		--	86	--	nS
t <sub>f</sub>	Turn-Off Fall Time		--	45	--	nS
<b>Source- Drain Diode Characteristics @ T<sub>J</sub> = 25°C (unless otherwise stated)</b>						
V <sub>SD</sub>	Forward on voltage	I <sub>SD</sub> =-30A,V <sub>GS</sub> =0V	--	-0.86	-1.3	V
t <sub>rr</sub>	Reverse Recovery Time	T <sub>J</sub> =25°C,I <sub>sd</sub> =-10A, V <sub>GS</sub> =0V	--	42	--	nS
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=-100A/μs		33		nC

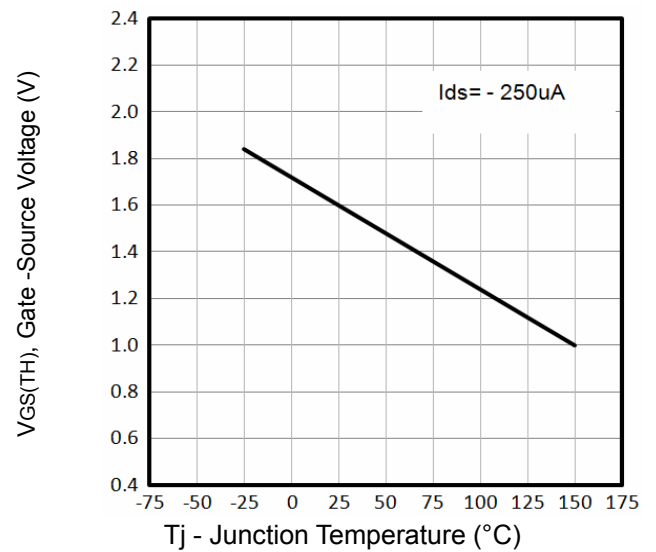
**NOTE:**

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ③ Limited by T<sub>Jmax</sub>, starting T<sub>J</sub> = 25°C, L = 0.3mH,R<sub>G</sub> = 25Ω, I<sub>AS</sub> = -25A, V<sub>GS</sub> = -10V. Part not recommended for use above this value

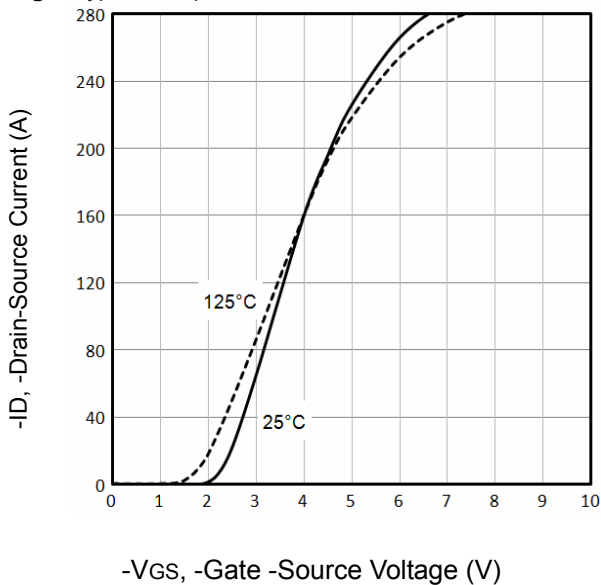
**Typical Characteristics**



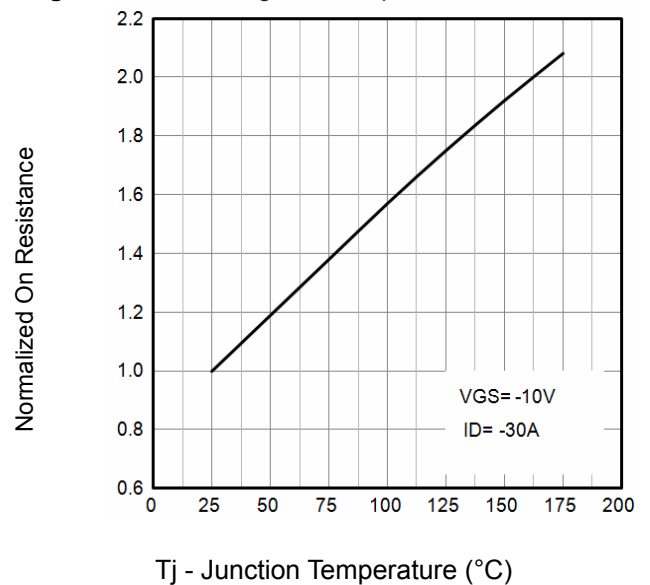
**Fig1.** Typical Output Characteristics



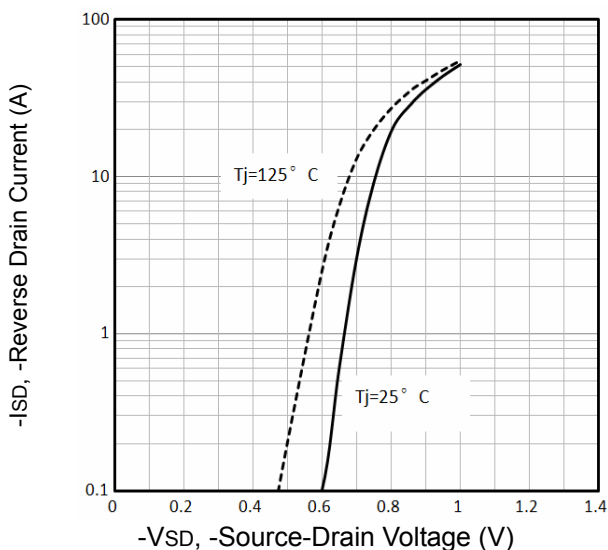
**Fig2.** Threshold Voltage Vs. Temperature



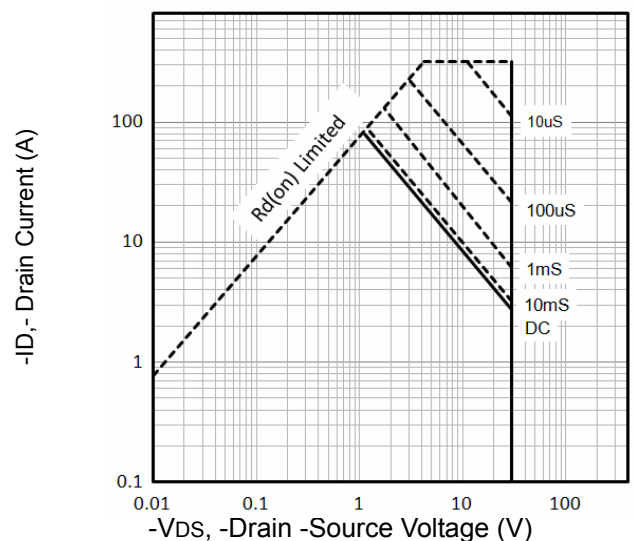
**Fig3.** Typical Transfer Characteristics



**Fig4.** Normalized On-Resistance Vs. Temperature

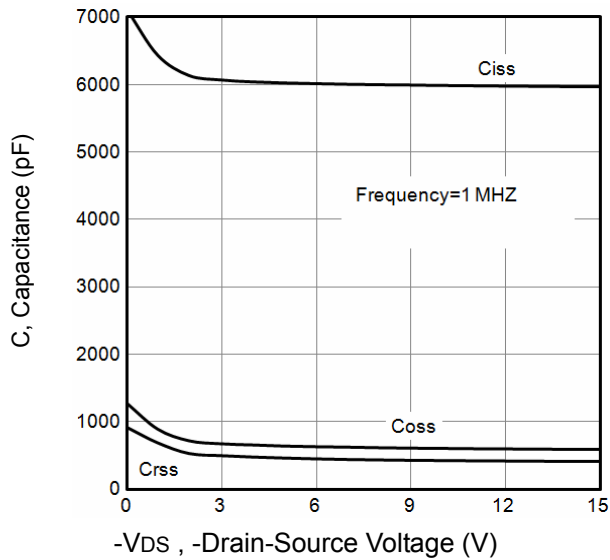


**Fig5.** Typical Source-Drain Diode Forward Voltage

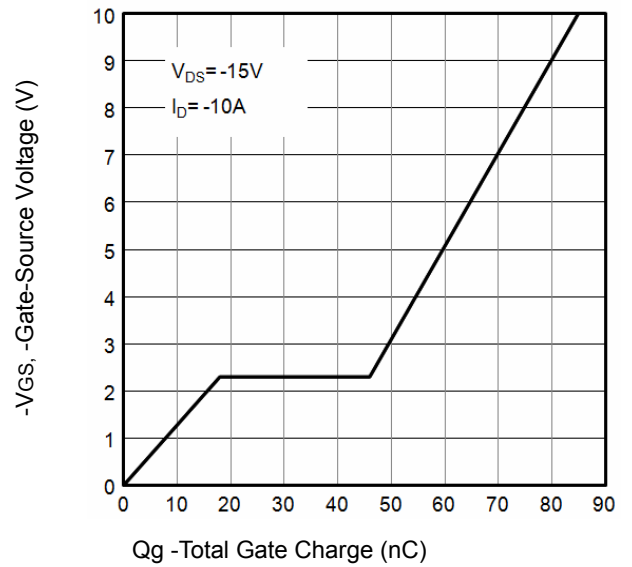


**Fig6.** Maximum Safe Operating Area

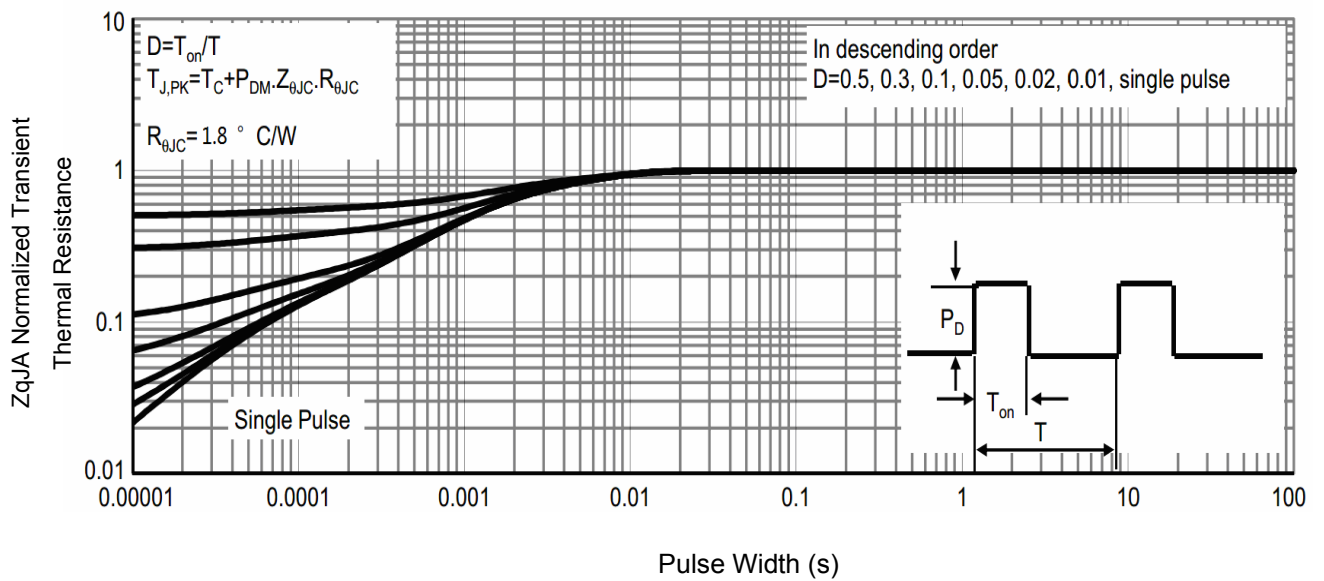
**Typical Characteristics**



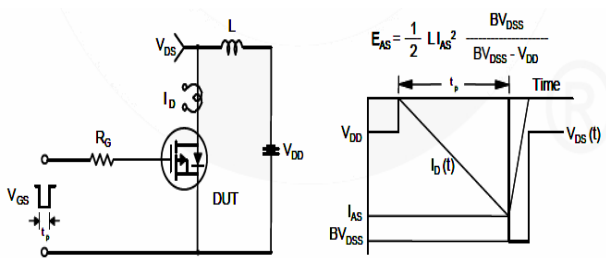
**Fig7.** Typical Capacitance Vs.Drain-Source Voltage



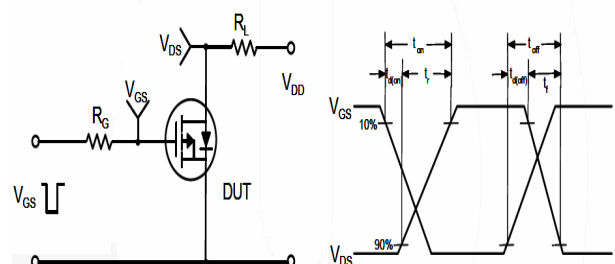
**Fig8.** Typical Gate Charge Vs.Gate-Source Voltage



**Fig9.** Normalized Maximum Transient Thermal Impedance  $T_j$ -Junction

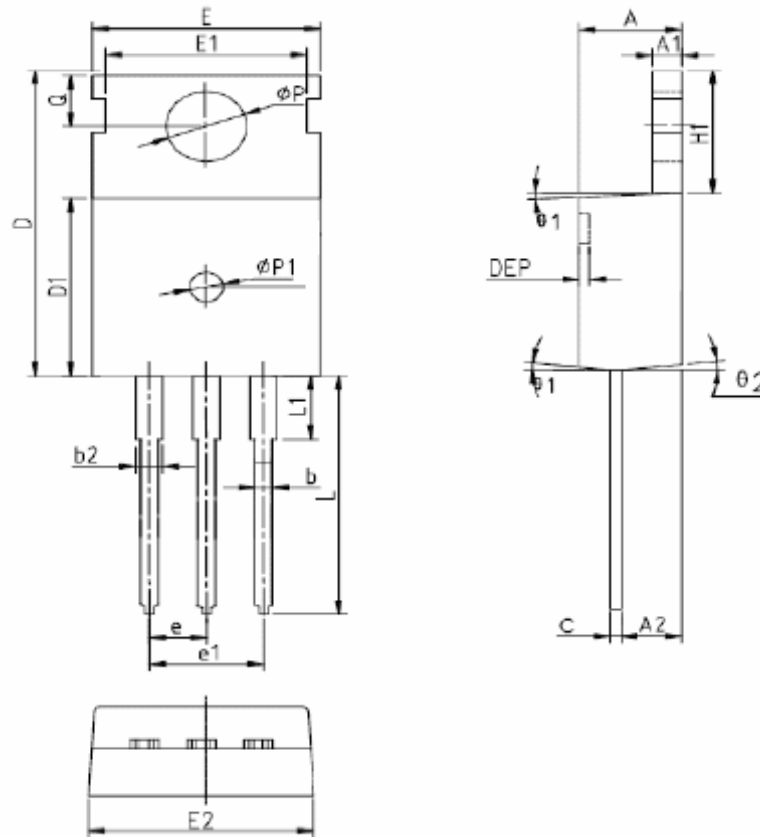


**Fig10.** Unclamped Inductive Test Circuit and Waveforms



**Fig11.** Switching Time Test Circuit and waveforms

**TO-220AB Package Outline**



SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185	$\theta p1$	1.40	1.50	1.60	0.055	0.059	0.063
A1	1.27	1.30	1.33	0.050	0.051	0.052	e	2.54BSC			0.1BSC		
A2	2.35	2.40	2.50	0.093	0.094	0.098	e1	5.08BSC			0.2BSC		
b	0.77	-	0.90	0.030	-	0.035	H1	6.40	6.50	6.60	0.252	0.256	0.260
b2	1.23	-	1.36	0.048	-	0.054	L	12.75	-	13.17	0.502	-	0.519
C	0.48	0.50	0.52	0.019	0.020	0.021	L1	-	-	3.95	-	-	0.156
D	15.40	15.60	15.80	0.606	0.614	0.622	L2	2.50REF.			0.098REF.		
D1	9.00	9.10	9.20	0.354	0.358	0.362	$\theta p$	3.57	3.60	3.63	0.141	0.142	0.143
DEP	0.05	0.10	0.20	0.002	0.004	0.008	Q	2.73	2.80	2.87	0.107	0.110	0.113
E	9.70	9.90	10.10	0.382	0.389	0.398	$\theta 1$	5°	7°	9°	5°	7°	9°
E1	-	8.70	-	-	0.343	-	$\theta 2$	1°	3°	5°	1°	3°	5°
E2	9.80	10.00	10.20	0.386	0.394	0.401							

**Customer Service**

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