

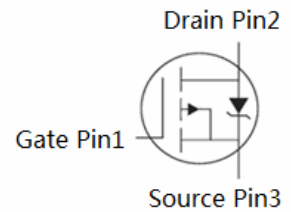
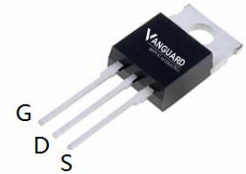
Features

- P-Channel, -5V Logic Level Control
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=-4.5V$
- Fast Switching
- Enhancement mode
- 100% Avalanche Tested
- Pb-free lead plating; RoHS compliant



Part ID	Package Type	Marking	Tape and reel information
VST007P06MS	TO-220AB	007P06M	50pcs/Tube

V_{DS}	-60	V
$R_{DS(on),TYP@ V_{GS}=-10V}$	8.5	m Ω
$R_{DS(on),TYP@ V_{GS}=-4.5V}$	10.5	m Ω
I_D	-80	A

TO-220AB


Maximum ratings, at $T_j=25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{GS}	Gate-Source Voltage	± 20	V	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	-60	V	
T_j	Maximum Junction Temperature	175	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range ^①	-55 to 175	$^\circ\text{C}$	
I_S	Diode Continuous Forward Current	$T_c=25^\circ\text{C}$ -80	A	
Mounted on Large Heat Sink				
I_D	Continuous Drain current @ $V_{GS}=-10V$	$T_c=25^\circ\text{C}$	-80	A
		$T_c=100^\circ\text{C}$	-51	A
I_{DM}	Pulse Drain Current Tested ^②	$T_c=25^\circ\text{C}$	-300	A
P_D	Maximum Power Dissipation	$T_c=25^\circ\text{C}$	115	W
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.3	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		48	$^\circ\text{C/W}$
Drain-Source Avalanche Ratings				
EAS	Avalanche Energy, Single Pulsed ^③		56	mJ

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =-250μA	-60	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current(Tc=25°C)	V _{DS} =-60V,V _{GS} =0V	--	--	-1	μA
	Zero Gate Voltage Drain Current(Tc=125°C)	V _{DS} =-60V,V _{GS} =0V	--	--	-100	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V,V _{DS} =0V	--	--	±100	nA
V _{GS(TH)}	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =-250μA	-1.0	-1.6	-2.5	V
R _{DS(ON)}	Drain-Source On-State Resistance ②	V _{GS} =-10V, I _D =-35A	--	8.5	10.0	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance ②	V _{GS} =-4.5V, I _D =-10A	--	10.5	13.0	mΩ
Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise stated)						
C _{iss}	Input Capacitance	V _{DS} =-30V,V _{GS} =0V, f=1MHz	--	6985	--	pF
C _{oss}	Output Capacitance		--	450	--	pF
C _{rss}	Reverse Transfer Capacitance		--	290	--	pF
R _g	Gate Resistance	f=1MHz		13.8		Ω
Q _g	Total Gate Charge	V _{DS} =-30V,I _D =-20A, V _{GS} =-10V	--	94	--	nC
Q _{gs}	Gate-Source Charge		--	21	--	nC
Q _{gd}	Gate-Drain Charge		--	25	--	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DD} =-30V, I _D =-5A, R _G =6.8Ω, V _{GS} =-10V	--	19	--	nS
t _r	Turn-on Rise Time		--	26	--	nS
t _{d(off)}	Turn-Off Delay Time		--	89	--	nS
t _f	Turn-Off Fall Time		--	45	--	nS
Source- Drain Diode Characteristics @ T_J = 25°C (unless otherwise stated)						
V _{SD}	Forward on voltage	I _{SD} =-35A,V _{GS} =0V	--	-0.88	-1.3	V
t _{rr}	Reverse Recovery Time	T _J =25°C,I _{sd} =-20A, V _{GS} =0V	--	35	--	nS
Q _{rr}	Reverse Recovery Charge	di/dt=-500A/μs		175		nC

NOTE:

① Repetitive rating; pulse width limited by max. junction temperature.

② Pulse width ≤ 300μs; duty cycle ≤ 2%.

③ Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH,R_G = 25Ω, I_{AS} = -15A, V_{GS} = -10V. Part not recommended for use above this value

Typical Characteristics

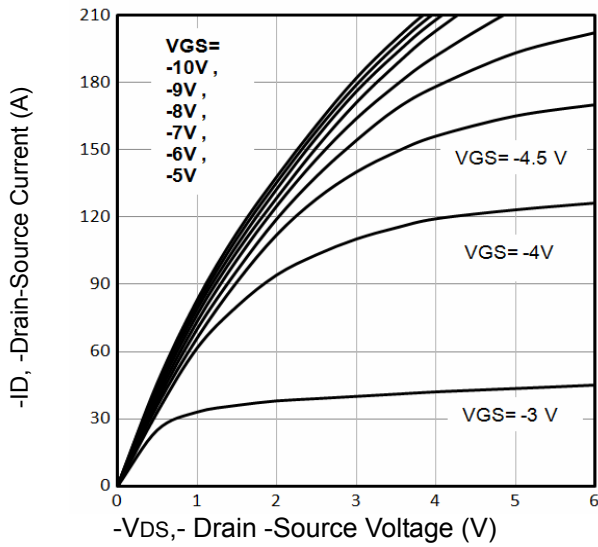


Fig1. Typical Output Characteristics

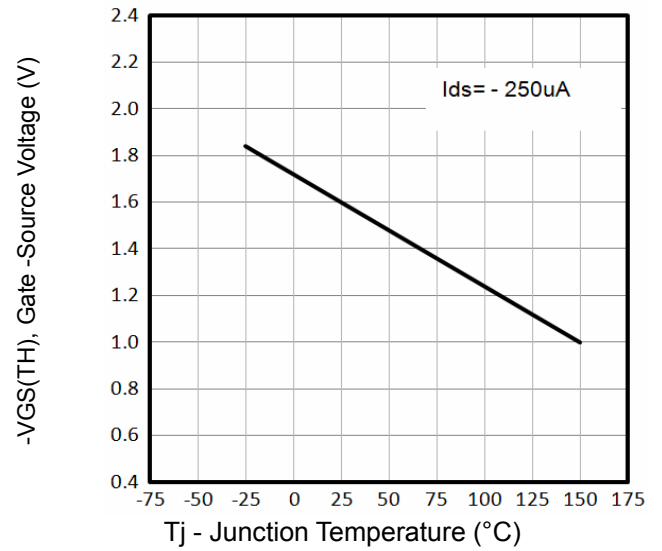


Fig2. Threshold Voltage Vs. Temperature

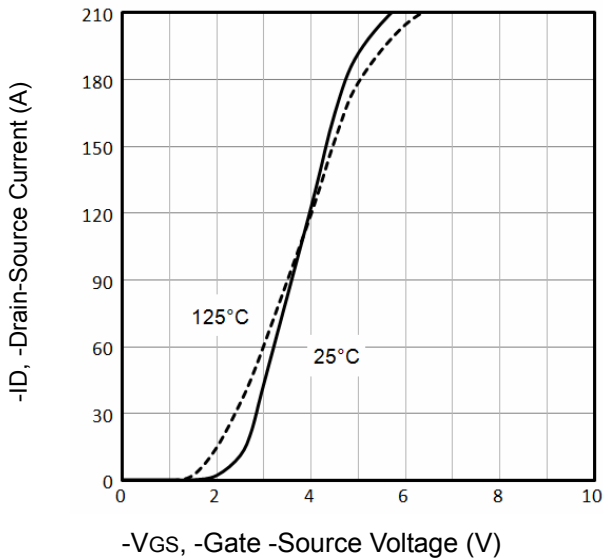


Fig3. Typical Transfer Characteristics

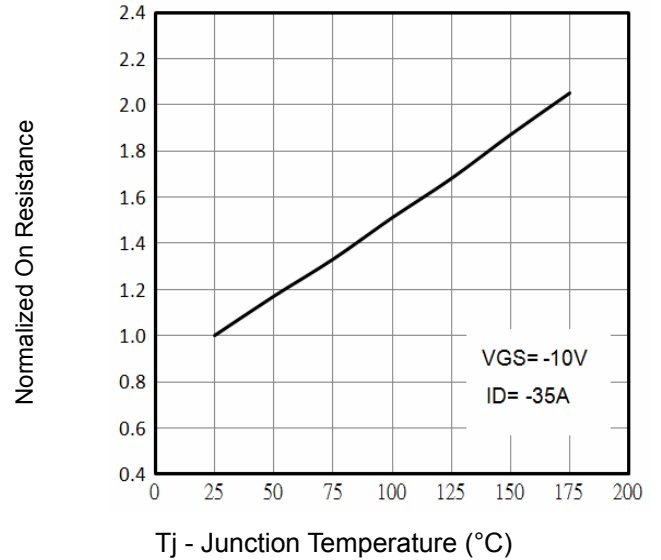


Fig4. Normalized On-Resistance Vs. Temperature

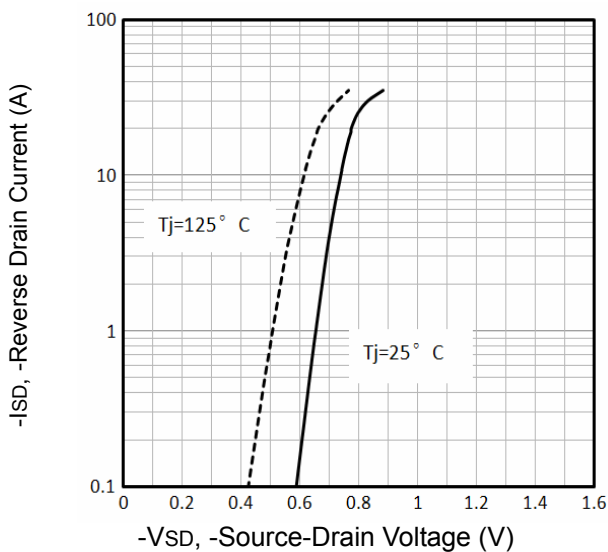


Fig5. Typical Source-Drain Diode Forward Voltage

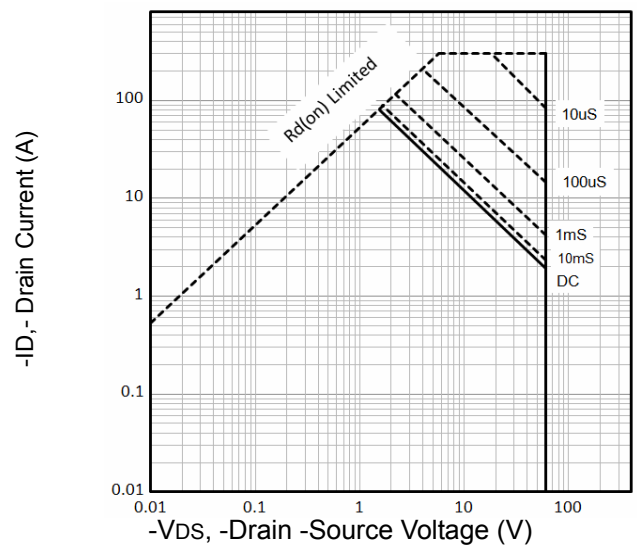


Fig6. Maximum Safe Operating Area

Typical Characteristics

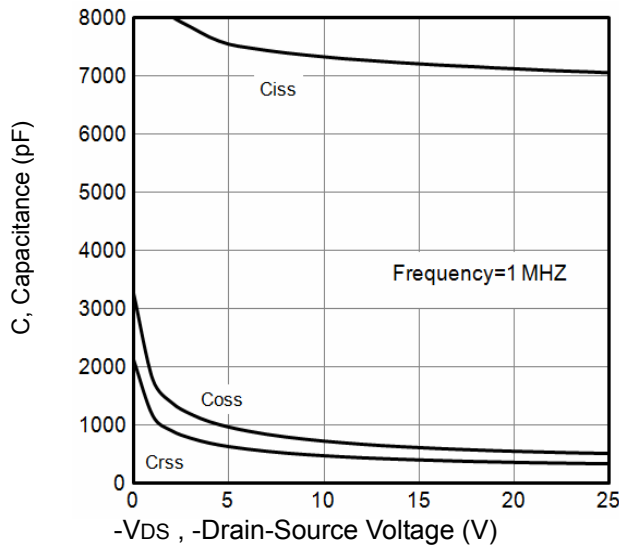


Fig7. Typical Capacitance Vs. Drain-Source Voltage

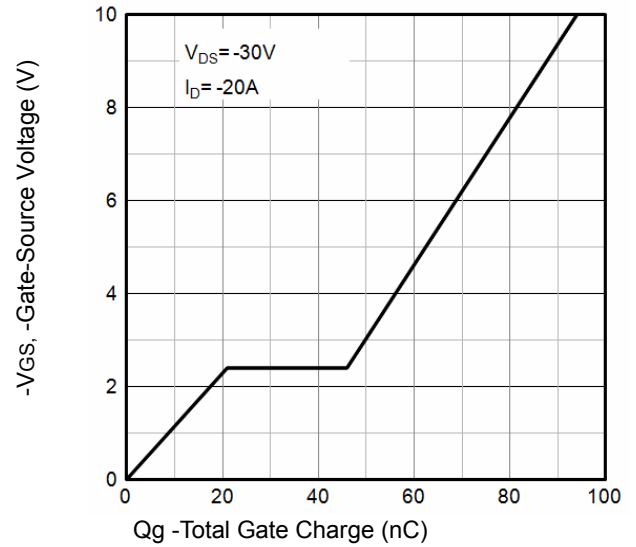


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

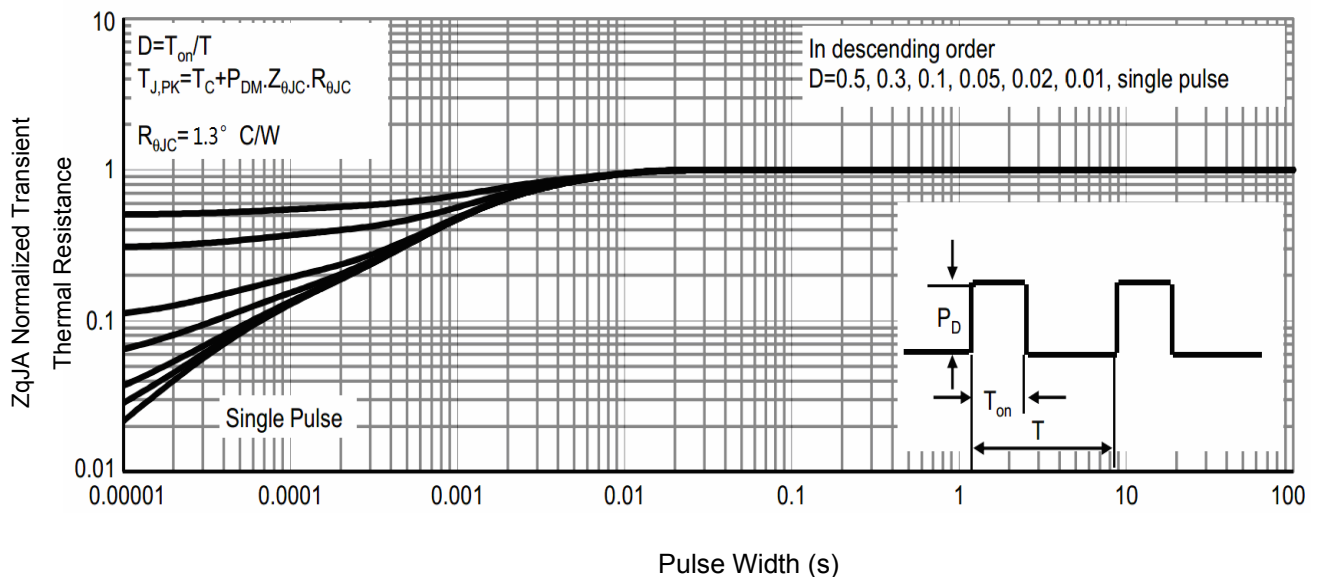


Fig9. Normalized Maximum Transient Thermal Impedance T_j -Junction

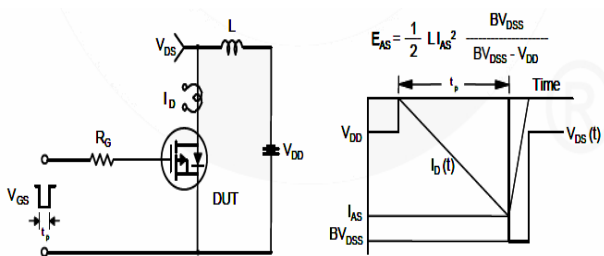


Fig10. Unclamped Inductive Test Circuit and Waveforms

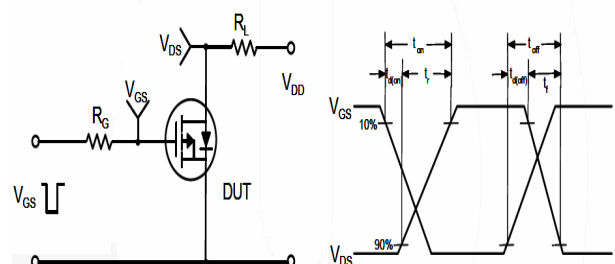
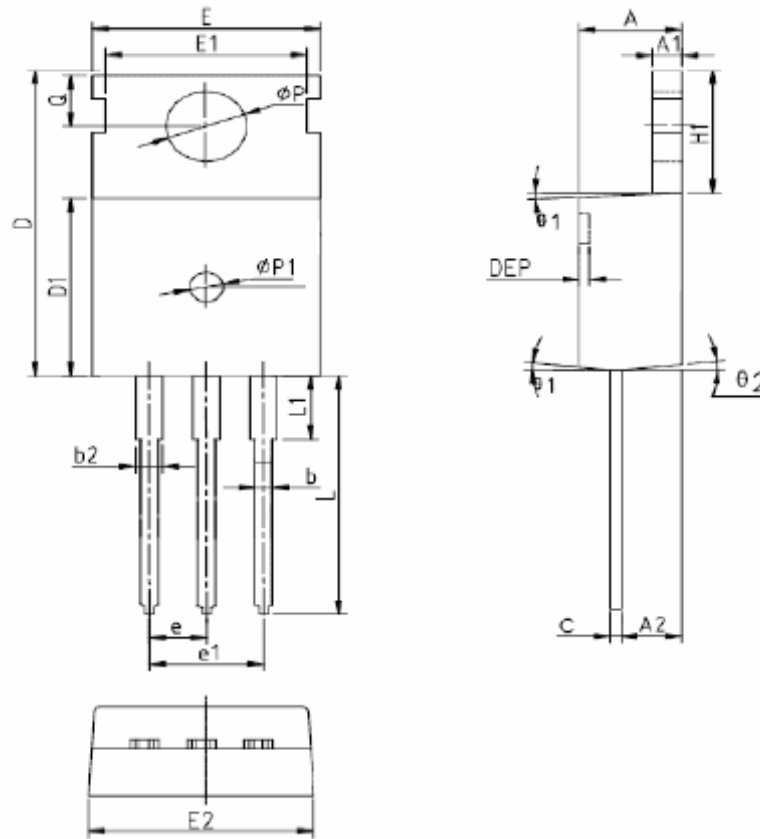


Fig11. Switching Time Test Circuit and waveforms

TO-220AB Package Outline



SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX		MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4.57	4.70	0.173	0.180	0.185	θ_{p1}	1.40	1.50	1.60	0.055	0.059	0.063
A1	1.27	1.30	1.33	0.050	0.051	0.052	e	2.54BSC			0.1BSC		
A2	2.35	2.40	2.50	0.093	0.094	0.098	e1	5.08BSC			0.2BSC		
b	0.77	-	0.90	0.030	-	0.035	H1	6.40	6.50	6.60	0.252	0.256	0.260
b2	1.23	-	1.36	0.048	-	0.054	L	12.75	-	13.17	0.502	-	0.519
C	0.48	0.50	0.52	0.019	0.020	0.021	L1	-	-	3.95	-	-	0.156
D	15.40	15.60	15.80	0.606	0.614	0.622	L2	2.50REF.			0.098REF.		
D1	9.00	9.10	9.20	0.354	0.358	0.362	θ_p	3.57	3.60	3.63	0.141	0.142	0.143
DEP	0.05	0.10	0.20	0.002	0.004	0.008	Q	2.73	2.80	2.87	0.107	0.110	0.113
E	9.70	9.90	10.10	0.382	0.389	0.398	θ_1	5°	7°	9°	5°	7°	9°
E1	-	8.70	-	-	0.343	-	θ_2	1°	3°	5°	1°	3°	5°
E2	9.80	10.00	10.20	0.386	0.394	0.401							

Customer Service

Sales and Service:

sales@vgsemi.com

Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: www.vgsemi.com