

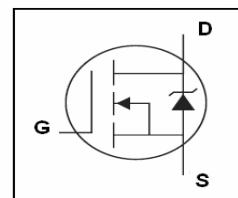
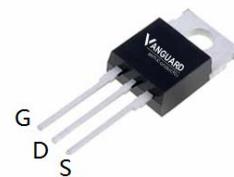
Features

- N-Channel
- Very low on-resistance
- 100% Avalanche Tested
- Fast Switching
- Repetitive Avalanche Allowed up to T_{jmax}
- Pb-free lead plating; RoHS compliant



V_{DS}	70	V
$R_{DS(on),TYP}$ @ $V_{GS}=10$ V	7.5	mΩ
I_D	80	A

TO-220AB



Part ID	Package Type	Marking	Tape and reel information
VST008N07HS	TO-220AB	008N07H	50pcs/Tube

Maximum ratings, at $T_j=25$ °C, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	70	V
I_s	Diode continuous forward current	$T_c=25^\circ\text{C}$	A
I_D	Continuous drain current@ $V_{GS}=10$ V	$T_c=25^\circ\text{C}$	A
		$T_c=70^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_c=25^\circ\text{C}$	A
EAS	Avalanche energy, single pulsed ②	$I_D=30$ A	mJ
IAS	Avalanche energy, single pulsed ②	40	A
P_D	Maximum power dissipation	$T_c=25^\circ\text{C}$	W
V_{GS}	Gate-Source voltage	±20	V
$T_{STG} T_J$	Storage and operating temperature range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance-Junction to Case	0.9	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	62.5	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	70	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_c=25^\circ\text{C}$)	$V_{\text{DS}}=64\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_c=125^\circ\text{C}$)	$V_{\text{DS}}=64\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS(TH)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	2	3	4	V
$R_{\text{DS(ON)}}$	Drain-Source On-State Resistance ⁽¹⁾	$V_{\text{GS}}=10\text{V}$, $I_D=40\text{A}$	--	7.5	8.5	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	3260	--	pF
C_{oss}	Output Capacitance		--	400	--	pF
C_{rss}	Reverse Transfer Capacitance		--	220	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=48\text{V}$, $I_D=30\text{A}$, $V_{\text{GS}}=10\text{V}$	--	70	--	nC
Q_{gs}	Gate-Source Charge		--	13	--	nC
Q_{gd}	Gate-Drain Charge		--	25	--	nC
Switching Characteristics						
$t_{\text{d(on)}}$	Turn-on Delay Time	$V_{\text{DD}}=30\text{V}$, $I_D=1\text{A}$, $R_G=6.8\Omega$, $V_{\text{GS}}=10\text{V}$	--	15	--	nS
t_r	Turn-on Rise Time		--	14	--	nS
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	28	--	nS
t_f	Turn-Off Fall Time		--	45	--	nS
Source- Drain Diode Characteristics@ $T_J = 25^\circ\text{C}$ (unless otherwise stated)						
I_{SD}	Source-drain current(Body Diode)	$T_c=25^\circ\text{C}$	--	--	80	A
I_{SDM}	Pulsed Source-drain current(Body Diode) ⁽¹⁾		--	--	330	A
V_{SD}	Forward on voltage	$I_{\text{SD}}=40\text{A}$, $V_{\text{GS}}=0\text{V}$	--	0.89	1.3	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}$, $I_{\text{sd}}=40\text{A}$, $V_{\text{GS}}=0\text{V}$ $di/dt=100\text{A}/\mu\text{s}$	--	52	--	nS
Q_{rr}	Reverse Recovery Charge		--	96	--	nC

NOTE:

(1) Pulse width $\leq 300\mu\text{s}$; duty cycles $\leq 2\%$.

(2) Limited by $T_{J\text{max}}$, starting $T_J = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 30\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value

Typical Characteristics

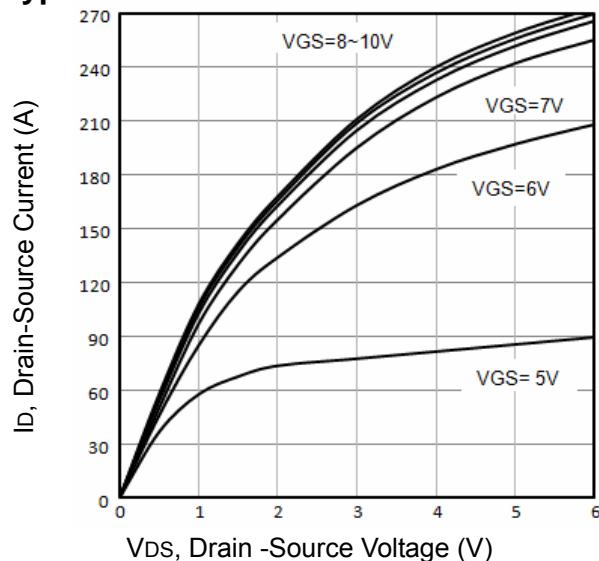


Fig1. Typical Output Characteristics

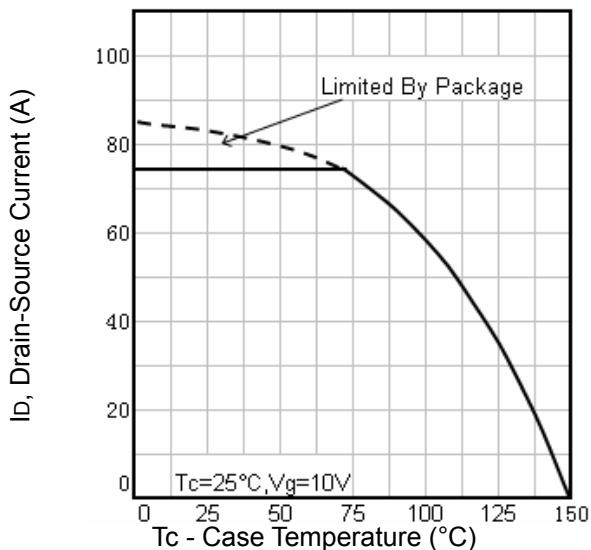


Fig2. Maximum Drain Current Vs. Case Temperature

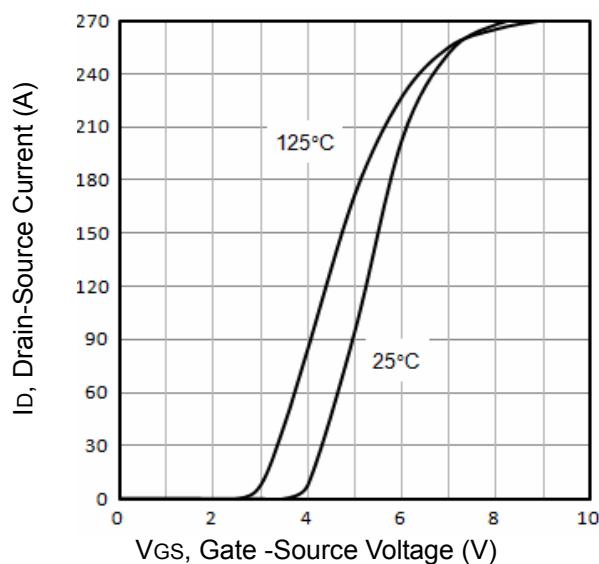


Fig3. Typical Transfer Characteristics

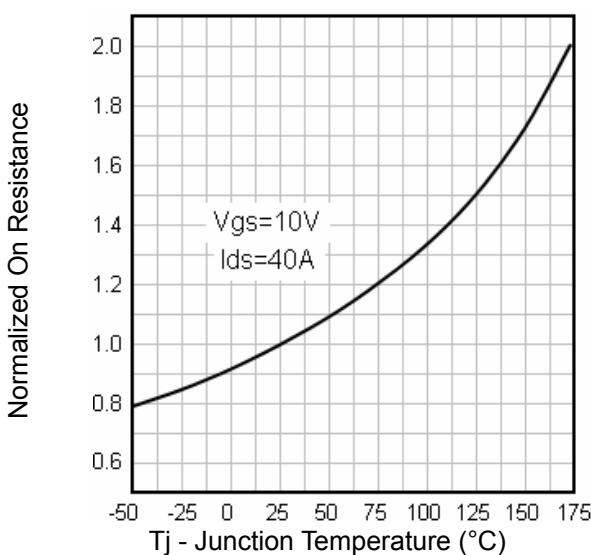


Fig4. Normalized On-Resistance Vs. Temperature

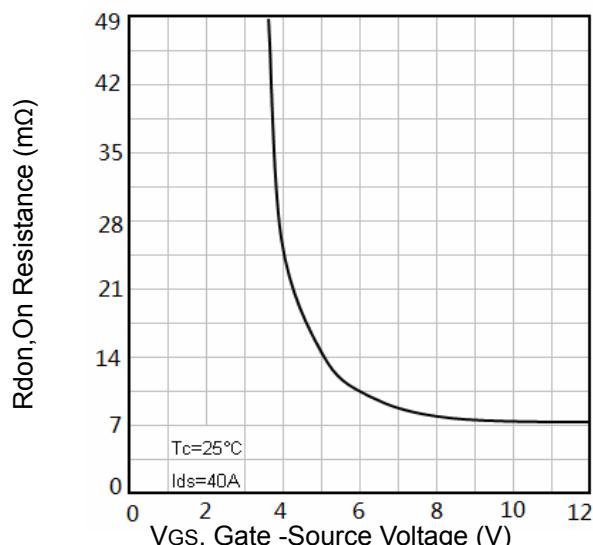


Fig5. Typical On-Resistance Vs. Gate-Source Voltage
Copyright © Vanguard Semiconductor Co., Ltd.
Rev. A- Nov.16th, 2015

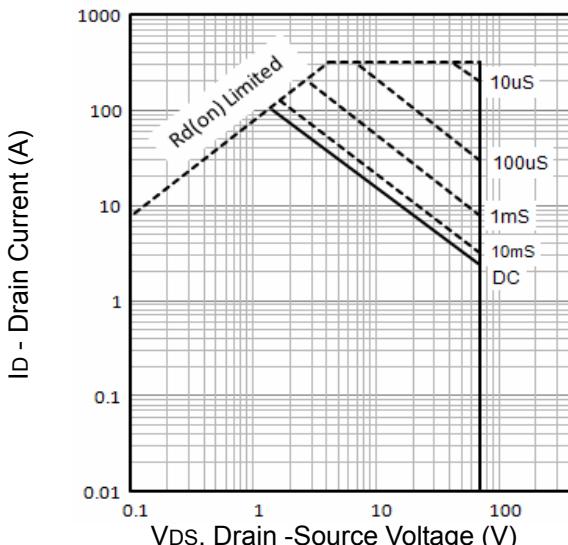


Fig6. Maximum Safe Operating Area

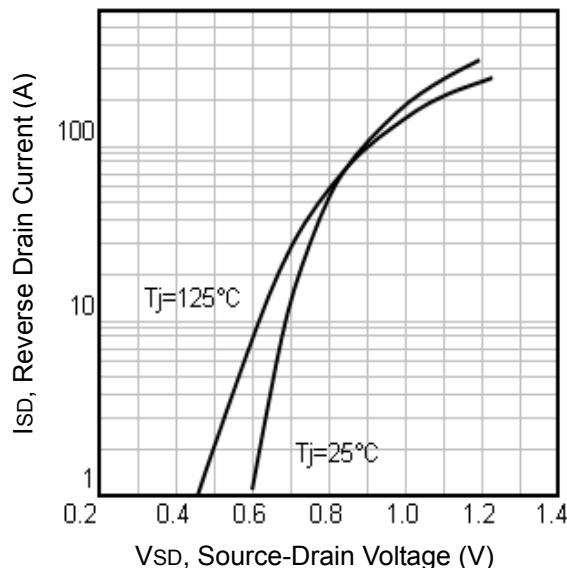


Fig7. Typical Source-Drain Diode Forward Voltage

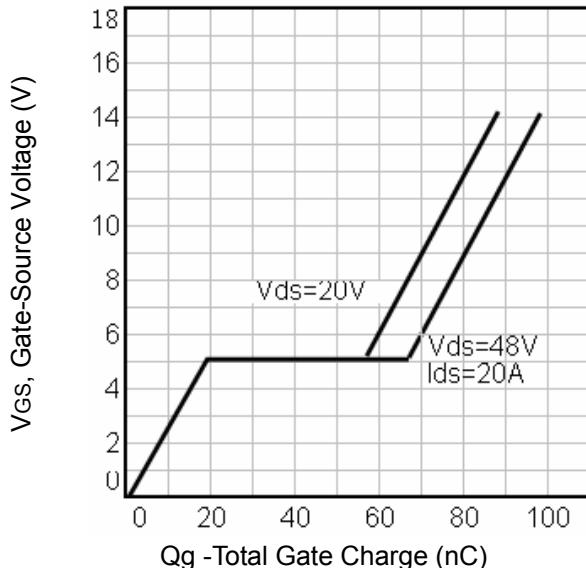


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

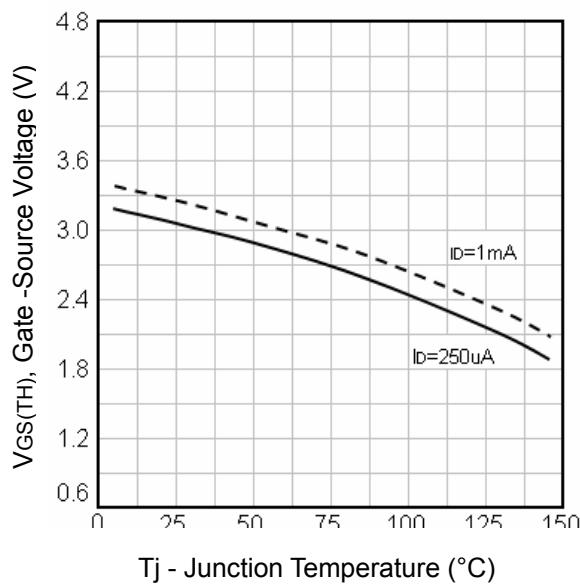


Fig9. Threshold Voltage Vs. Temperature

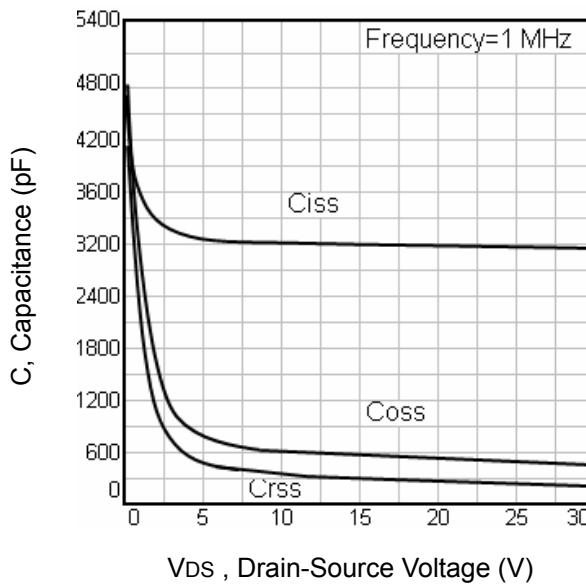


Fig10. Typical Capacitance Vs.Drain-Source Voltage

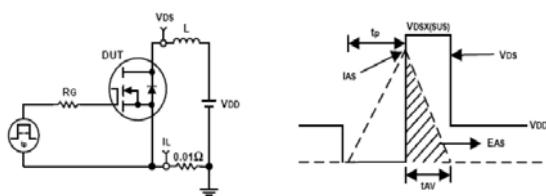


Fig11. Unclamped Inductive Test Circuit and waveforms

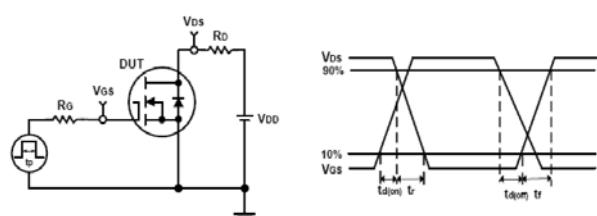
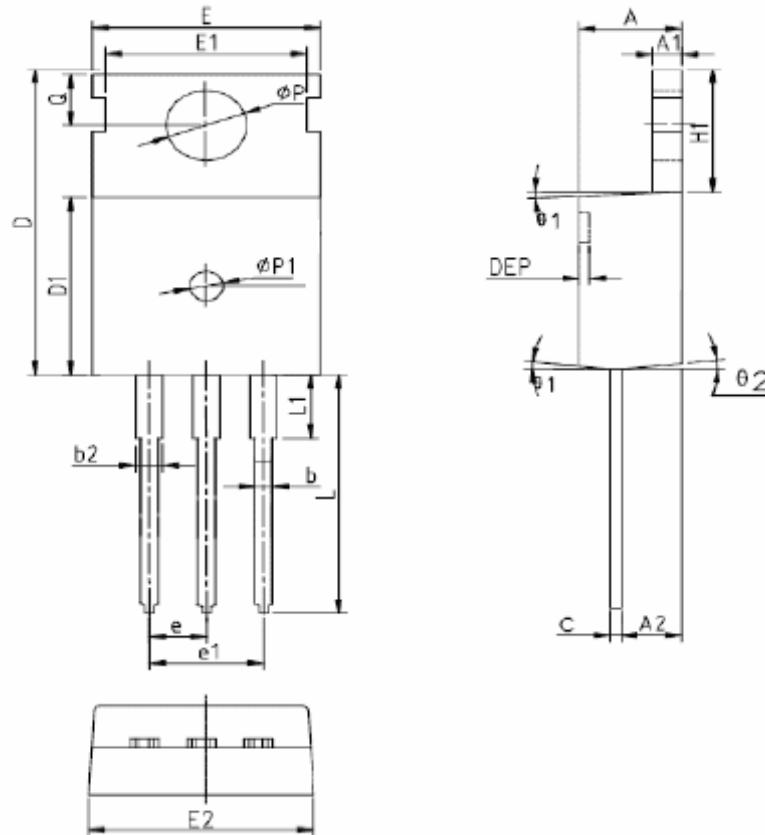


Fig12. Switching Time Test Circuit and waveforms

TO-220AB Package Outline



SYMBOL	MM			INCH			SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX	θp1	1.40	1.50	1.60	0.055	0.059	0.063
A	4.40	4.57	4.70	0.173	0.180	0.185	θp1	1.40	1.50	1.60	0.055	0.059	0.063
A1	1.27	1.30	1.33	0.050	0.051	0.052	e	2.54BSC			0.1BSC		
A2	2.35	2.40	2.50	0.093	0.094	0.098	ε1	5.08BSC			0.2BSC		
b	0.77	-	0.90	0.030	-	0.035	H1	6.40	6.50	6.60	0.252	0.256	0.260
b2	1.23	-	1.36	0.048	-	0.054	L	12.75	-	13.17	0.502	-	0.519
C	0.48	0.50	0.52	0.019	0.020	0.021	L1	-	-	3.95	-	-	0.156
D	15.40	15.60	15.80	0.606	0.614	0.622	L2	2.50REF.			0.098REF.		
D1	9.00	9.10	9.20	0.354	0.358	0.362	θp	3.57	3.60	3.63	0.141	0.142	0.143
DEP	0.05	0.10	0.20	0.002	0.004	0.008	Q	2.73	2.80	2.87	0.107	0.110	0.113
E	9.70	9.90	10.10	0.382	0.389	0.398	θ1	5°	7°	9°	5°	7°	9°
E1	-	8.70	-	-	0.343	-	θ2	1°	3°	5°	1°	3°	5°
E2	9.80	10.00	10.20	0.386	0.394	0.401							

Customer Service

Sales and Service:

sales@vgsemi.com

Shen Zhen Vanguard Semiconductor CO., LTD

TEL: (86-755) -26902410

FAX: (86-755) -26907027

WEB: www.vgsemi.com