

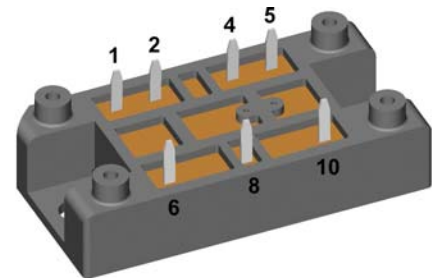
Standard Rectifier Module

3~ Rectifier	
V_{RRM}	= 1200 V
I_{DAV}	= 30 A
I_{FSM}	= 150 A

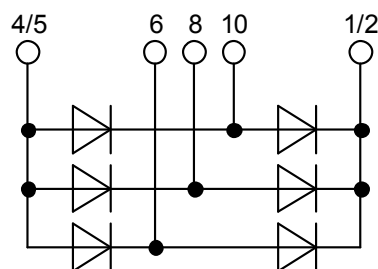
3~ Rectifier Bridge

Part number

VUO22-12NO1



 E72873



Features / Advantages:

- Package with DCB ceramic
- Reduced weight
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- Very low leakage current

Applications:

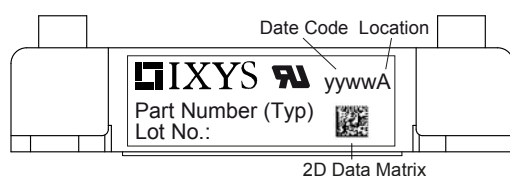
- Diode for main rectification
- For three phase bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- Field supply for DC motors

Package: V1-A-Pack

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Height: 17 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Rectifier				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V_{RSM}	max. non-repetitive reverse blocking voltage	$T_{VJ} = 25^{\circ}\text{C}$				1300	V
V_{RRM}	max. repetitive reverse blocking voltage	$T_{VJ} = 25^{\circ}\text{C}$				1200	V
I_R	reverse current	$V_R = 1200\text{ V}$	$T_{VJ} = 25^{\circ}\text{C}$			10	μA
		$V_R = 1200\text{ V}$	$T_{VJ} = 150^{\circ}\text{C}$			0.7	mA
V_F	forward voltage drop	$I_F = 10\text{ A}$	$T_{VJ} = 25^{\circ}\text{C}$			1.19	V
		$I_F = 30\text{ A}$				1.59	V
		$I_F = 10\text{ A}$	$T_{VJ} = 125^{\circ}\text{C}$			1.13	V
		$I_F = 30\text{ A}$				1.66	V
I_{DAV}	bridge output current	$T_C = 110^{\circ}\text{C}$ rectangular $d = \frac{1}{3}$	$T_{VJ} = 150^{\circ}\text{C}$			30	A
V_{FO}	threshold voltage	} for power loss calculation only		$T_{VJ} = 150^{\circ}\text{C}$		0.84	V
r_F	slope resistance					28	m Ω
R_{thJC}	thermal resistance junction to case					2.5	K/W
R_{thCH}	thermal resistance case to heatsink				0.4		K/W
P_{tot}	total power dissipation	$T_C = 25^{\circ}\text{C}$				50	W
I_{FSM}	max. forward surge current	$t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$	$T_{VJ} = 45^{\circ}\text{C}$			150	A
		$t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$	$V_R = 0\text{ V}$			160	A
		$t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$	$T_{VJ} = 150^{\circ}\text{C}$			130	A
		$t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$	$V_R = 0\text{ V}$			140	A
I^2t	value for fusing	$t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$	$T_{VJ} = 45^{\circ}\text{C}$			115	A ² s
		$t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$	$V_R = 0\text{ V}$			105	A ² s
		$t = 10\text{ ms}; (50\text{ Hz}), \text{ sine}$	$T_{VJ} = 150^{\circ}\text{C}$			85	A ² s
		$t = 8,3\text{ ms}; (60\text{ Hz}), \text{ sine}$	$V_R = 0\text{ V}$			82	A ² s
C_J	junction capacitance	$V_R = 400\text{ V}; f = 1\text{ MHz}$		$T_{VJ} = 25^{\circ}\text{C}$		4	pF

Package V1-A-Pack			Ratings			
Symbol	Definition	Conditions	min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal			100	A
T _{stg}	storage temperature		-40		125	°C
T _{VJ}	virtual junction temperature		-40		150	°C
Weight				37		g
M _D	mounting torque		2		2.5	Nm
d _{Spp/App}	creepage distance on surface striking distance through air		6.0			mm
d _{Spb/Apb}	terminal to backside		12.0			mm
V _{ISOL}	isolation voltage	t = 1 second	3600			V
		t = 1 minute	3000			V
			50/60 Hz, RMS; I _{ISOL} ≤ 1 mA			

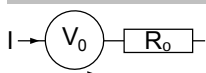


Ordering	Part Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	VUO22-12NO1	VUO22-12NO1	Box	10	461083

Equivalent Circuits for Simulation

* on die level

$T_{VJ} = 150^\circ\text{C}$



Rectifier

$V_{0\max}$	threshold voltage	0.84	V
$R_{0\max}$	slope resistance *	27	mΩ

Technical drawing of a mechanical part, showing three views: front view, top view, and side view. The drawing includes dimensions and a title block.

Front View:

- Overall width: 63
- Overall height: 17 ± 0,25
- Top surface radius: R2
- Central slot width: 35
- Side slot width: 13
- Bottom surface thickness: 0,25

Top View:

- Overall width: 50 ± 0,2
- Overall height: 23,5 ± 0,1
- Top surface radius: R1
- Central slot width: 38,6
- Side slot width: 14 ± 0,3
- Bottom surface thickness: 0,5
- Bottom surface radius: R2
- Bottom surface chamfer: 4x45°
- Bottom surface slot width: 7 ± 0,3
- Bottom surface slot depth: 11 ± 0,3
- Bottom surface slot width: 11 ± 0,3
- Bottom surface slot depth: 5,5
- Bottom surface slot width: 25,75 ± 0,15
- Bottom surface slot depth: 51,5 ± 0,3

Side View:

- Overall width: 31,6
- Overall height: 3,3 ± 0,5
- Top surface radius: R2
- Central slot width: 26
- Side slot width: 2
- Bottom surface thickness: 0,25

Detail "X" (M 2:1):

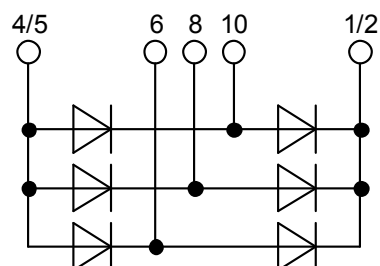
- Overall width: 6,1
- Overall height: 1,5
- Top surface radius: R1
- Central slot width: 2,5
- Side slot width: 1,5
- Bottom surface thickness: 0,5
- Bottom surface radius: R2
- Bottom surface chamfer: 4x45°
- Bottom surface slot width: 7 ± 0,3
- Bottom surface slot depth: 11 ± 0,3
- Bottom surface slot width: 11 ± 0,3
- Bottom surface slot depth: 5,5
- Bottom surface slot width: 25,75 ± 0,15
- Bottom surface slot depth: 51,5 ± 0,3

Detail "Y" (M 5:1):

- Overall width: 1,5
- Overall height: 1 ± 0,2
- Top surface radius: R1
- Central slot width: 0,5 ± 0,3
- Side slot width: 1,5
- Bottom surface thickness: 0,5
- Bottom surface radius: R2
- Bottom surface chamfer: 4x45°
- Bottom surface slot width: 7 ± 0,3
- Bottom surface slot depth: 11 ± 0,3
- Bottom surface slot width: 11 ± 0,3
- Bottom surface slot depth: 5,5
- Bottom surface slot width: 25,75 ± 0,15
- Bottom surface slot depth: 51,5 ± 0,3

Title Block:

Aufdruck der Typenbezeichnung



Rectifier

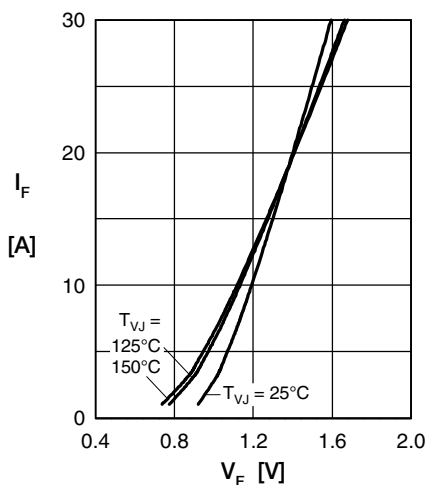


Fig. 1 Forward current vs. voltage drop per diode

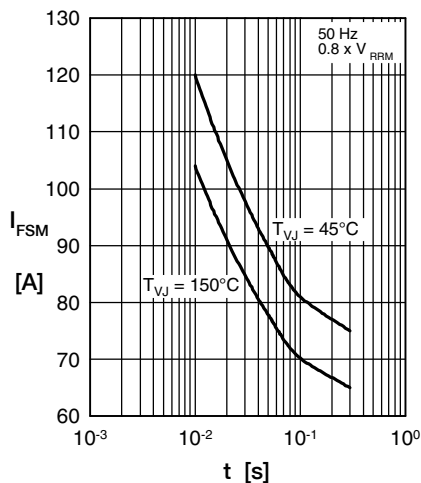


Fig. 2 Surge overload current vs. time per diode

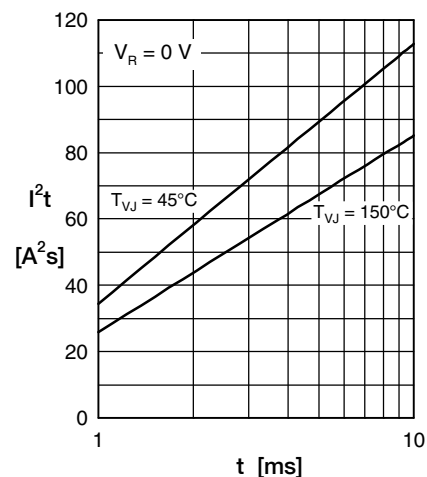


Fig. 3 I^2t vs. time per diode

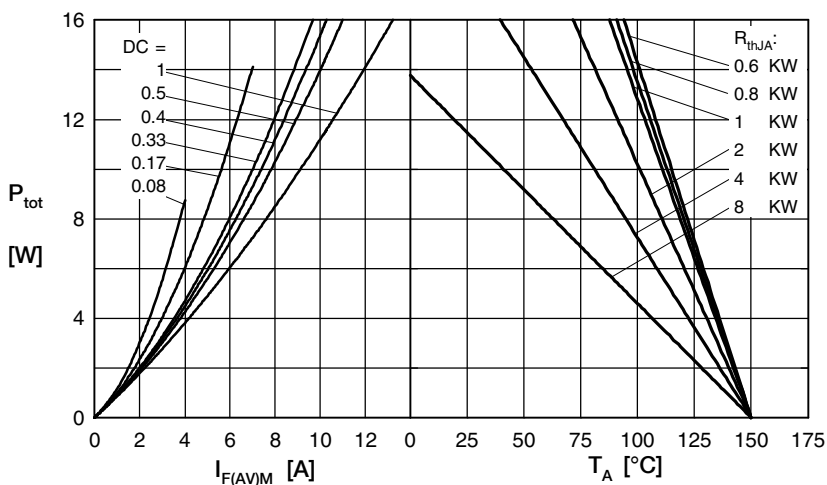


Fig. 4 Power dissipation vs. forward current and ambient temperature per diode

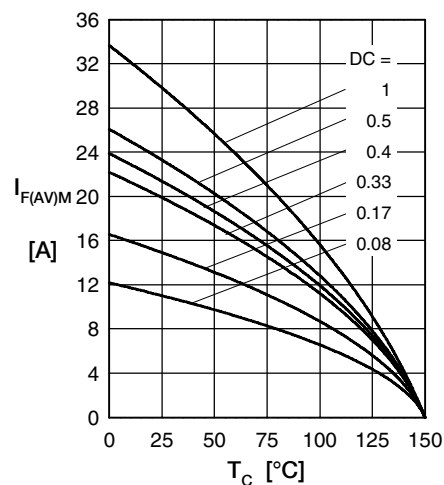


Fig. 5 Max. forward current vs. case temperature per diode

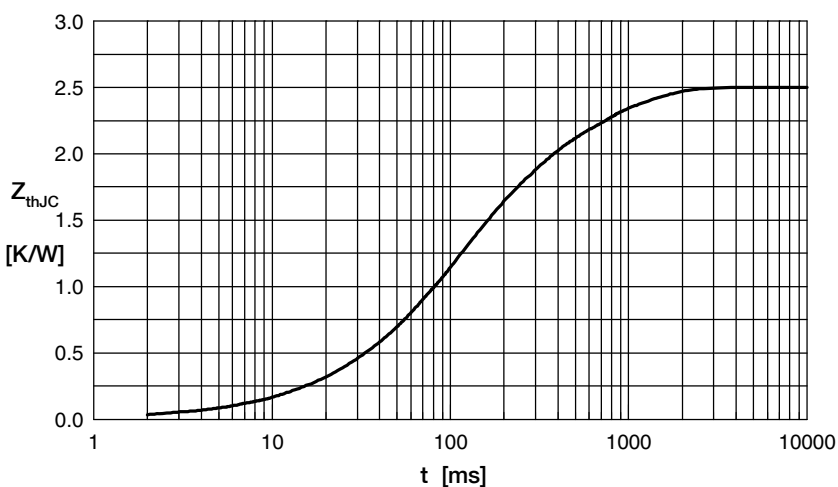


Fig. 6 Transient thermal impedance junction to case vs. time per diode

Constants for Z_{thJC} calculation:

i	R_{th} (K/W)	t_i (s)
1	1.300	0.1015
2	0.300	0.1026
3	0.350	0.4919
4	0.550	0.6200