

# WCR380N65T/WCR380N65TF/ WCR380N65TH 650V N-Channel Super Junction MOSFET

## Description

The WCR380N65T series is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. This device is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

## Features

- 700V@T<sub>J</sub>=150°C
- Typ.R<sub>DS(on)</sub>=0.33Ω
- Low gate charge(typ. Qg= 32nC)
- 100% avalanche tested
- 100% R<sub>g</sub> tested

## Order Information

Device	Package	Marking	Units/Tube	Units/Real
WCR380N65T-3/T	TO-220	WCR380N65TYYWW	50	
WCR380N65TF-3/T	TO-220F	WCR380N65TFYYWW	50	
WCR380N65TH-3/TR	TO-263E-2	WCR380N65THYW		800

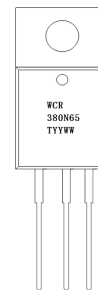
Note 1: WCR380N65T=Device code ; YY=Year ;WW=Week (A~z);

Note 2: WCR380N65TF=Device code ; YY=Year ;WW=Week (A~z);

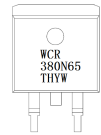
Note 3: WCR380N65TH=Device code ; Y=Year ;W=Week (A~z);



TO-220



TO-220F



TO-263E-2

## Absolution Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Parameter	Symbol	WCR380N65T WCR380N65TH	WCR380N65TF	Unit	
Drain-Source Voltage	V <sub>DS</sub>	650		V	
Gate-Source Voltage	V <sub>GS</sub>	±30			
Continuous Drain Current <sup>A</sup>	I <sub>D</sub>	T <sub>C</sub> =25°C	10.6	A	
		T <sub>C</sub> =100°C	6.7		
Pulsed Drain Current <sup>B</sup>	I <sub>DM</sub>	30		A	
Single Pulsed Avalanche Energy <sup>C</sup>	E <sub>AS</sub>	215		mJ	
Avalanche Current <sup>B</sup>	I <sub>AR</sub>	2.7		A	
Repetitive Avalanche Energy <sup>B</sup>	E <sub>AR</sub>	0.30		mJ	
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C	83	31.2	W
		Derate above 25°C	0.66	0.25	W/°C
Operating and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55~150		°C	
Lead Temperature	T <sub>L</sub>	260		°C	

## Thermal Resistance Ratings

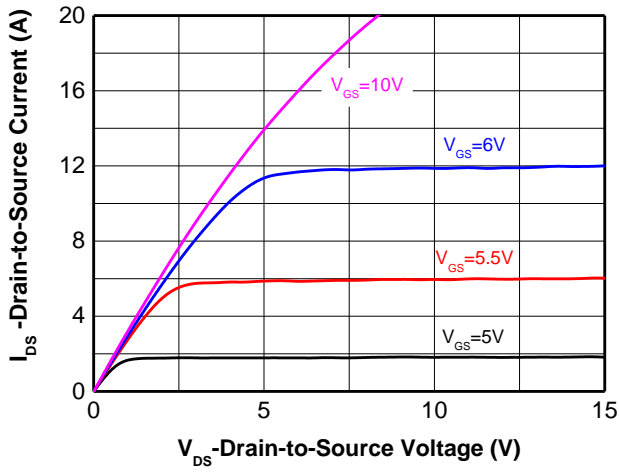
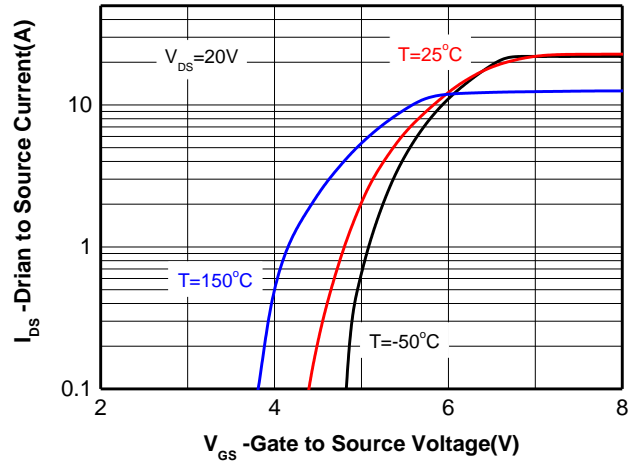
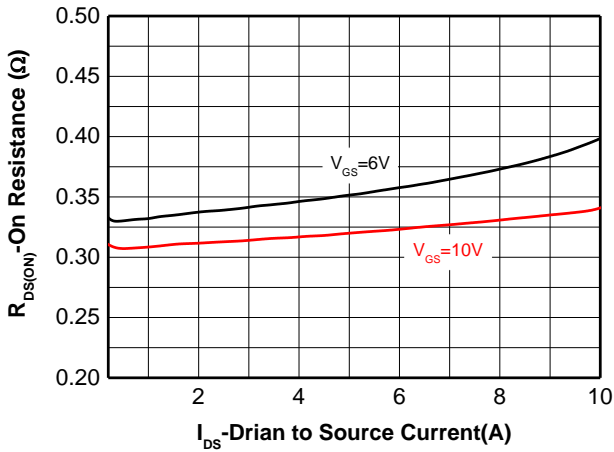
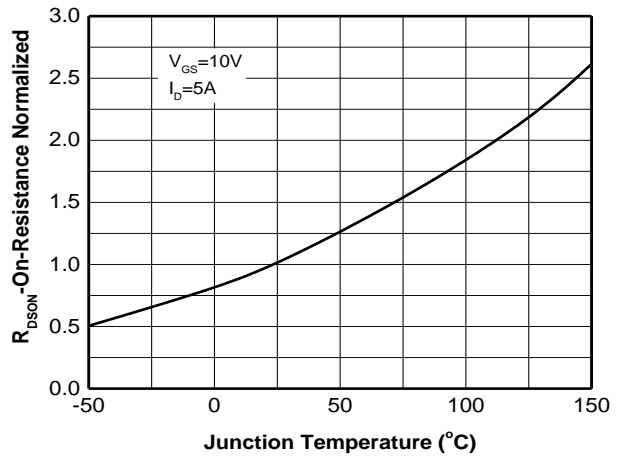
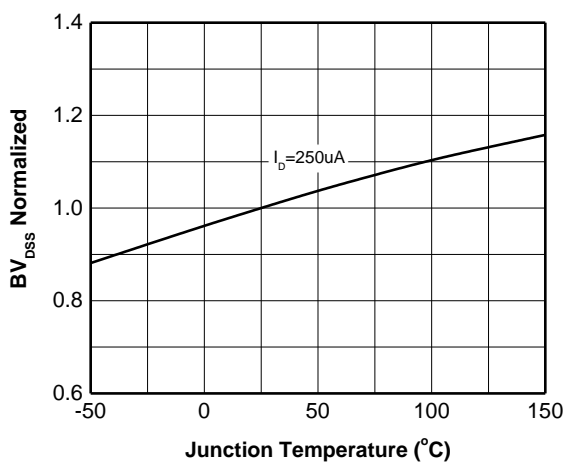
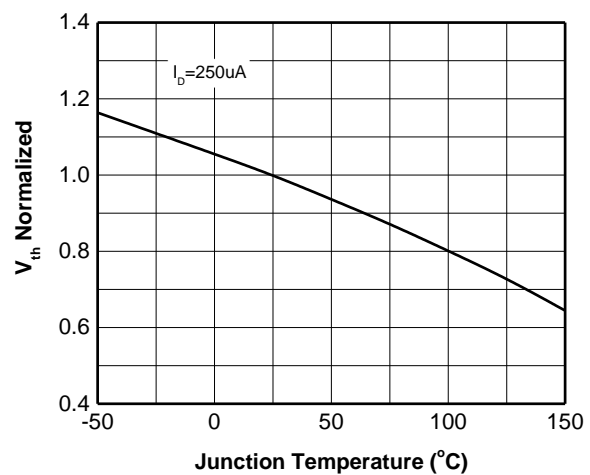
Maximum Junction-to-Ambient	R <sub>θJA</sub>	60	80	°C/W
Maximum Junction-to-Case	R <sub>θJC</sub>	1.5	4	

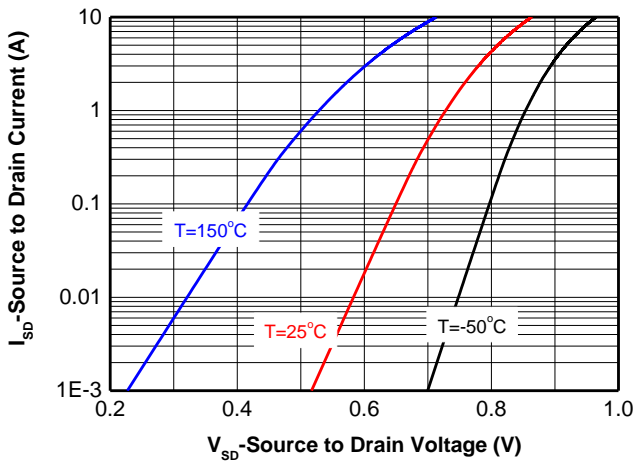
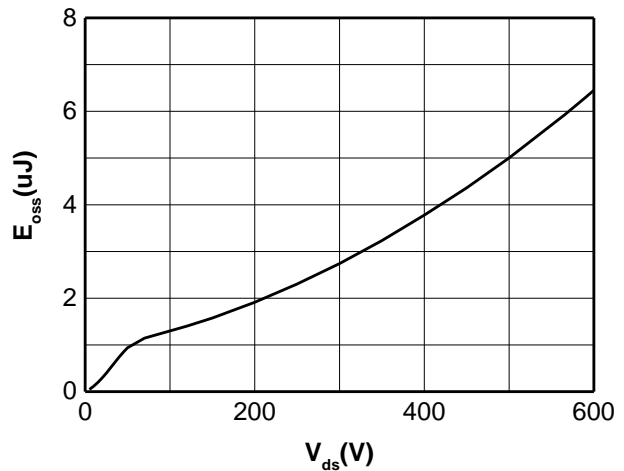
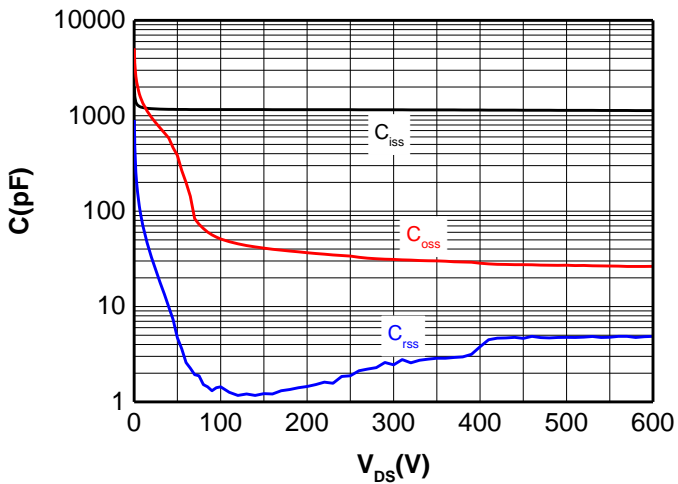
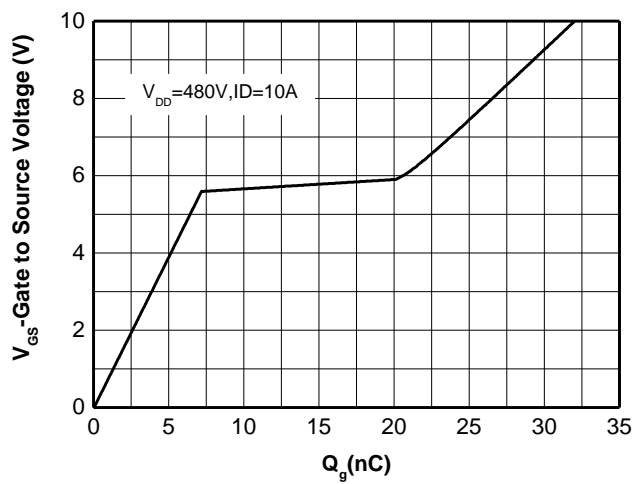
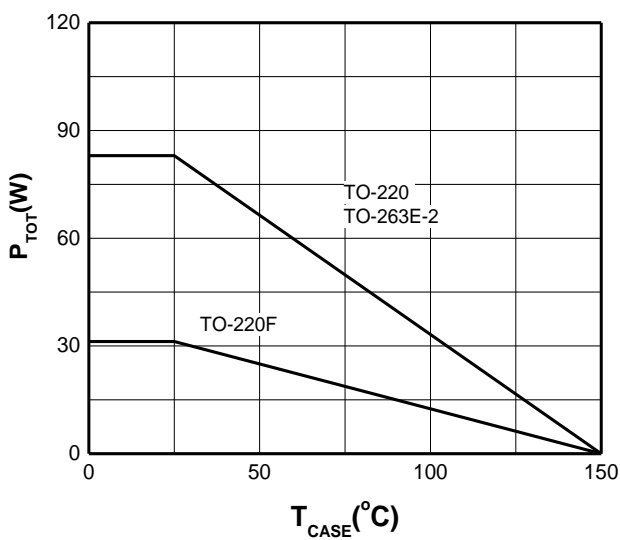
**Electronics Characteristics (T<sub>A</sub>=25°C, unless otherwise noted)**

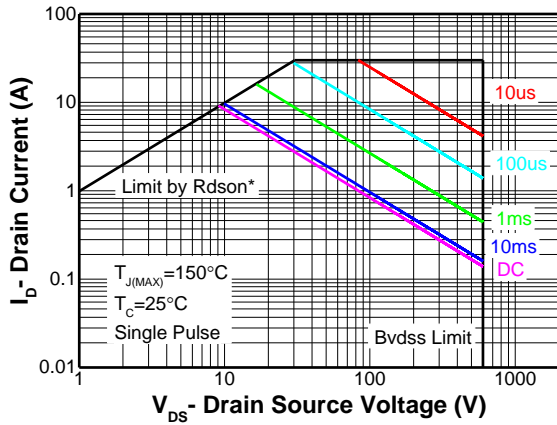
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250uA, T <sub>J</sub> =25°C	650			V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250uA, T <sub>J</sub> =150°C		700		V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C			1	uA
Gate-to-source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 30 V			± 100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250uA	2.5		4.5	V
Drain-to-source On-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A (NOTE D)		0.33	0.38	Ω
Forward Transconductance	G <sub>fs</sub>	V <sub>DS</sub> = 40V, I <sub>D</sub> = 5A			20	s
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> = 0 V, f = 1.0 MHz, V <sub>DS</sub> = 400 V		1140		pF
Output Capacitance	C <sub>OSS</sub>			30		
Reverse Transfer Capacitance	C <sub>RSS</sub>			4		
Total Gate Charge	Q <sub>G(TOT)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 480 V, I <sub>D</sub> = 10A		32		nC
Gate-to-Source Charge	Q <sub>GS</sub>			7.3		
Gate-to-Drain Charge	Q <sub>GD</sub>			14.7		
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHZ		7.4		Ω
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 400 V, I <sub>D</sub> = 11A, R <sub>G</sub> =20 Ω		21		ns
Rise Time	t <sub>r</sub>			38		
Turn-Off Delay Time	t <sub>d(off)</sub>			84		
Fall Time	t <sub>f</sub>			27		
<b>Drain to Source Diode Characteristics and Maximum Ratings</b>						
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 9.5A			1.5	V
Body-Diode Continuous Current	I <sub>S</sub>				10	A
Body-Diode Pulsed Current	I <sub>SM</sub>				30	A
Body Diode Reverse Recovery Time	T <sub>rr</sub>	I <sub>F</sub> =9.5A, dI/dt=100A/us, V <sub>DS</sub> =100V		347		nS
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			4		uC
Peak reverse recovery Current	I <sub>rrm</sub>			23		A

**NOTES:**

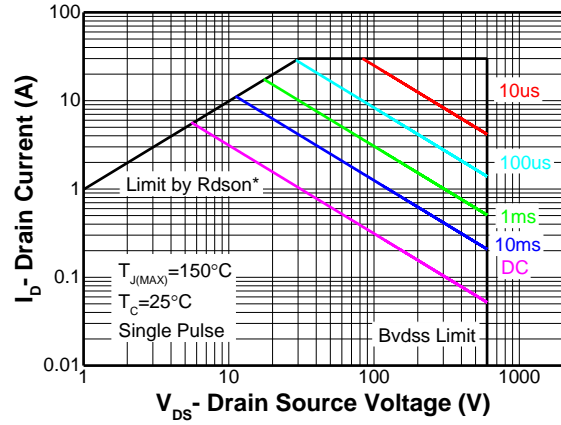
- Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75
- Pulse width limited by maximum junction temperature
- L=60mH, I<sub>AS</sub>=2.7A, V<sub>DD</sub>=50V, Starting T<sub>J</sub>=25°C
- Pulse Test: Pulse width ≤ 300us, Duty Cycle ≤ 2%
- Essentially Independent of Operating Temperature Typical Characteristics
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heat sink, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=150°C. The SOA curve provides a single pulse rating.
- These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C.

**Typical Characteristics ( $T_A=25^\circ\text{C}$ , unless otherwise noted)**

**Output characteristics**

**Transfer characteristics**

**On-Resistance vs. Drain current**

**On-Resistance vs. Junction temperature**

**Breakdown Voltage vs. Junction temperature**

**Threshold voltage vs. Junction temperature**

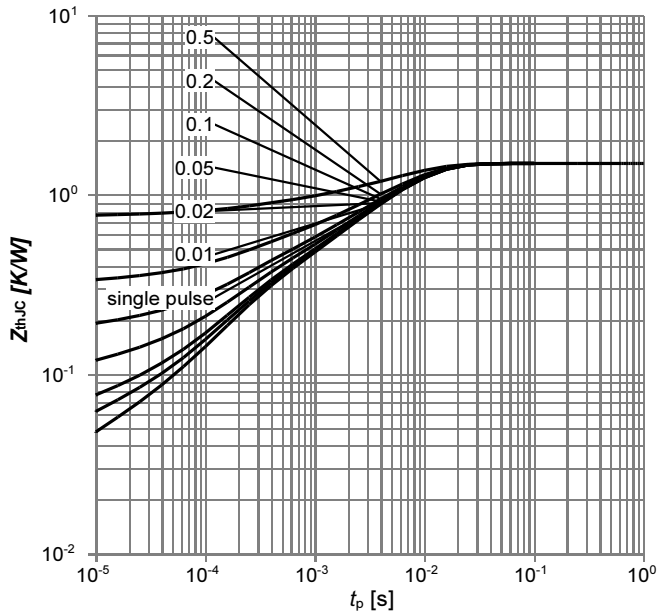

**Body diode forward voltage**

**Coss stored Energy**

**Capacitance**

**Gate charge Characteristics**

**Power dissipation**



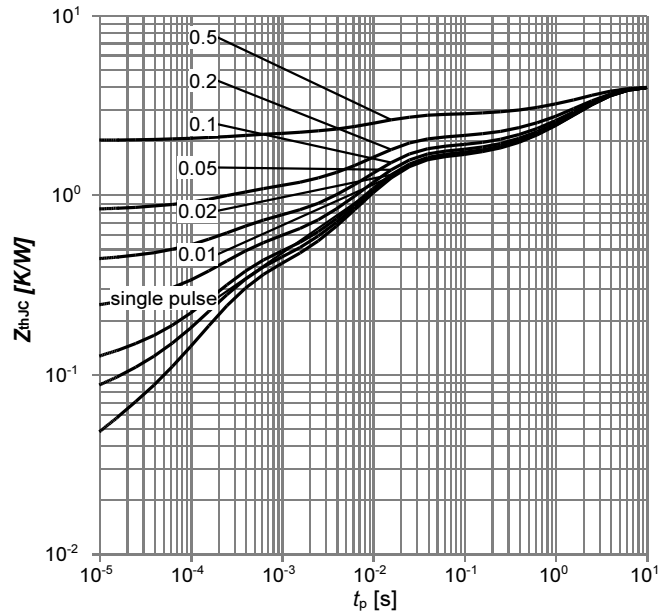
**TO-220 TO-263E-2**  
Safe operating area(Note F)



**TO-220F**  
Safe operating area(Note F)

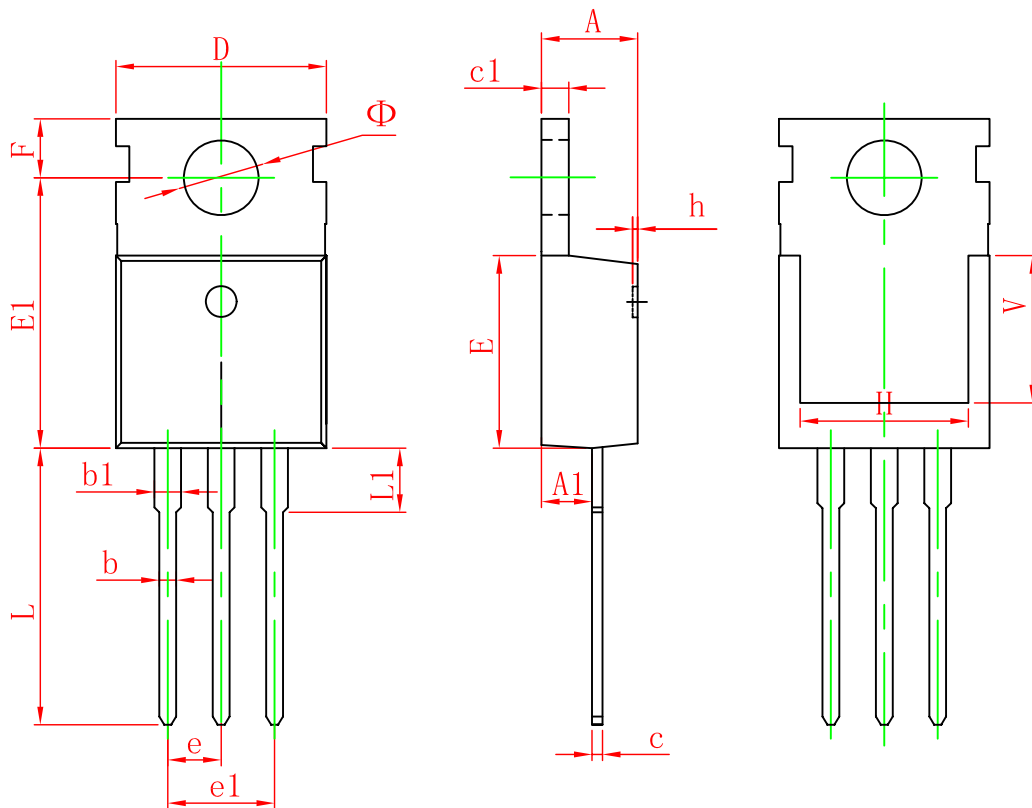


**TO-220 TO-263E-2**

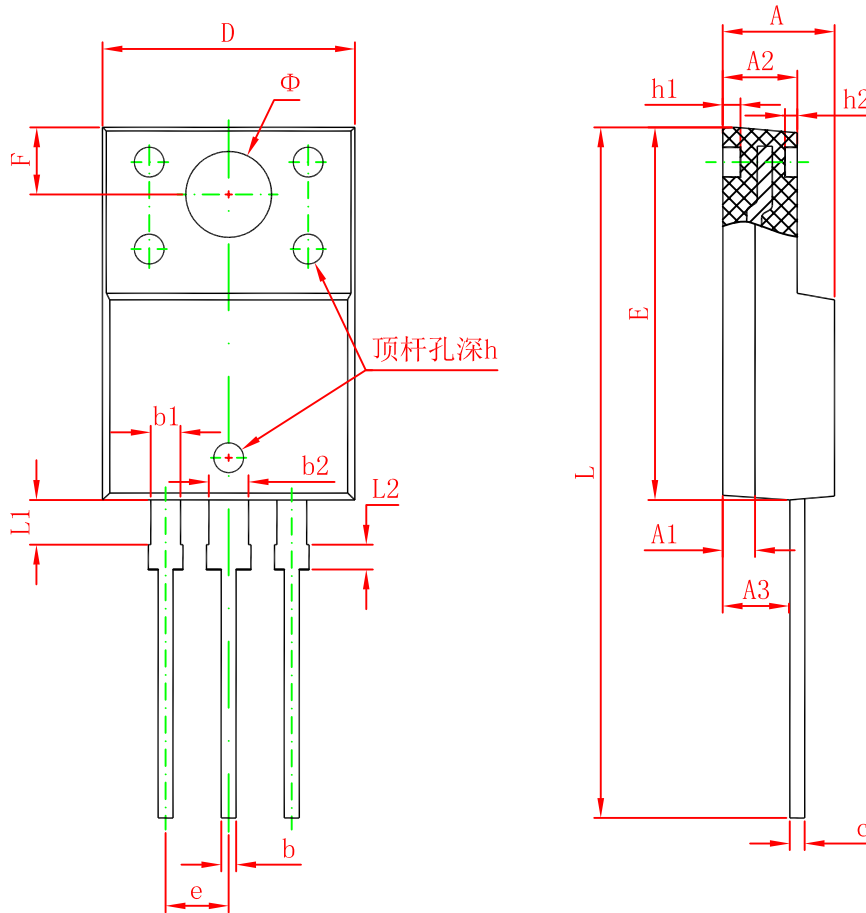


**TO-220F**

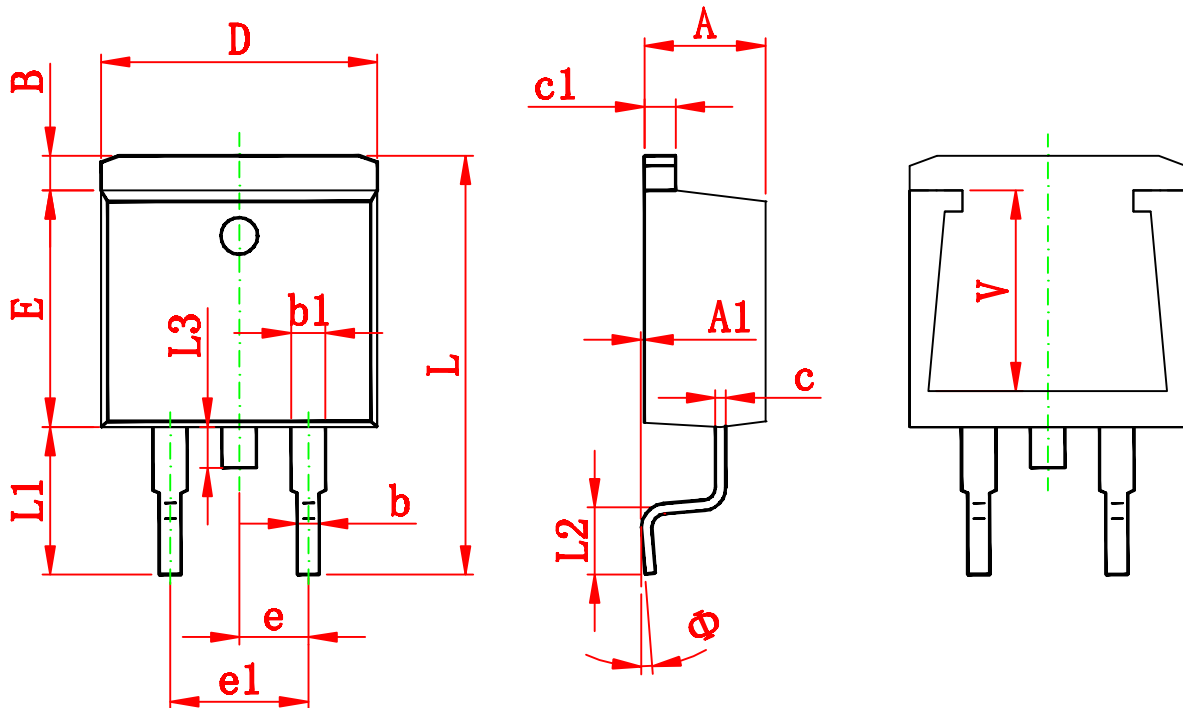
**Transient thermal response (Junction-to-Case)(Note F)**

**Package outline dimensions**
**TO-220-3L**


Symbol	Dimensions in Millimeters	
	Min.	Max.
A	4.40	4.60
A1	2.25	2.55
b	0.71	0.91
b1	1.17	1.37
c	0.33	0.65
c1	1.20	1.40
D	9.91	10.25
E	8.95	9.75
E1	12.65	12.95
e	2.54 Typ.	
e1	4.98	5.18
F	2.65	2.95
H	7.90	8.10
h	0.00	0.30
L	12.90	13.40
L1	2.85	3.25
V	6.90 Ref.	
$\Phi$	3.40	3.80

**Package outline dimensions**
**TO-220F-3L**


Symbol	Dimensions in Millimeters	
	Min.	Max.
A	4.30	4.70
A1	1.30 Ref.	
A2	2.80	3.20
A3	2.50	2.90
b	0.50	0.75
b1	1.10	1.35
b2	1.50	1.75
c	0.50	0.75
D	9.96	10.36
E	14.80	15.20
e	2.54 Typ.	
F	2.70 Ref.	
$\Phi$	3.50 Ref.	
h	0.00	0.30
h1	0.80 Ref.	
h2	0.50 Ref.	
L	28.00	28.40
L1	1.70	1.90
L2	0.90	1.10

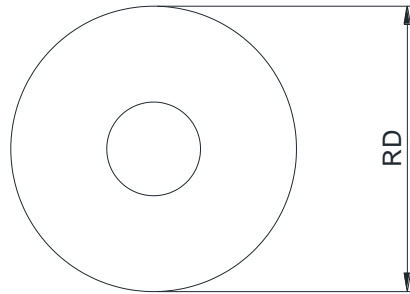
**Package outline dimensions**
**TO-263E-2L**


Symbol	Dimensions in Millimeters	
	Min.	Max.
A	4.47	4.67
A1	0.00	0.15
B	1.12	1.42
b	0.71	0.91
b1	1.17	1.37
c	0.31	0.53
c1	1.17	1.37
D	10.01	10.31
E	8.50	8.90
e	2.54 Typ.	
e1	4.98	5.18
L	14.94	15.50
L1	4.95	5.45
L2	2.34	2.74
L3	1.30	1.70
Φ	0 °	8 °
V	6.60 Ref.	

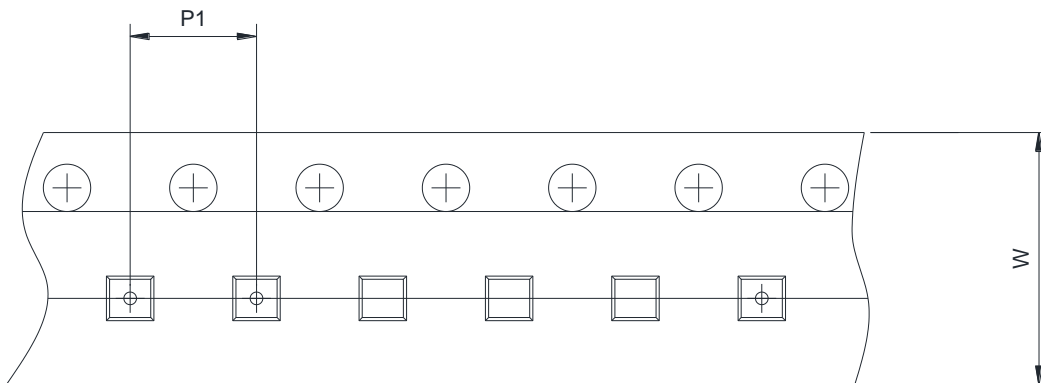


**TAPE AND REEL INFORMATION**
**TO-263E-2L**

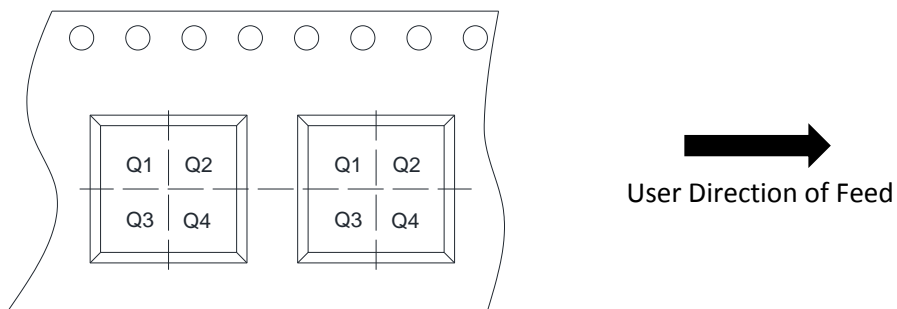
Reel Dimensions



Tape Dimensions



Quadrant Assignments For PIN1 Orientation In Tape



RD	Reel Dimension	<input type="checkbox"/> 7 inch <input checked="" type="checkbox"/> 13 inch
P1	Pitch between successive cavity centers	<input type="checkbox"/> 4 mm <input type="checkbox"/> 8 mm <input checked="" type="checkbox"/> 16 mm
W	Overall width of the carrier tape	<input type="checkbox"/> 8 mm <input type="checkbox"/> 12 mm <input checked="" type="checkbox"/> 24 mm
Pin1	Pin1 Quadrant	<input type="checkbox"/> Q1 <input type="checkbox"/> Q2 <input type="checkbox"/> Q3 <input type="checkbox"/> Q4