

WCR650N60T/ WCR650N60TF/ WCR650N60TG/ WCR650N60TN
600V N-Channel Super Junction MOSFET
Description

The WCR600N60T series is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. This device is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

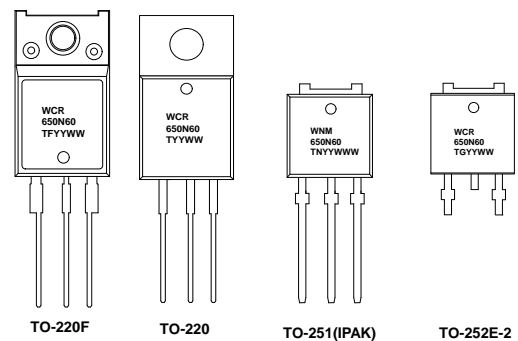
Features

- 650V @ $T_J=150^{\circ}\text{C}$
- Typ. $R_{DS(on)}=0.57\Omega$
- Low gate charge (typ. $Q_g=9.6\text{nC}$)
- 100% avalanche tested
- 100% R_g tested

Order Information

Device	Package	Marking	Units/Tube
WCR650N60T-3/T	TO-220	WCR650N60TYYWW	50
WCR650N60TF-3/T	TO-220-F	WCR650N60TFYYWW	50
WCR650N60TN-3/T	TO-251(IPAK)	WCR650N60TGYWW	70
WCR650N60TG-3/T	TO-252E-2	WCR650N60TNYWW	70

Note 1: WCR650N60T=Device code ; YY=Year ; WW=Week (A-z);
 Note 2: WCR650N60TF=Device code ; YY=Year ; WW=Week (A-z);
 Note 3: WCR650N60TG=Device code ; YY=Year ; WW=Week (A-z);
 Note 4: WCR650N60TN=Device code ; YY=Year ; WW=Week (A-z);


Absolution Maximum Ratings $T_A=25^{\circ}\text{C}$ unless otherwise noted

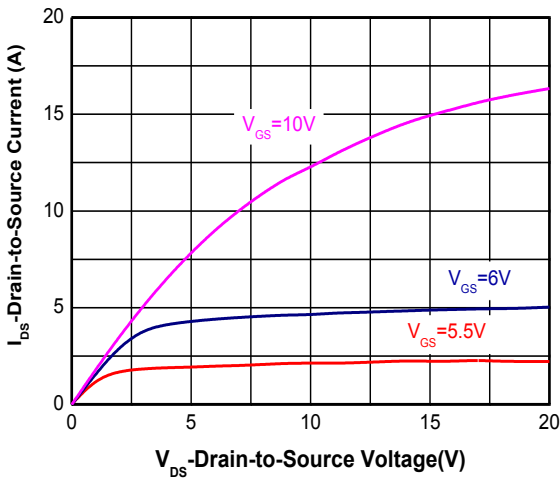
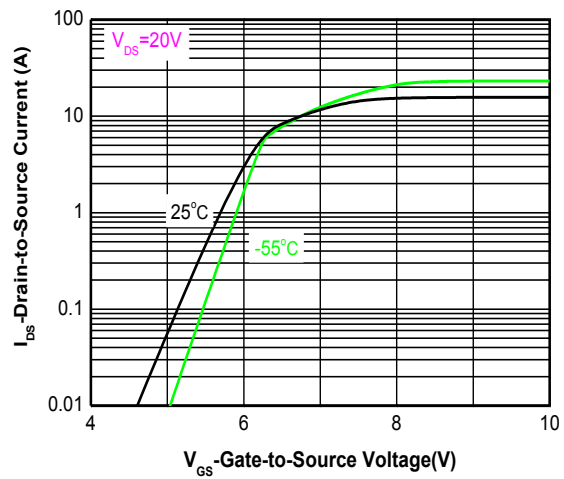
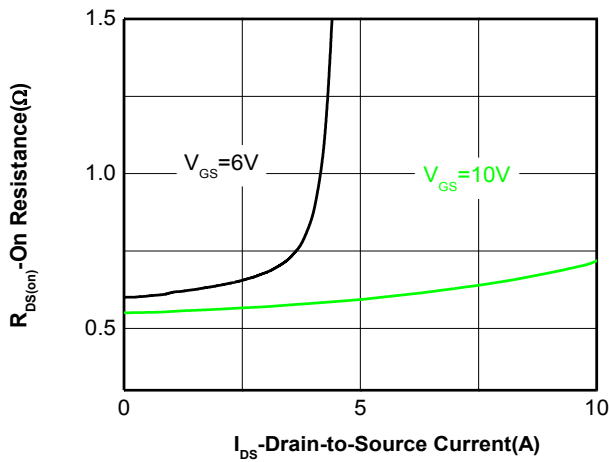
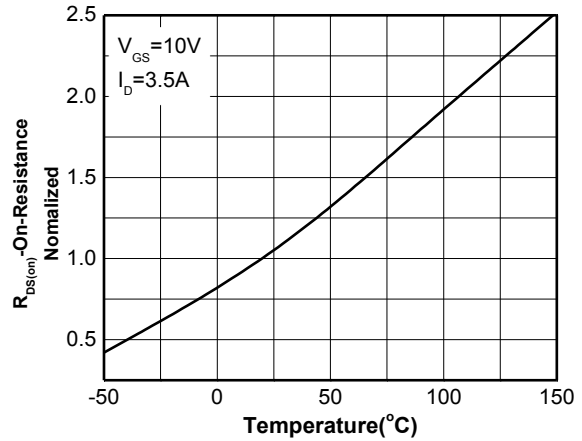
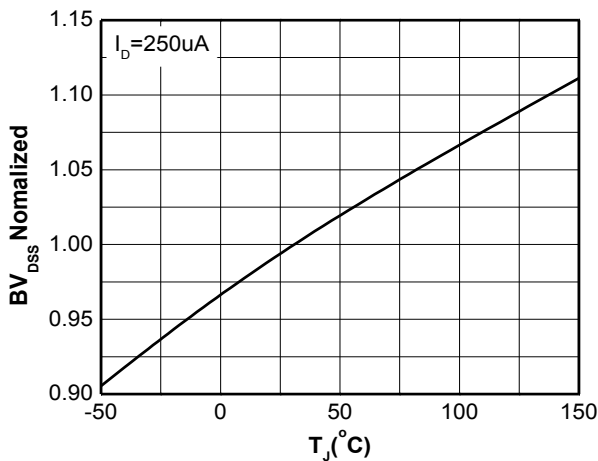
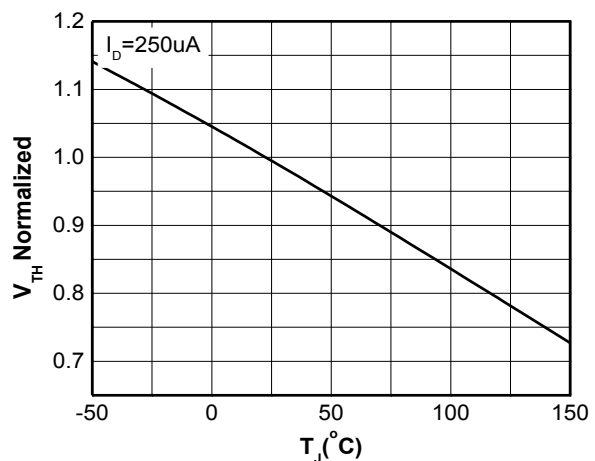
Parameter	Symbol	WCR650N60T WCR650N60TN WCR650N60TG	WCR650N60TF	Unit	
Drain-Source Voltage	V_{DS}	600		V	
Gate-Source Voltage	V_{GS}	± 30			
Continuous Drain Current ^A	$T_C=25^{\circ}\text{C}$	I_D	7.3	A	
	$T_C=100^{\circ}\text{C}$		4.6		
Pulsed Drain Current ^B	I_{DM}		16	A	
Single Pulsed Avalanche Energy ^C	E_{AS}		86	mJ	
Avalanche Current ^B	I_{AR}		1.7	A	
Repetitive Avalanche Energy ^B	E_{AR}		0.2	mJ	
Power Dissipation	$T_C=25^{\circ}\text{C}$	P_D	62.5	27.7	W
	Derate above 25°C		0.5	0.22	W/ $^{\circ}\text{C}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55~150		$^{\circ}\text{C}$	
Lead Temperature	T_L	260		$^{\circ}\text{C}$	
Thermal Resistance Ratings					
Maximum Junction-to-Ambient	$R_{\theta JA}$	60	80	$^{\circ}\text{C/W}$	
Maximum Junction-to-Case	$R_{\theta JC}$	2	4.5		

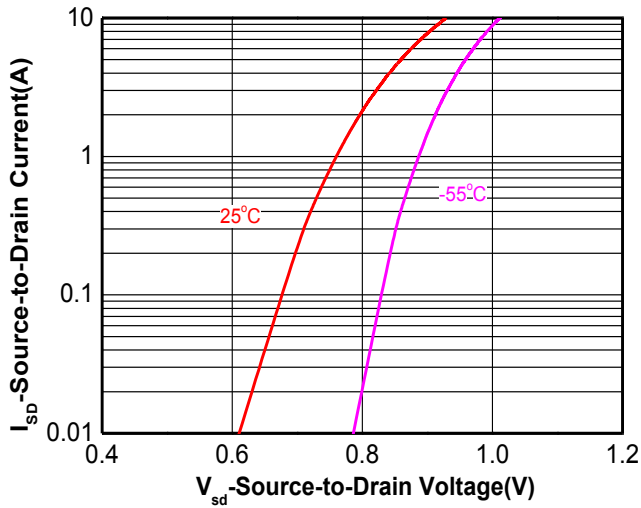
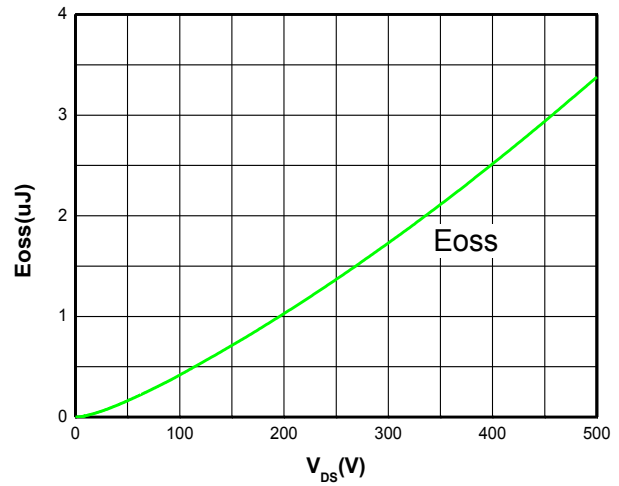
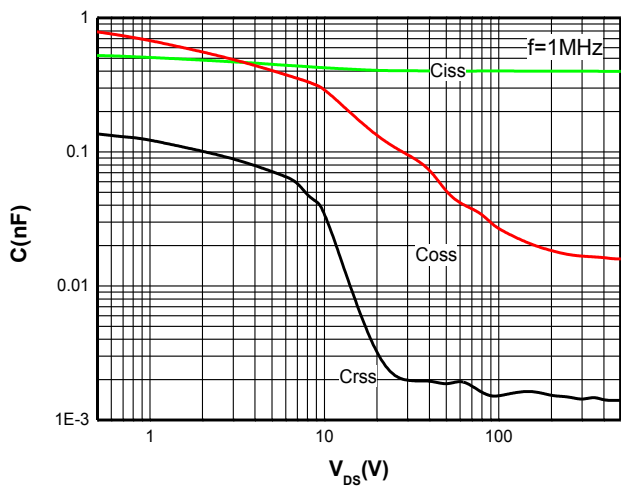
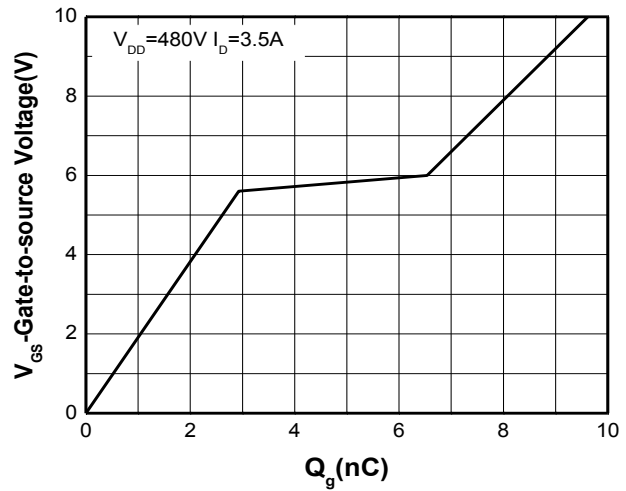
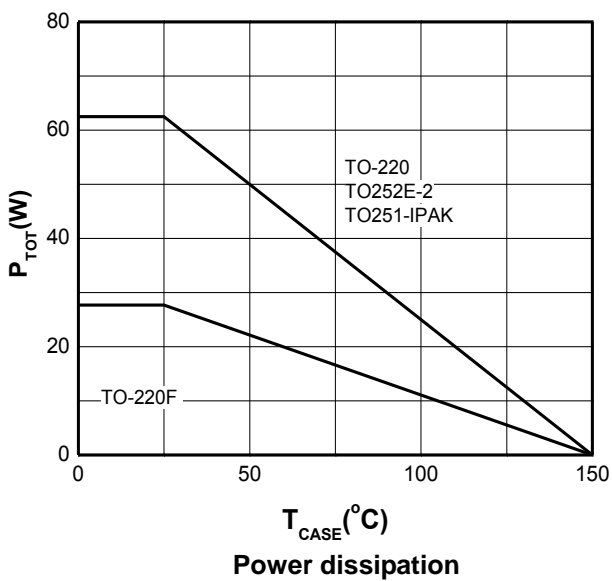
Electronics Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

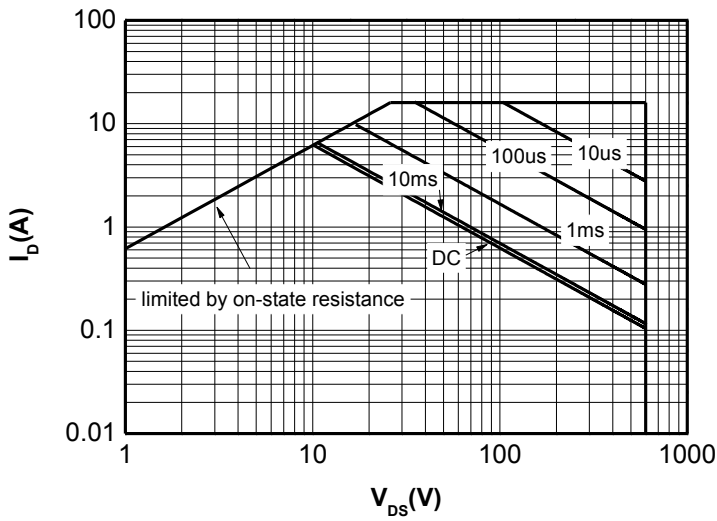
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0\text{ V}, I_D = 250\mu\text{A}, T_J=25^\circ\text{C}$	600			V
		$V_{GS} = 0\text{ V}, I_D = 250\mu\text{A}, T_J=150^\circ\text{C}$		650		V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V}, T_J=25^\circ\text{C}$			1	μA
		$V_{DS} = 480\text{ V}, V_{GS} = 0\text{ V}, T_J=125^\circ\text{C}$			10	μA
Gate-to-source Leakage Current	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 30\text{ V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	2.5		4.5	V
Drain-to-source On-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 3.5\text{ A}$		0.55	0.65	Ω
Forward Transconductance	g_{FS}	$V_{DS} = 40\text{ V}, I_D = 3.5\text{ A}$ (NOTE D)			20	S
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}, V_{DS} = 25\text{ V}$		403		pF
Output Capacitance	C_{OSS}			96		
Reverse Transfer Capacitance	C_{RSS}			21		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 10\text{ V},$ $V_{DS} = 480\text{ V}, I_D = 3.5\text{ A}$ (NOTE D, E)		9.6		nC
Gate-to-Source Charge	Q_{GS}			2.9		
Gate-to-Drain Charge	Q_{GD}			3.6		
Gate resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, F=1\text{MHZ}$		5		Ω
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{ V},$ $V_{DS} = 400\text{ V},$ $I_D = 3.5\text{ A}, R_G=20\ \Omega$ (NOTE D, E)		17.1		ns
Rise Time	t_r			15.9		
Turn-Off Delay Time	$t_{d(off)}$			33.2		
Fall Time	t_f			11.2		
Drain to Source Diode Characteristics and Maximum Ratings						
Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 7.0\text{ A}$			1.5	V
Body-Diode Continuous Current	I_S				7.3	A
Body-Diode Pulsed Current	I_{SM}				16	A
Body Diode Reverse Recovery Time	T_{rr}	$I_F=7\text{A}, di/dt=100\text{A}/\mu\text{s}, V_{DS}=100\text{V}$ (NOTE D)		416		nS
Body Diode Reverse Recovery Charge	Q_{rr}			2.58		μC
Peak reverse recovery current	I_{rr}			12.4		A

NOTES:

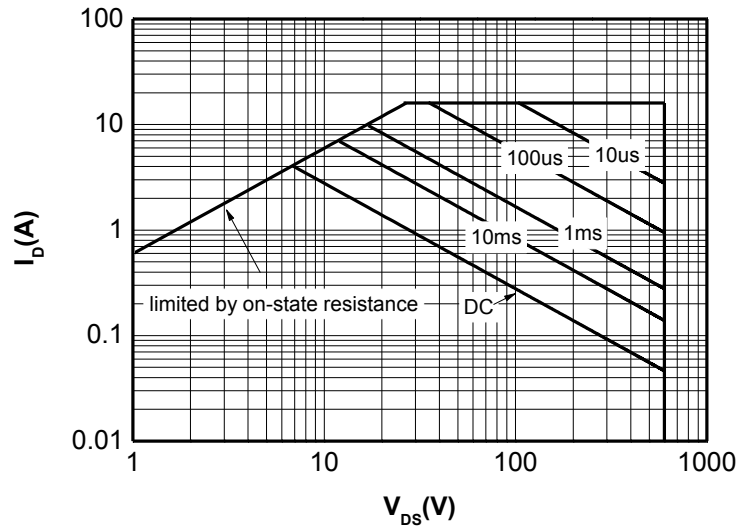
- Drain current limited by maximum junction temperature. Maximum duty cycle $D=0.75$
- Pulse width limited by maximum junction temperature
- $L=60\text{mH}, I_{AS}=1.7\text{A}, V_{DD}=150\text{V}$, Starting $T_J=25^\circ\text{C}$
- Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
- Essentially Independent of Operating Temperature Typical Characteristics
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heat sink, assuming a maximum junction temperature of $T_{J(MAX)}=150^\circ\text{C}$. The SOA curve provides a single pulse rating.

Typical Characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Output characteristics

Transfer characteristics

On-Resistance vs. Drain current

On-Resistance vs. Junction temperature

Breakdown Voltage vs. Junction temperature

Threshold voltage vs. Junction temperature

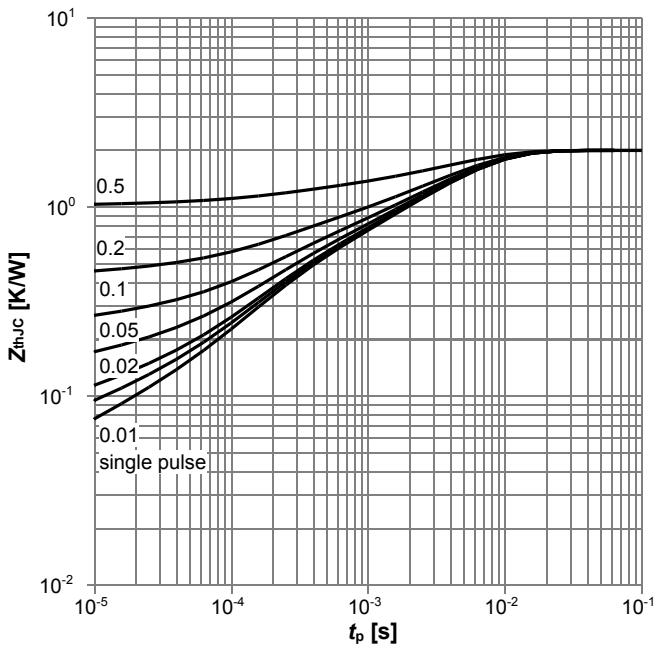

Body diode forward voltage

Coss stored Energy

Capacitance

Gate charge Characteristics

Power dissipation



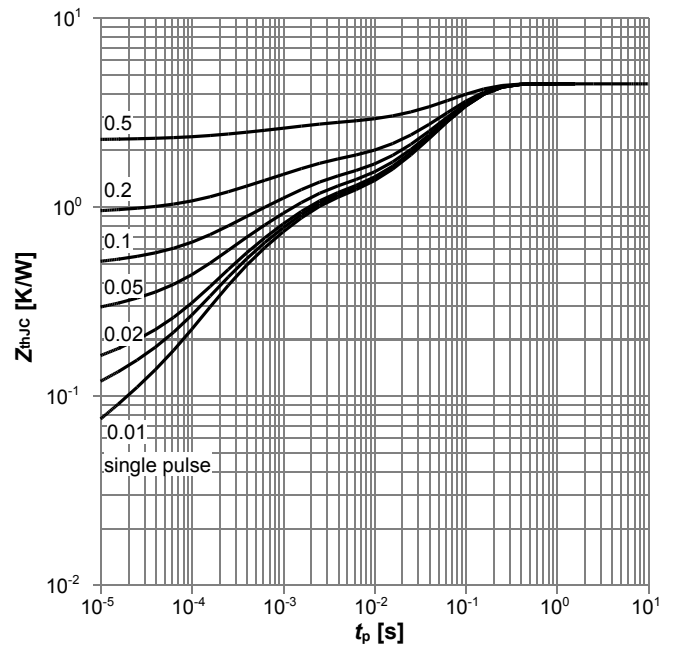
TO-220 TO-252E-2 TO-251(IPAK)
Safe Operating Area(Note F)



TO-220F
Safe Operating Area(Note F)



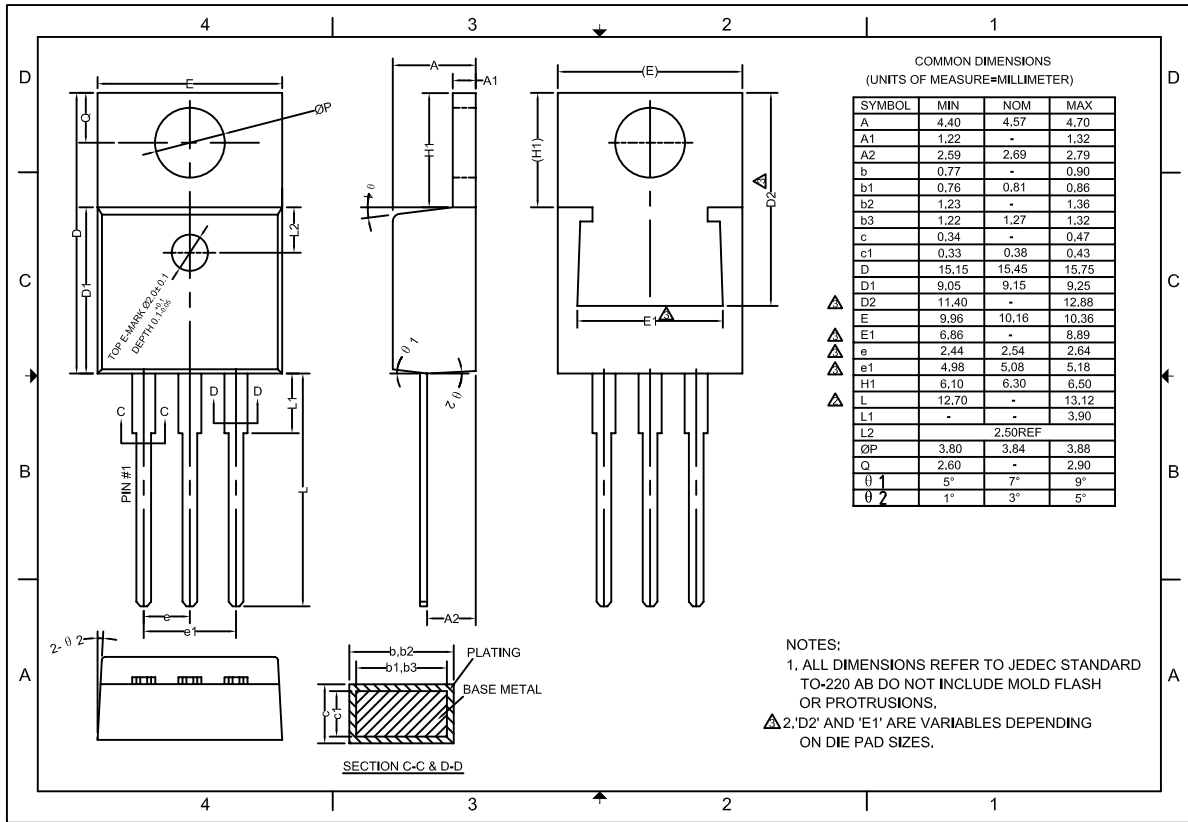
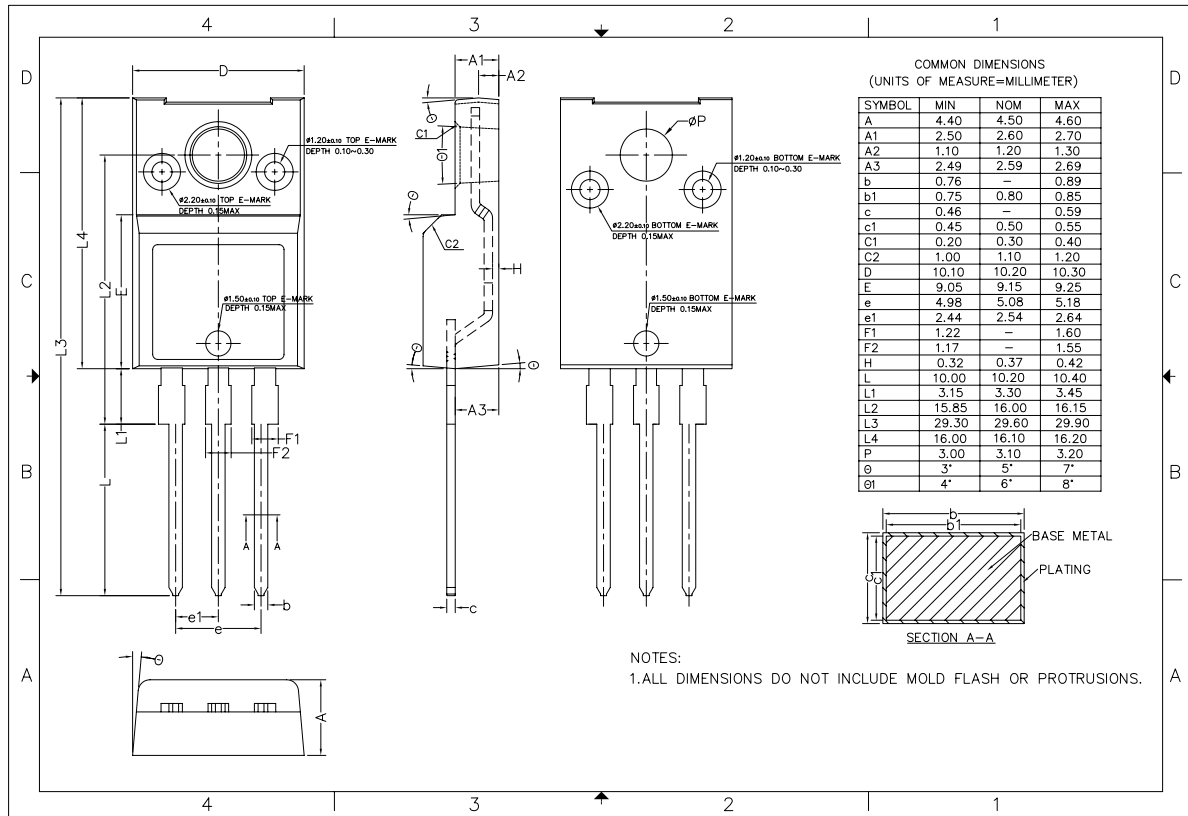
TO-220 TO-252E-2 TO-251(IPAK)

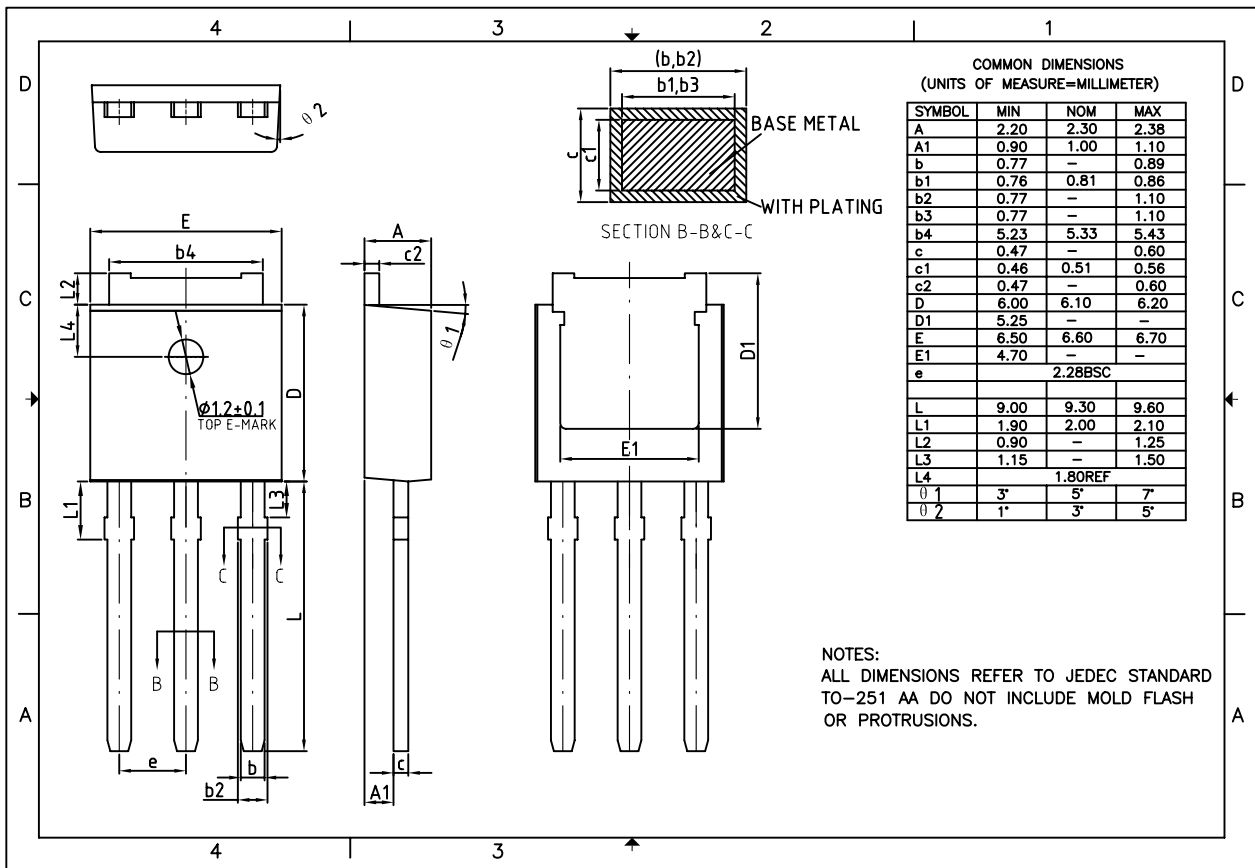


TO-220F

Transient thermal response (Junction-to-Case)(Note F)

Package outline dimensions

TO-220

TO-220F


TO-251E(IPAK)

TO-252E-2
