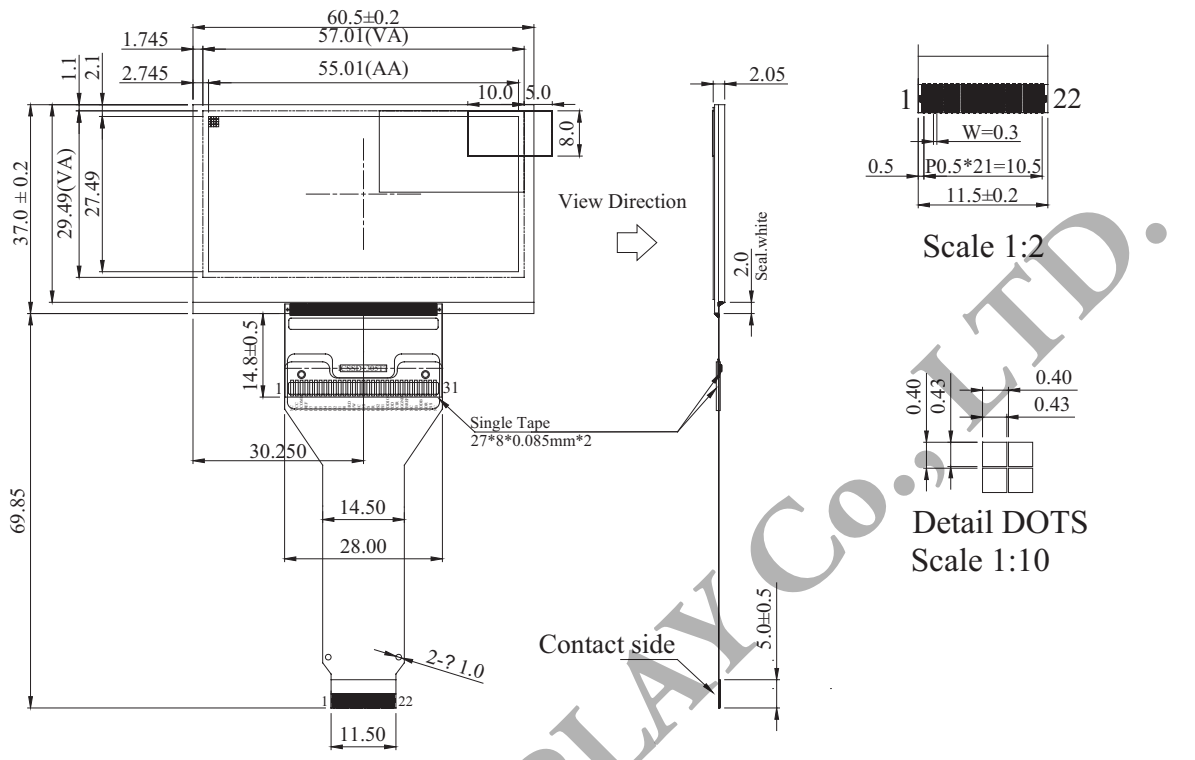
WEG012864M OLED Graphic 128x64 dots

Dimension drawing



OLED Graphic type

Feature

1. 128x64 dots
2. Built-in Controller SSD1305T7R1
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 6800, 8080, SPI, I2C
6. Polarizer optional

No.	Symbol	Description
1	VCC	Power supply for analog circuit.
2	VCOMH	Com Voltage Output. A capacitor should be connected between this pin and VSS.
3	IREF	Reference current input pin. A resistor should be connected between this pin and VSS.
4~11	D7~D0	Data bus.
12	E/RD#	Data read operation is initiated when it's pull low.
13	R/W#	Data write operation is initiated when it's pull low.
14	D/C#	Data/ Command control. Pull high for write/read display data. Pull low for write command or read status.
15	RES#	Reset signal input. When it's low, initialization of SSD1305 is executed.
16	CS#	Chip select input.
17	BS2	Communicating Protocol Select These pins are MCU interface selection input. See the following table:
18	BS1	

	68XX-parallel	80XX-parallel	Serial
BS1	0	1	0
BS2	1	1	0

19	VDD	Power supply for logic circuit.
20	NC	No connection.
21	VSS	Ground.
22	VSS	Ground.

Mechanical Date

Item	Dimension	Unit
Module dimension	60.5 × 37.0 × 2.05	mm
View area	57.01 × 29.49	mm
Active area	55.01 × 27.49	mm
Dot Size	0.4 × 0.4	mm
Dot Pitch	0.43 × 0.43	mm

Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	V_{DD}	-0.3	3.5	V	1, 2
Supply Voltage for Display	V_{CC}	8	16	V	1, 2

Electronical Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Max
Supply Voltage for Logic	V_{DD}		2.4	2.7	3.5	V
Supply Voltage for Display	V_{CC}		14.5	15	15.5	V
High Level Input	V_{IH}	I_{OUT}= 100μA, 3.3MHz	0.8×VDD	–	VDD	V
Low Level Input	V_{IL}	I_{OUT}= 100μA, 3.3MHz	0	–	0.2×VDD	V
High Level Output	V_{OH}	I_{OUT}= 100μA, 3.3MHz	0.9×VDD	–	VDD	V
Low Level Input	V_{OL}	I_{OUT}= 100μA, 3.3MHz	0	–	0.1×VDD	V
Operating Current for V_{DD}	I_{DD}	Note 4	–	250	400	μA
		Note 5	–	250	400	μA
Operating Current for V_{CC}	I_{CC}	Note 4	–	31	39	mA
		Note 5	–	53	66	ma
Sleep Mode Current for V_{DD}	$I_{DD, SLEEP}$		–	–	10	μA
Sleep Mode Current for V_{CC}	$I_{CC, SLEEP}$		–	–	10	μA

Note 3: Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer's request.
Note 4: VDD = 2.7V, VCC = 15V, 50% Display Area Turn on.
Note 5: VDD = 2.7V, VCC = 15V, 100% Display Area Turn on