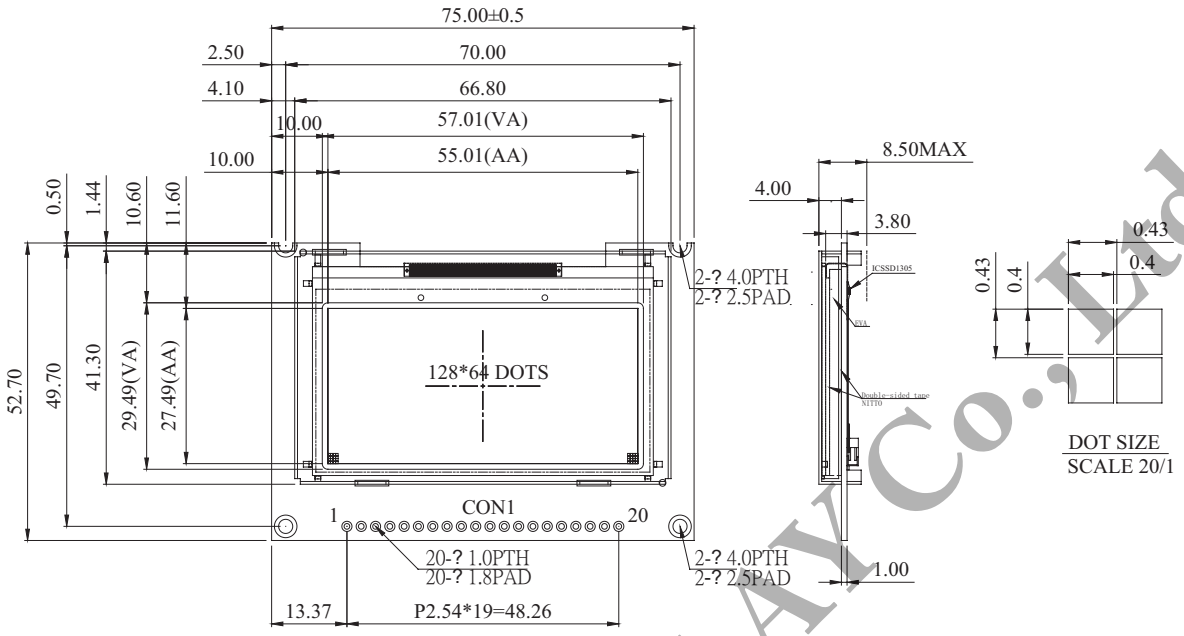
Dimension drawing



Feature

1. 128 x 64 dots
2. Built-in Controller SSD1305T7R1
3. +3V power supply
4. 1/64 duty cycle
5. Interface: 8080, option 6800 & SPI & I2C
6. Sunlight readable & polarizer optional

Pin No.	Symbol	Function
1	VDD	Power supply for analog circuit.
2	VSS	Ground.
3	NC	No connection
4~11	D0~D7	Data bus.
12	CS#	Chip select input.
13	NC	No connection
14	RES#	Reset signal input. When it's low, initialization of SSD1305 is executed.
15	R/W#	Data write operation is initiated when it's pull low.
16	D/C#	Data/ Command control. Pull high for write/read display data. Pull low for write command or read status.
17	E/RD#	Data read operation is initiated when it's pull low.
18	NC	No connection
19	DISP	Display off
20	NC	No connection

Mechanical Date

Item	Dimension	Unit
Module dimension	75.0 × 52.7 × 8.5	mm
View Area	57.01 × 29.49	mm
Active Area	55.01 × 27.49	mm
Mounting hole	70.00 × 49.70	mm
Dot Size	0.4 × 0.4	mm
Dot Pitch	0.43 × 0.43	mm

Absolute Maximum Rating

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	3.5	V	1, 2
Supply Voltage for Display	VCC	8	16	V	1, 2

Electronical Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	V_{DD}	–	2.4	2.7	3.5	V
Supply Voltage for Display	V_{CC}	–	11.0	13.0	15.0	V
High Level Input	V_{IH}	I_{OUT}= 100μA, 3.3MHz	0.8xVDD	–	VDD	V
Low Level Input	V_{IL}	I_{OUT}= 100μA, 3.3MHz	0	–	0.2×VDD	V
High Level Output	V_{OH}	I_{OUT}= 100μA, 3.3MHz	0.9×VDD	–	VDD	V
Low Level Output	V_{OL}	I_{OUT}= 100μA, 3.3MHz	0	–	0.1×VDD	V
Operating Current for V_{DD}	I_{DD}	Note 4	–	45	–	mA
Sleep Mode Current for V_{DD}	$I_{DD, SLEEP}$		–	–	1.5	mA

Note 3: Brightness (L_{br}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.
Note 4: V_{DD} = 2.7V, V_{CC} = 13V, 50% Display Area Turn on.